# Proof-producing decompilation and compilation

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This talk concerns verification of functional correctness of machine code for commercial processors (ARM, PowerPC, x86...).

Outline of talk:

- motivation for decompilation into logic
- implementing decompilation
- compilation

- direct reasoning about *next*-state function.
- annotating code with assertions:

```
xor eax, eax
L1: test esi, esi
jz L2
inc eax
mov esi, [esi]
jmp L1
L2:
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xor eax, eax mov r0, #0
{...}
L1: test esi, esi L: cmp r1, #0
jz L2 ldrne r1, [r1]
{...}
addne r0, r0, #1
inc eax bne L
mov esi, [esi]
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L2: {...}
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- annotating code with assertions:

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{...}
                            {???}
    xor eax, eax
                            mov r0, #0
    {...}
                            {???}
L1: test esi, esi
                   L: cmp r1, #0
    jz L2
                            ldrne r1, [r1]
    {...}
                            addne r0, r0, #1
    inc eax
                            bne L
                            {???}
    mov esi, [esi]
    jmp L1
L2: {...}
                          Proof reuse?
```

### Our approach

Decompilation produces the following tail-recursive functions describing the effect of the code, f for x86 and f' for ARM:

```
f(eax, esi, m) =
let eax = eax \otimes eax in
g(eax, esi, m)
g(eax, esi, m) =
if esi \& esi = 0
then (eax, esi, m) else
let eax = eax + 1 in
let esi = m(esi) in
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```
f'(r_0, r_1, m) = \\ let r_0 = 0 in \\ g'(r_0, r_1, m) \\ g'(r_0, r_1, m) = \\ if r_1 = 0 \\ then (r_0, r_1, m) else \\ let r_1 = m(r_1) in \\ let r_0 = r_0 + 1 in \\ g'(r_0, r_1, m) \\ \end{cases}
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g(eax, esi, m)	$g'(r_0,r_1,m)$
g(eax, esi, m) =	$g'(r_0,r_1,m) =$
<b>if</b> <i>esi</i> & <i>esi</i> = 0	if $r_1 = 0$
then (eax, esi, m) else	then $(r_0, r_1, m)$ else
let $eax = eax + 1$ in	let $r_1 = m(r_1)$ in
let $esi = m(esi)$ in	let $r_0 = r_0 + 1$ in
g(eax, esi, m)	$g'(r_0, r_1, m)$

Advantages: 1. no need for knowledge of the next-state function;

- 2. suitable for proofs in HOL, and
- 3. proof reuse, f = f' using w & w = w and  $w \otimes w = 0$ .

### Produced theorem

How does f relate to the x86 code?

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{ (eax, esi, m) is (eax, esi, m) \* eip  $p * f_{pre}(eax, esi, m)$  } p : 31C085F67405408B36EBF7{ (eax, esi, m) is f(eax, esi, m) \* eip (p+11) }

Here **eax**, **esi**, **m** and **eip** (program counter) assert values of resources and '(x, y, z) is (a, b, c)' abbreviates  $(x \ a) * (y \ b) * (z \ c)$ .

The decompiler automates machine specific proofs and leaves the user (verifier) to prove properties of the generated function f.

Suppose we have proved

$$\forall xs \ w \ a \ m. \ list(xs, a, m) \ \Rightarrow \ f(w, a, m) = (length(xs), 0, m)$$
$$\forall xs \ w \ a \ m. \ list(xs, a, m) \ \Rightarrow \ f_{pre}(w, a, m)$$

for an appropriate definition of list.

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#### implementing decompilation

compilation

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  - b) generates a function describing effect of code;
  - c) for loops, instantiates special loop rule.
- 4. composes the top-level specifications and repeats step 3 until all of the code is described by one specification.

# Proving loops

Approach: assume existence of termination proof, use induction from termination proof to prove loop.

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We define pre(x) to state that there exists and invariant which guarantees that k terminates when applied to x. Here  $\prec$  is some well-founded relation.

$$pre(x) = \\ \exists inv. inv(x) \land \\ \exists \prec. \forall y. inv(y) \land G(y) \Rightarrow inv(F(y)) \land F(y) \prec y$$

### Proving loops (continued)

The loop rule, used by the decompiler, for function

$$k(x) = \text{if } G(x) \text{ then } k(F(x)) \text{ else } D(x)$$

is the following: for any resource assertions res and res',

$$(\forall x. \quad G(x) \Rightarrow \{ \operatorname{res} x \} c \{ \operatorname{res} F(x) \} ) \land \\ (\forall x. \neg G(x) \Rightarrow \{ \operatorname{res} x \} c \{ \operatorname{res}' D(x) \} ) \\ \Rightarrow (\forall x. \{ \operatorname{res} x * pre(x) \} c \{ \operatorname{res}' k(x) \} )$$

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In our x86 example the loop uses assertions:

res 
$$x = (eax, esi, m)$$
 is  $x * eip p$   
res'  $x = (eax, esi, m)$  is  $x * eip (p+9)$ 

# Proving loops (continued)

The loop rule is derived from the following induction, provable from the definition of *pre* and well-founded relation:

$$\forall \varphi. \quad (\forall x. \ G(x) \land \varphi(F(x)) \Rightarrow \varphi(x)) \land \\ (\forall x. \ \neg G(x) \Rightarrow \varphi(x)) \\ \Rightarrow (\forall x. \ pre(x) \Rightarrow \varphi(x))$$

The proof of the loop rule uses the following composition:

$$\{\operatorname{res} x\} c \{\operatorname{res} F(x)\} \land \{\operatorname{res} F(x)\} c \{\operatorname{res'} k(x)\}$$
  
$$\Rightarrow \{\operatorname{res} x\} c \cup c \{\operatorname{res'} k(x)\}$$
  
$$\Rightarrow \{\operatorname{res} x\} c \{\operatorname{res'} k(x)\}$$

### Decompilation

For most part proof-producing decompilation is just:

- 1. deriving specifications for individual instructions;
- 2. composing them; and
- 3. instantiating a loop rule.

Failure prone heuristic are largely avoided.

This method does not support advanced control-flow, *e.g.* computed jumps and code pointers.

However, it does support non-nested loops, procedure calls and, in an awkward way, procedural recursion.

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# Proof-producing compilation

For a simple compiler, given a function f:

- generate code;
- ▶ run decompiler to get *f*′;
- automatically prove f = f'.

Works well, even for functions f which are hundreds of lines long.

# Proof-producing compilation

For a simple compiler, given a function f:

- generate code;
- run decompiler to get f';
- automatically prove f = f'.

Works well, even for functions f which are hundreds of lines long.

Each expression in f must implementable in the target language, e.g. "let eax = eax + 1 in" and "if eax < 400 then ... else ..."

However, we can do better...

# Proof-producing compilation (continued)

Suppose we have a specification for allocation on a garbage collected heap h, which allows allocation of a new element if the size of the current heap h does not exceed the limit I.

{heap 
$$(v_1, v_2, v_3, v_4, h, l) * eip p * size(h) < l}$$
  
...code...

 $\{\texttt{heap}\;(\textit{fresh}(h), v_2, v_3, v_4, \textit{h}[\textit{fresh}(h) \mapsto (v_1, v_2)], \textit{l}) * \texttt{eip}\;(\textit{p}+416)\}$ 

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Such specifications can be fed into the automation so that the compiler can handle:

"let  $(v_1, h) = (fresh(h), h[fresh(h) \mapsto (v_1, v_2)])$  in"

The side-condition size(h) < l is recorded in the precondition of the theorem from the decompiler.

# Conclusions

Decompilation and compilation are based on:

- (a) modelling loops as tail-recursion, and
- (b) proving (a) correct using termination proofs.

Details described in paper available at: www.cl.cam.ac.uk/mom22

Acknowledgments: I would like to thank Mike Gordon, Konrad Slind, Thomas Tuerk, Anthony Fox, Susmit Sarkar, Peter Sewell, Boris Feigin, Max Bolingbroke, John Regehr, Lu Zhao and Matthew Parkinson for discussions.

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#### My questions for you:

- How has the correspondence between loops and tail-recursion been formally proved before?
- Have termination proofs been used for this?

#### Questions?

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