

# Computer Design

## Supervision 3

**Supervisor:** Joe Isaacs (josi2).

All work should be submitted in PDF form 36 hours before the supervision to the email [josi2@cam.ac.uk](mailto:josi2@cam.ac.uk). If you have any questions on the course please include these at the top of the supervision work and we can talk about them in the supervision.

Some work will be taken from the exercises here: <http://www.cl.cam.ac.uk/teaching/1718/CompDesign/cam-only/Exercises-parts1and2.pdf>

There is some duplication of exams questions set, you do not need to answer the same question part multiple times.

1. <http://www.cl.cam.ac.uk/teaching/exams/pastpapers/y2011p5q3.pdf>
2. Describe the 5-stage MIPS pipeline
3. What control hazards apply to the MIPS pipeline and how are they resolved?
4. What data hazards apply to the MIPS pipeline and how are they resolved?
5. <http://www.cl.cam.ac.uk/teaching/exams/pastpapers/y2007p6q2.pdf>
6. <http://www.cl.cam.ac.uk/teaching/exams/pastpapers/y2014p5q2.pdf> (part c)