

# Computer Design

## Supervision 2

**Supervisor:** Joe Isaacs (josi2).

All work should be submitted in PDF form 36 hours before the supervision to the email [josi2@cl.cam.ac.uk](mailto:josi2@cl.cam.ac.uk)

Some work will be taken from the exercises here: <http://www.cl.cam.ac.uk/teaching/1718/CompDesign/cam-only/Exercises-parts1and2.pdf>

1. Lecture 5, from exercises.
2. How many bits of constant can be specified in one RISC V instruction?
3. Lecture 8.
4. What is the significance of some MIPS registers being preserved in some circumstances and others not?
5. What restrictions on memory accesses are imposed by the MIPS ISA?
6. How does the i386 differ from the MIPS in goals and implementation?
7. <http://www.cl.cam.ac.uk/teaching/exams/pastpapers/y2014p5q2.pdf> (parts a and b)
8. <http://www.cl.cam.ac.uk/teaching/exams/pastpapers/y2009p5q3.pdf>