

## CADE-24: List of Accepted Papers

In alphabetical order by first author's last name:

- Noran Azmy and Christoph Weidenbach. Computing tiny clause normal forms.
- Franz Baader, Stefan Borgwardt and Marcel Lippmann. Temporalizing ontology-based data access.
- Peter Baumgartner and Uwe Waldmann. Hierarchic superposition with weak abstraction.
- Bernhard Beckert and Daniel Bruns. Dynamic logic with trace semantics.
- Bernhard Beckert, Rajeev Goré and Carsten Schürmann. Analysing vote counting algorithms via logic and its application to the CADE election scheme (System description).
- Markus Bender, Björn Pelzer and Claudia Schon. System description: E-KRHyper 1.4 – Extensions for unique names and description logic.
- Jasmin Christian Blanchette and Andrei Paskevich. TFF1: The TPTP typed first-order form with rank-1 polymorphism (System description).
- Zakaria Chihani, Dale Miller and Fabien Renaud. Foundational proof certificates in first-order logic.
- Marta Cialdea Mayer. A proof procedure for hybrid logic with binders, transitivity and relation hierarchies.
- Koen Claessen, Moa Johansson, Dan Rosen and Nicholas Smallbone. Automating inductive proofs using theory exploration.
- Hubert Comon-Lundh, Veronique Cortier and Guillaume Scerri. Tractable inference systems: an extension with a deducibility predicate.
- Leonardo De Moura and Grant Olney Passmore. Computation in real closed infinitesimal and transcendental extensions of the rationals.
- Serdar Erbatur, Santiago Escobar, Deepak Kapur, Zhiqiang Liu, Christopher Lynch, Catherine Meadows, José Meseguer, Paliath Narendran, Sonia Santiago and Ralf Sasse. Asymmetric unification: A new unification paradigm for cryptographic protocol analysis.
- Serdar Erbatur, Deepak Kapur, Andrew M Marshall, Paliath Narendran and Christophe Ringeissen. Hierarchical combination.
- Graeme Gange, Harald Sondergaard, Peter Stuckey and Peter Schachte. Solving difference constraints over modular arithmetic.
- Sicun Gao, Soonho Kong and Edmund Clarke. dReal: An SMT solver for nonlinear theories of the reals (System description).

- Rajeev Goré and Jimmy Thomson. An improved BDD method for intuitionistic propositional logic: BDDIntKt System Description.
- Reiner Hähnle, Ina Schäfer and Richard Bubel. Reuse in software verification by abstract method calls.
- Marijn Heule, Warren Hunt and Nathan Wetzler. Verifying refutations with extended resolution.
- Kryštof Hoder and Andrei Voronkov. The 481 ways to split a clause and deal with propositional variables.
- Radu Iosif, Adam Rogalewicz and Jiri Simacek. The tree width of separation logic with recursive definitions. **(Best Paper Award winner)**
- Cezary Kaliszyk and Josef Urban. PRoCH: Proof reconstruction for HOL Light (System description).
- Mark Kaminski and Tobias Tebbi. InKreSAT: Modal reasoning via incremental reduction to SAT (System description).
- Abdelkader Kersani and Nicolas Peltier. Completeness and decidability results for first-order clauses with indices.
- Gergely Kovásznaï, Andreas Fröhlich and Armin Biere. BV2EPR: A tool for polynomially translating quantifier-free bit-vector formulas into EPR (System description).
- Daniel Kühlwein, Josef Urban and Stephan Schulz. E-MaLeS 1.1 (System description).
- Shuvendu Lahiri, Chris Hawblitzel, Henrique Rebêlo and Ming Kawaguchi. Towards modularly comparing programs using automated theorem provers.
- Ulrich Loup, Karsten Scheibler, Florian Corzilius, Erika Abraham and Bernd Becker. A symbiosis of interval constraint propagation and cylindrical algebraic decomposition.
- Andrew Reynolds, Cesare Tinelli, Amit Goel, Sava Krstić, Morgan Deters and Clark Barrett. Quantifier instantiation techniques for finite model finding in SMT.
- Viorica Sofronie-Stokkermans. Hierarchical reasoning and model generation for the verification of parametric hybrid systems.
- Richard Williams and Boris Konev. Propositional temporal proving with reductions to a SAT problem.