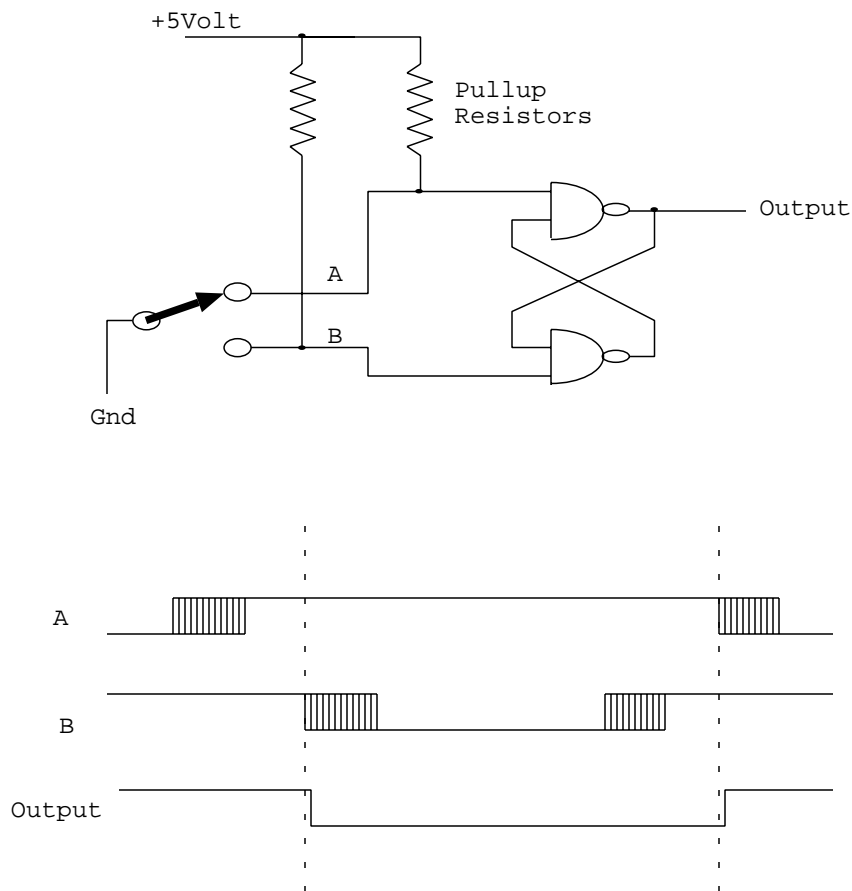


Debouncing Handout



The circuit above shows the standard technique for debouncing a switch. This means getting a single clean edge from each transition.

The RS latch flips the first time the wiper of the switch gets to the other side and so produces the clean output.

Since the Teaching Board switches all have two contacts configured in this way, this debounce circuit can be implemented in Verilog inside the FPGA.