

CHASSIS 1.

<u>Inputs.</u>		<u>Outputs.</u>	
<u>Pin No.</u>	<u>Source.</u>	<u>Pin No.</u>	<u>Designation.</u>
41	CH (J1)	55	+F
42	CcH(J2)	60	+E
43	KC (J3)	61	-F
44	XCn(J4) See note 2.	66	-E
45	Fn(J16) See note 4.	67	Carry out.
46	MF (J18)	68	-M
47	LE (J15)	69	+M
48	MK (J10)	<u>Neon Outputs.</u>	
49	LMn(J8) See note 3.		
50	FRM(J7)	25	M
51	FLM(J6)	26	L
52	ERL(J14) See note 1.	27	C
53	XL (J11)	28	K
54	ELL(J13)	29	E
56	+ER	30	F
57	+FL	31	H
58	+EL	32	+
59	+FR See note 1.		
62	-ER	33	Cy
63	-FL		
64	-EL		
65	+FR		
70	-X		
71	+X		
72	Carry in, See note 5.		

Notes.

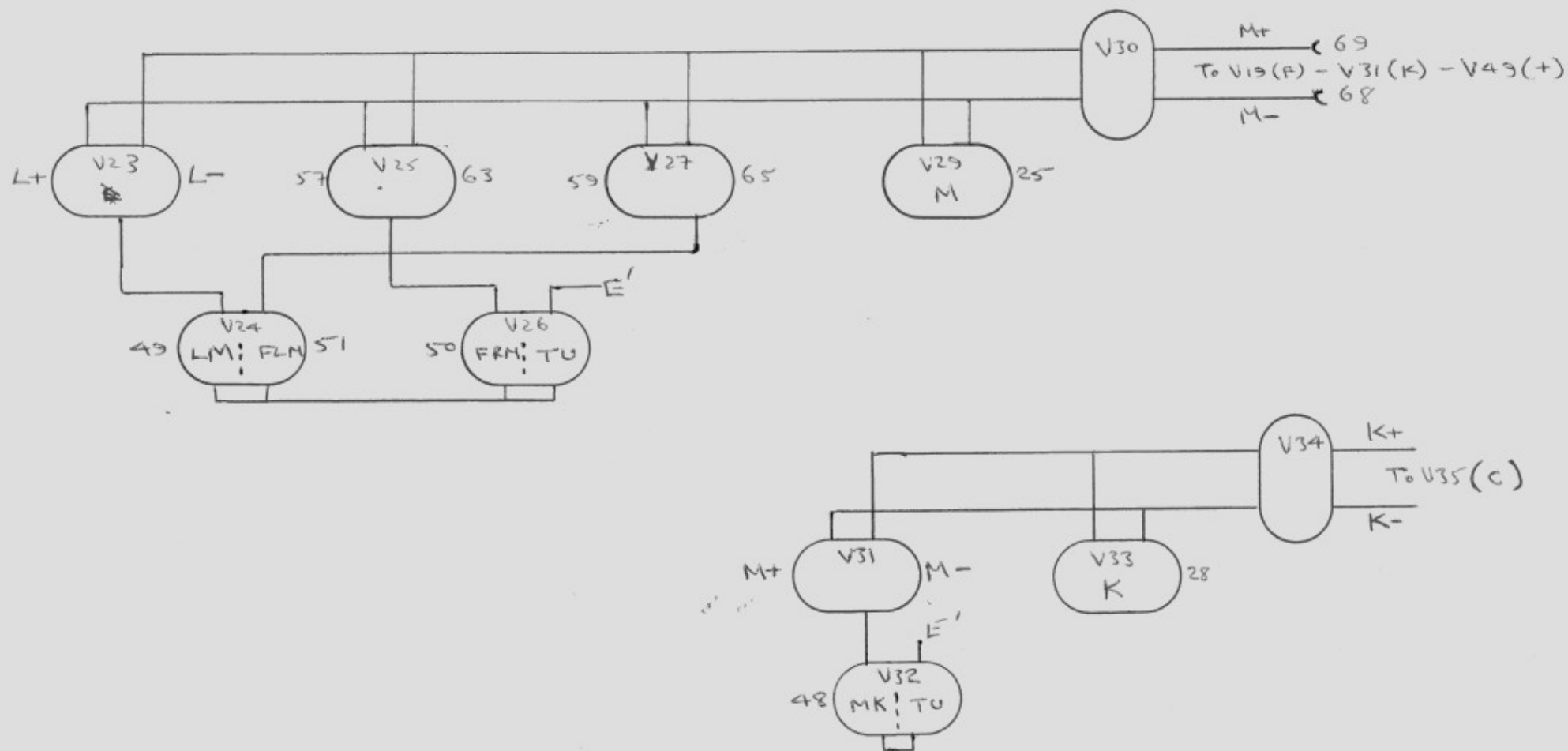
1. End Connections:-

- F-1 is connected to F-lR; i.e. Pin 55 digit -1 to pin 59 digit -1, also Pin 61 digit -1 to Pin 65 digit -1.
- E1 connected to F39L; i.e.
Pin 60 digit 1 to pin 57 digit 39, also
Pin 66 digit 1 to pin 63 digit 39.
- F39 is connected to ElR; i.e.
Pin 55 digit 39 to Pin 56 digit 1; also
Pin 61 digit 39 to Pin 62 digit 1.
- Z1 is connected to E39L; i.e.
Pin 58 digit 39 to -Z1; also
Pin 64 digit 39 to +Z1.

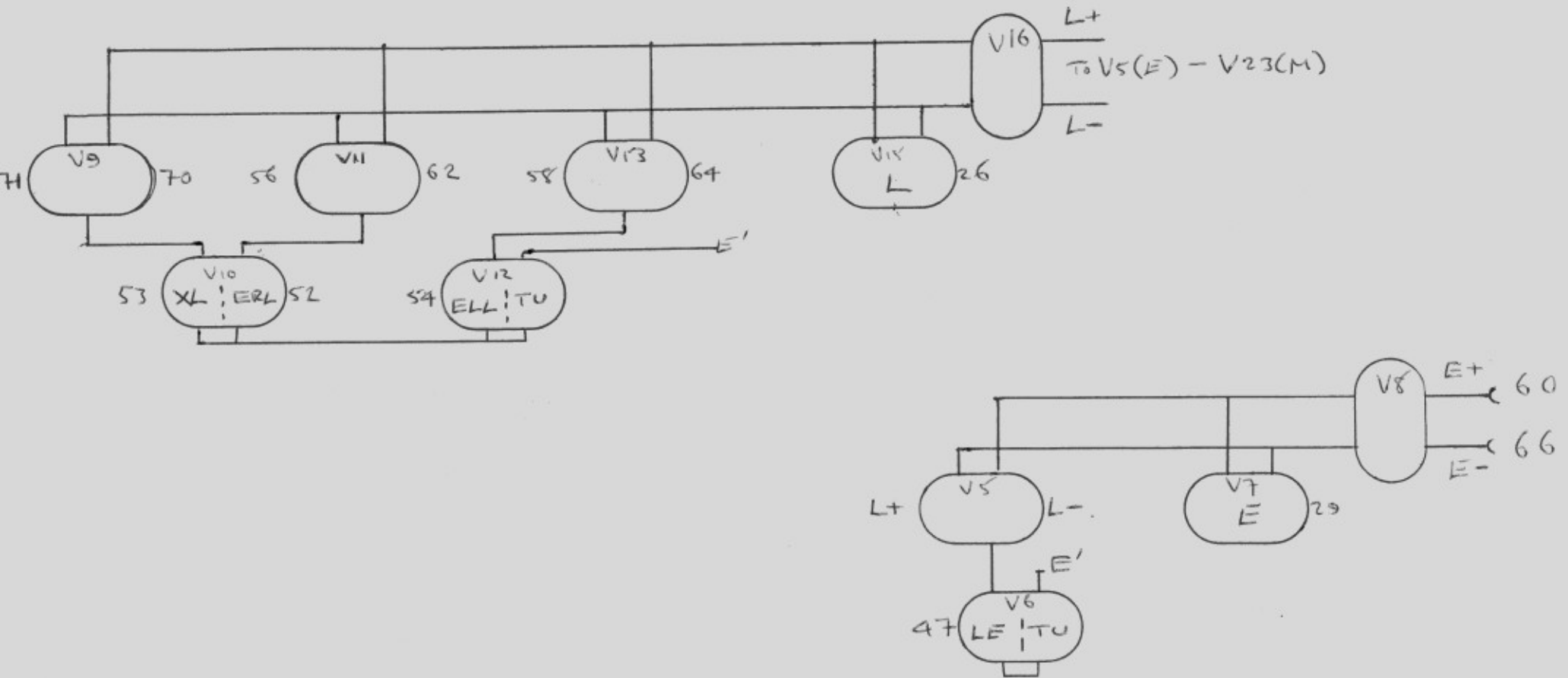
Chassis 1 (Continued).

Notes.

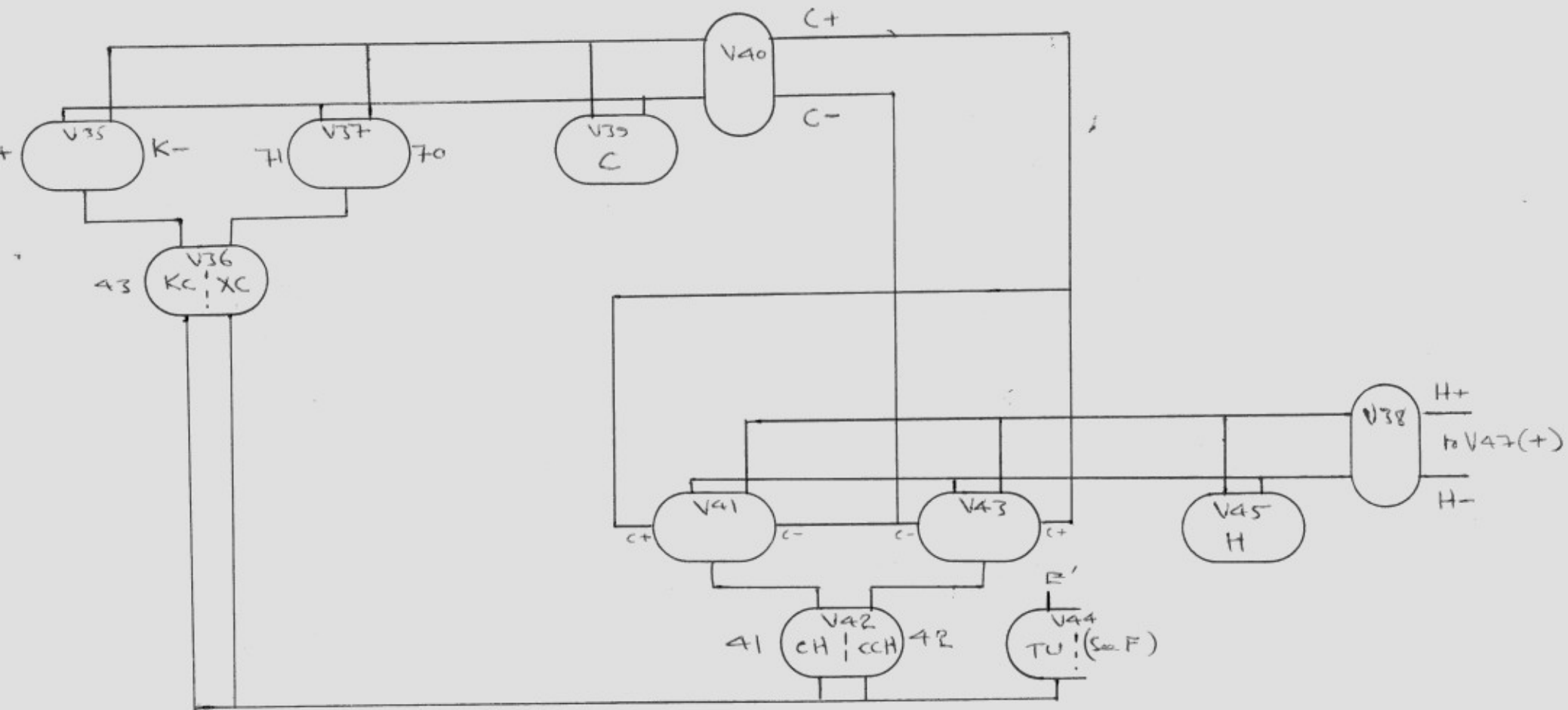
1. e) E-1 is connected to E-1R and E0R; i.e.
 Pin 60 digit -1 to Pin 56 digits -1 and 0; also
 Pin 66 digit -1 to Pin 62 digits -1 and 0.
 f) E0 is connected to E-1L; i.e.
 Pin 60 digit 0 to Pin 58 digit -1; also
 Pin 66 digit 0 to Pin 64 digit -1.
 g) ELL (J13) is not connected on digits -1 and 0; i.e.
 Pin 54 digits -1 and 0 is connected to -300 volts.
 h) ERL (J14) is connected to pin 52 on digits 1 to 39;
 EOL0 (J17) is connected to Pin 52 on digits -1 and 0.
2. XCN (J4) is connected to Pin 44 on digits
 -1,0 to 31;
 XCe (J5) is connected to Pin 44 on digits
 32 to 39.
3. LMn (J8) is connected to Pin 49 on digits
 -1,0 to 31;
 LMe (J9) is connected to Pin 49 on digits
 32 to 39.
4. +Fn (J16) is connected to Pin 45 on digits
 -1,0 to 31;
 +Fe (J19) is connected to Pin 45 on digits
 32 to 39.
5. +Z1 is connected to pin 72 of digit 39,
 Pin 72 is connected to Pin 67 of next
 less significant digit on the remaining chassis.



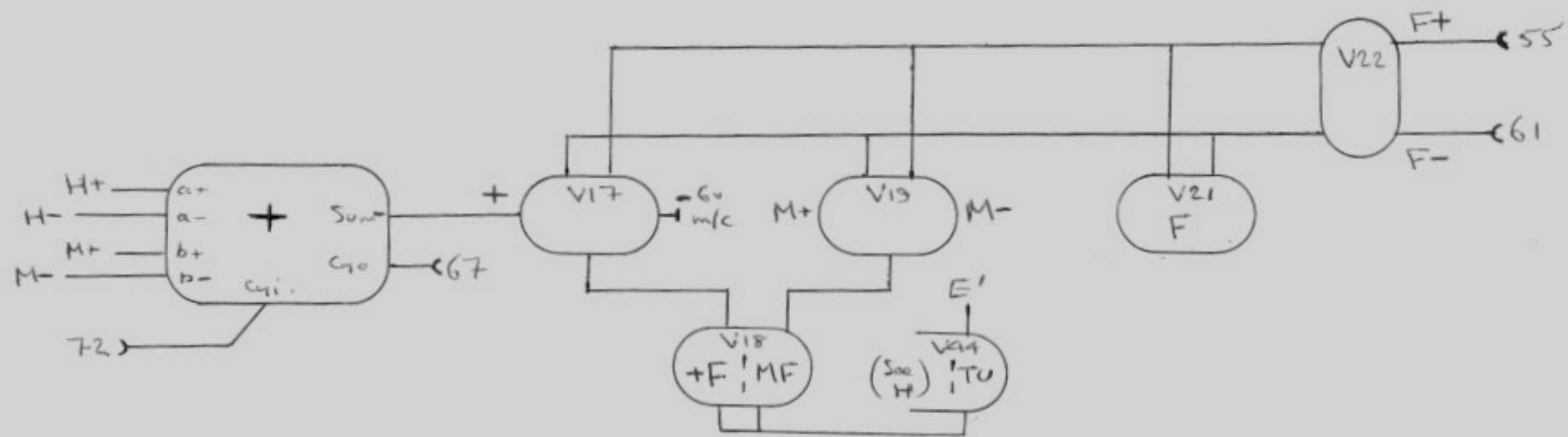
Schematics for 1-bit slice of RM and RK



Schematics for 1-bit slice of RL and RE



Schematic for 1-bit slice of RC and RH



Schematic for 1-bit slice of arithmetic mill adder and RF