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Explicit stabilisation for modular rely-guarantee reasoning

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Abstract

We propose a new formalisation of stability for Rely-Guarantee, in which an assertion's stability is encoded into its syntactic form. This allows two advances in modular reasoning. Firstly, it enables Rely-Guarantee, for the first time, to verify concurrent libraries independently of their clients' environments. Secondly, in a sequential setting, it allows a module's internal interference to be hidden while verifying its clients. We demonstrate our approach by verifying, using RGSep, the Version 7 Unix memory manager, uncovering a twenty-year-old bug in the process.

This technical report comprises the contents of a conference paper of the same name [21], plus several appendices.

1 Introduction

Reasoning about concurrent programs is hard because commands from different threads are interleaved non-deterministically. With many threads and many commands per thread, naïve reasoning soon succumbs to a combinatorial explosion. The Rely-Guarantee (RG) method [14] restores tractability through abstraction. In addition to the pre and postconditions inherited from Hoare logic [12], a command is specified by two relations between states: a *rely* R that specifies all the state transitions (or ‘actions’) the environment can cause, and a *guarantee* G that specifies all the actions of the command itself. (The environment is the set of concurrently-running threads.) The method conservatively assumes that between consecutive commands in a thread, any number of actions in R may occur. The truth of an assertion that holds after one command must be preserved by this ‘interference’, so that it may be safely assumed by the next command. Such an assertion is deemed *stable under* R .

Stability is traditionally enforced through side-conditions on proof rules. We propose (Sect. 3) a new formalisation in which stability is recorded within the syntactic form of the assertion itself. Just as ‘explicit substitution’ [1] added substitution to the syntax of λ -calculus terms, our work adds stabilisation to the syntax of RG assertions. We propose two new constructs: $\lfloor p \rfloor_R$ to denote the weakest assertion that is both stronger than p and stable under R , and dually, $\lceil p \rceil_R$ to denote the strongest stable assertion that is weaker than p .

The main benefit is in modularity, two forms of which we tease apart and tackle separately: verifying concurrent libraries independently of clients, and verifying clients of a (sequential) module independently of its implementation.

Verifying libraries independently of clients RG is a compositional method: an entire program’s proof depends only upon the proofs of its constituent commands. Yet it is not modular: a command’s proof cannot necessarily be re-used when the command features in a different program, because proofs are environment-specific. Thus, RG cannot verify libraries that are invoked in several different environments. Our solution (Sect. 4) has the library record stability requirements using $\lfloor - \rfloor_R$ and $\lceil - \rceil_R$, but leave the specification parametric in R . Each client then instantiates R appropriately and performs the stabilisation.

Verifying clients independently of module implementations In Sect. 5, we bring explicit stabilisation to an RG-style logic that reasons about heap-manipulating programs: RGSep [20]. Because it divides the heap into both thread-local and shared regions, RGSep’s notion of stability is more complex than that of ordinary RG; in particular, while only the shared heap is susceptible to interference, we shall see that the local heap can still affect stability arguments. Originally conceived for concurrency, RGSep is apt for verifying sequential modules too. Such a verification must consider every action by which a client can mutate the module’s part of the heap. Our extension of explicit stabilisation to RGSep permits an INFOHIDING rule that allows this so-called ‘internal interference’ to be hidden while verifying clients. We demonstrate (Sect. 6) this approach by verifying – for the first time – the Version 7 Unix memory manager. In doing so, we uncover a bug that has lain dormant since 1979.

$$\begin{array}{c}
\text{WEAKEN} \\
\frac{R', G' \vdash \{p'\} C \{q'\} \quad p \Rightarrow p' \quad q' \Rightarrow q \quad R \subseteq R' \quad G' \subseteq G}{R, G \vdash \{p\} C \{q\}} \\
\\
\text{PAR} \\
\frac{R \cup G_2, G_1 \vdash \{p_1\} C_1 \{q_1\} \quad R \cup G_1, G_2 \vdash \{p_2\} C_2 \{q_2\}}{R, G_1 \cup G_2 \vdash \{p_1 \wedge p_2\} C_1 \parallel C_2 \{q_1 \wedge q_2\}} \\
\\
\text{BASIC} \\
\frac{\vdash \{p\} c \{q\} \quad \overline{p} \cap c \subseteq G \quad p \text{ stab } R \quad q \text{ stab } R}{R, G \vdash \{p\} c \{q\}} \quad \text{SKIP} \quad \frac{p \text{ stab } R}{R, G \vdash \{p\} \text{ skip } \{p\}} \quad \text{LOOP} \quad \frac{R, G \vdash \{p\} C \{p\}}{R, G \vdash \{p\} C^+ \{p\}}
\end{array}$$

Figure 1: Selected RG proof rules (with stability checks)

We begin with a short introduction to the RG proof system, followed by a brief account of the failure of traditional RG to provide a modular specification for even one of the most trivial library functions: increment.

2 Background: Rely-Guarantee reasoning

RG specifications are of the form $R, G \vdash \{p\} C \{q\}$, where R and G are relations between states. Following [17], G shall be reflexive. This specification expresses that when C begins execution in a state satisfying the precondition p , in an environment whose interference is limited to the actions in the rely R , then any state transitions performed by C are within its guarantee G , and moreover, if the execution terminates, the final state satisfies the postcondition q .

Figure 1 presents a selection of the RG proof rules, which concern commands of the following simple parallel language:

$$C ::= \text{skip} \mid C; C \mid C \parallel C \mid C + C \mid C^+ \mid c$$

The $+$ operator chooses one of its operands to execute, while C^+ executes C at least once.¹ We consider only partial correctness, so these non-deterministic constructs for choice and looping suffice for encoding **if** and **while** commands. The language is parameterised on the set of basic commands c , which are relations that model atomic state transformations. We shall assume c includes **assert** and **assume** commands and variable assignment. See Appx. A and for the formal semantics of our language and Appx. B for the complete set of proof rules.

The BASIC rule requires that c meets the sequential specification $\{p\} c \{q\}$, and that any action it performs is within its guarantee. It uses the notation $\overline{p} \stackrel{\text{def}}{=} \{(\sigma, \sigma') \mid \sigma \models p\}$. The pre and postconditions of the two ‘ground’ commands, c and **skip**, are required to be stable. Since the other commands are built inductively from these, their rules can assume any inherited assertions to be stable (or else derived from stable assertions by the WEAKEN rule). Stability checks are notated as follows:

¹Interestingly, a variant of the LOOP rule for reasoning about C^* commands would require a stability check on p , in case C^* should behave like **skip**. Our language uses C^+ so as to sidestep this check.

Definition 1 (Stability). $p \text{ stab } R \stackrel{\text{def}}{=} \forall \sigma, \sigma'. \sigma \models p \wedge R(\sigma, \sigma') \implies \sigma' \models p$.

The PAR rule marks the epitome of RG reasoning. When reasoning about commands composed in parallel, the rely of each command is extended to include the guarantee of the other. The composed command $C_1 \parallel C_2$ guarantees actions in either of its components' guarantees, and establishes both components' postconditions upon completion.

2.1 The problem with verifying libraries

Consider a library function $f()$ that atomically increments a shared variable x . Its two clients, $g()$ and $h()$, invoke $f()$ in an empty environment and an environment that may increase x , respectively. Call this latter environment R_{x+} . The guarantee G_{x+} additionally dictates that no variable other than x changes.

Definition 2.

$f()$	$\stackrel{\text{def}}{=}$	$x++$
$g()$	$\stackrel{\text{def}}{=}$	$\text{assume}(x=3); f(); \text{assert}(x=4)$
$h()$	$\stackrel{\text{def}}{=}$	$\text{assume}(x=5); (f() \parallel f()); \text{assert}(x \geq 6)$
R_{x+}	$\stackrel{\text{def}}{=}$	$\{(\sigma, \sigma') \mid \sigma(x) \leq \sigma'(x)\}$
G_{x+}	$\stackrel{\text{def}}{=}$	$\{(\sigma, \sigma') \mid \sigma(x) \leq \sigma'(x) \wedge \forall v \neq x. \sigma(v) = \sigma'(v)\}$

Now, the proofs of $g()$ and $h()$ hinge, respectively, upon deriving the following two specifications for $f()$:

$$\emptyset, G_{x+} \vdash \{x=X\} f() \{x=X+1\} \qquad R_{x+}, G_{x+} \vdash \{x \geq X\} f() \{x \geq X+1\}$$

Both hold, yet no single 'most general' specification can derive them both. The first has the stronger postcondition but the smaller rely; the second is vice versa. This troublesome tradeoff can be blamed on stability: the larger the rely, the tougher the stability requirement, and thus, the weaker the postcondition.

In Sect. 4, we shall present a single specification for $f()$ from which both of the above can be derived. Parameterised on an arbitrary rely R , it simply states that the postcondition needs weakening from $x=X+1$ just enough to become stable under R . Upon instantiating R to R_{x+} , to verify $h()$, the postcondition becomes $x \geq X+1$. And when R is \emptyset , for $g()$'s proof, no weakening is required.

3 Explicit Stabilisation

This section describes our formalisation of stability and applies it to the RG proof rules. The remaining sections develop two alternate proof systems: one (Sect. 4) that can specify libraries independently of clients, and another (Sects. 5 and 6) that lets a module hide from clients its internal interference.

We propose two new syntactic constructs: $\lfloor p \rfloor_R$ for the weakest assertion that is stronger than p and stable under R , and $\lceil p \rceil_R$ for the strongest assertion that is weaker than p and stable under R . That is, $\lfloor p \rfloor_R = \bigvee \{q \mid q \Rightarrow p \wedge q \text{ stab } R\}$ and $\lceil p \rceil_R = \bigwedge \{q \mid q \Leftarrow p \wedge q \text{ stab } R\}$.

Definition 3 (Semantics of $\lfloor p \rfloor_R$ and $\lceil p \rceil_R$). The required properties are realised uniquely by the following constructions:

$$\begin{aligned} \sigma \models \lfloor p \rfloor_R &\stackrel{\text{def}}{\iff} \forall \sigma'. (\sigma, \sigma') \in R^* \implies \sigma' \models p \\ \sigma \models \lceil p \rceil_R &\stackrel{\text{def}}{\iff} \exists \sigma'. (\sigma', \sigma) \in R^* \wedge \sigma' \models p. \end{aligned}$$

Figure 2 presents the intuition behind our new operators. The nodes represent states; those that are filled satisfy some assertion p . The edges depict transitions of an arbitrary rely R . The states in $\lfloor p \rfloor_R$ are those from which any reachable state satisfies p . The states in $\lceil p \rceil_R$ are those reachable from a state in p .

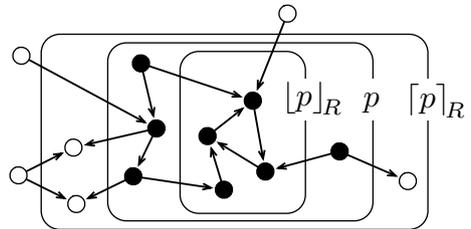


Figure 2: $\lfloor - \rfloor$ and $\lceil - \rceil$, intuitively

Our operators can also be defined using Dijkstra's predicate transformer semantics [6]: $\lfloor p \rfloor_R$ is the weakest precondition of R^* given postcondition p , while $\lceil p \rceil_R$ is the strongest postcondition of R^* given precondition p .

Example. We stabilise $x=0$ and $x \neq 0$ under R_{x+} (see Definition 2) like so:

$$\lfloor x=0 \rfloor_{R_{x+}} \iff \text{false} \quad \lceil x=0 \rceil_{R_{x+}} \iff x \geq 0 \quad \lfloor x \neq 0 \rfloor_{R_{x+}} \iff x > 0 \quad \lceil x \neq 0 \rceil_{R_{x+}} \iff \text{true}$$

3.1 Properties of explicit stabilisation

Both $\lfloor - \rfloor$ and $\lceil - \rceil$ are monotonic with respect to \implies . They are related via the equivalence $\lfloor \neg p \rfloor_R \iff \neg \lceil p \rceil_{R^{-1}}$. Each has no effect on an already-stable operand, or when R is empty. Both **true** and **false** are stable, and conjunction and disjunction both preserve stability. The distributivity properties of $\lfloor - \rfloor$ and $\lceil - \rceil$ over \wedge and \vee are analogous to those of \forall and \exists respectively:

$$\begin{aligned} \lfloor p \wedge q \rfloor_R &\iff \lfloor p \rfloor_R \wedge \lfloor q \rfloor_R & \lfloor p \vee q \rfloor_R &\iff \lfloor p \rfloor_R \vee \lfloor q \rfloor_R \\ \lceil p \wedge q \rceil_R &\implies \lceil p \rceil_R \wedge \lceil q \rceil_R & \lceil p \vee q \rceil_R &\iff \lceil p \rceil_R \vee \lceil q \rceil_R \end{aligned}$$

Several properties mirror those of the floor and ceiling functions in arithmetic, from which our syntax is borrowed. If $R \subseteq R'$, we have:

$$\begin{aligned} \lfloor \lfloor p \rfloor_R \rfloor_{R'} &\iff \lfloor \lfloor p \rfloor_{R'} \rfloor_R \iff \lceil \lceil p \rceil_{R'} \rceil_R \iff \lceil p \rceil_{R'} \\ \lceil \lceil p \rceil_R \rceil_{R'} &\iff \lceil \lceil p \rceil_{R'} \rceil_R \iff \lfloor \lfloor p \rfloor_{R'} \rfloor_R \iff \lfloor p \rfloor_{R'} \end{aligned}$$

Finally, the following property reminds us of the trade-off mentioned in Sect. 2.1: that as the rely becomes more permissive, stability becomes harder to show:

$$R \subseteq R' \text{ implies } \lfloor p \rfloor_R \iff \lfloor p \rfloor_{R'} \text{ and } \lceil p \rceil_R \implies \lceil p \rceil_{R'}$$

The properties presented in this section have been formalised using the Isabelle theorem prover; the proof script is available online.²

3.2 Application to RG proof rules

We now describe how the RG proof rules (Fig. 1) can be adapted to use explicit stabilisation rather than side-conditions.

²<http://www.cl.cam.ac.uk/~jpw48/expstab.thy.html>

Figure 3 displays the replacements for the BASIC and SKIP rules; the others remain unchanged. The BASIC-S rule first derives p and q by considering c sequentially; that is, without concern for stability. A concurrent specification is obtained by strengthening p and weakening q until they are both stable. The SKIP-S axiom is justified by considering the execution of `skip` from an initial state satisfying p . This state also satisfies $\lceil p \rceil_R$, and the final state must too, since `skip` does nothing. The following backward-reasoning alternative is interderivable: $R, G \vdash \{\lceil p \rceil_R\} \text{ skip } \{p\}$.

The new rules are at least as powerful as the originals, which can be obtained by restoring the stability checks and then removing the redundant stabilisations.

$$\begin{array}{c} \text{BASIC-S} \\ \frac{\vdash \{p\} c \{q\} \quad \overline{\lceil p \rceil} \cap c \subseteq G}{R, G \vdash \{\lceil p \rceil_R\} c \{\lceil q \rceil_R\}} \\ \\ \text{SKIP-S} \\ \frac{}{R, G \vdash \{p\} \text{ skip } \{\lceil p \rceil_R\}} \end{array}$$

Figure 3: New RG proof rules (with stabilised assertions)

3.3 Aside: Simplification of complex RG proof rules

We now highlight the elegance of explicit stabilisation by showing how it can simplify and generalise complex RG proof rules that rely subtly upon stability.

Coleman [5] proposes the following rule for reasoning about one-armed conditional statements whose test conditions are evaluated non-atomically in the presence of interference.

$$\frac{\text{StableExpr}(e_s, R) \quad R, G \vdash \{p \wedge e_s\} C \{q\} \quad \text{SingleUnstableVar}(e_u, R) \quad \forall \sigma, \sigma'. \sigma \models p \wedge (\sigma, \sigma') \in R^* \wedge \sigma' \models \neg(e_s \wedge e_u) \implies \sigma' \models q \quad \{\neg e_u, p, q\} \text{ stab } R}{R, G \vdash \{p\} \text{ if } e_u \wedge e_s \text{ then } C \{q\}}$$

Tests are pure, and comprise an unstable conjunct e_u and a ‘stable’ conjunct e_s that contains no variables that R can change (first premise). Crucially, only e_s can be assumed still to hold by C (second premise). By requiring e_u to involve only a single read of an unstable variable (third premise), we can treat it as a predicate of a single state – the state in which the read occurs – despite not knowing which state that is. Should the test fail, the postcondition must be met without evaluating C (fourth premise). That premise requires R to preserve the falsity of e_u (fifth premise) so as to ensure that the obligation to fulfil q cannot be bypassed by having the test evaluate to `false` but later become logically `true`.

Now consider the following alternative rule, which uses explicit stabilisation.

$$\frac{\text{SingleUnstableVar}(e, R) \quad \{p, q\} \text{ stab } R \quad \frac{R, G \vdash \{p \wedge \lceil e \rceil_R\} C_1 \{q\} \quad R, G \vdash \{p \wedge \lceil \neg e \rceil_R\} C_2 \{q\}}{R, G \vdash \{p\} \text{ if } e \text{ then } C_1 \text{ else } C_2 \{q\}}}{R, G \vdash \{p\} \text{ if } e \text{ then } C_1 \text{ else } C_2 \{q\}}$$

Essentially, the execution of C_1 begins in a state that is reachable (by a sequence of environment actions) from one in which e evaluated to `true`. Similarly, $\lceil \neg e \rceil_R$ describes a state reached from one where e did not hold. Stability checks on p and q remain only for compatibility with the rest of Coleman’s system.

Thanks to explicit stabilisation, the new rule has fewer and simpler premises, plus it extends naturally to two-armed conditionals. Moreover, e need not be split into stable and unstable conjuncts, for our rule handles arbitrary test conditions.

$$\begin{array}{c}
\text{P-WEAKEN} \\
\frac{\mathbb{R}', G' \vdash_{\text{P}} \{\mathbf{p}'\} C \{\mathbf{q}'\} \quad \mathbf{p} \Rightarrow_{\mathbb{R}} \mathbf{p}' \quad \mathbf{q}' \Rightarrow_{\mathbb{R}} \mathbf{q} \quad \mathbb{R} \subseteq \mathbb{R}' \quad G' \subseteq G}{\mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}\} C \{\mathbf{q}\}}
\end{array}
\quad
\begin{array}{c}
\text{P-PAR} \\
\frac{\mathbb{R} \cup G_2, G_1 \vdash_{\text{P}} \{\mathbf{p}_1\} C_1 \{\mathbf{q}_1\} \quad \mathbb{R} \cup G_1, G_2 \vdash_{\text{P}} \{\mathbf{p}_2\} C_2 \{\mathbf{q}_2\}}{\mathbb{R}, G_1 \cup G_2 \vdash_{\text{P}} \{\mathbf{p}_1 \parallel_{G_2} \mathbf{p}_2\} C_1 \parallel C_2 \{\mathbf{q}_1 \parallel_{G_1} \mathbf{q}_2\}}
\end{array}$$

$$\begin{array}{c}
\text{P-BASIC} \\
\frac{\vdash \{\mathbf{p}\} c \{\mathbf{q}\} \quad \overline{p} \cap c \subseteq G}{\mathbb{U}, G \vdash_{\text{P}} \{\lambda R. \lfloor p \rfloor_R\} c \{\lambda R. \lceil q \rceil_R\}}
\end{array}
\quad
\begin{array}{c}
\text{P-SKIP} \\
\frac{}{\mathbb{U}, G \vdash_{\text{P}} \{\lambda _ . p\} \text{skip} \{\lambda R. \lceil p \rceil_R\}}
\end{array}$$

Abbreviations:

$$\begin{array}{l}
\mathbf{p}_1 \Rightarrow_{\mathbb{R}} \mathbf{p}_2 \stackrel{\text{def}}{=} \forall R \in \mathbb{R}. \mathbf{p}_1(R) \Rightarrow \mathbf{p}_2(R) \quad \mathbb{R} \cup R \stackrel{\text{def}}{=} \{R' \cup R \mid R' \in \mathbb{R}\} \\
\mathbf{p}_1 \parallel_{R_1} \mathbf{p}_2 \stackrel{\text{def}}{=} \lambda R. \mathbf{p}_1(R \cup R_1) \wedge \mathbf{p}_2(R \cup R_2) \quad \mathbb{U} \stackrel{\text{def}}{=} \text{universal set of all relies}
\end{array}$$

Figure 4: Selected proof rules for parametric specifications

4 Verifying concurrent library code

Equipped with a notation for stabilising assertions, we revisit the challenge we set in Sect. 2.1: to verify concurrent library code using RG reasoning.

Recall our library function $\mathbf{f}()$ and its clients $\mathbf{g}()$ and $\mathbf{h}()$ from Definition 2. Using explicit stabilisation, we can derive the following specification, which is parametric in R (although its instantiation will be restricted, as described shortly).

$$R, G_{x^+} \vdash \{\lfloor \mathbf{x} = X \rfloor_R\} \mathbf{f}() \{\lceil \mathbf{x} = X + 1 \rceil_R\}$$

Observe that instantiating R to \emptyset yields a specification suitable for proving $\mathbf{g}()$, while $\mathbf{h}()$ can be proved having set R to R_{x^+} . We now present a proof system for such ‘parametric specifications’ and formally derive the above one for $\mathbf{f}()$.

In a parametric specification, the rely is replaced by a set of relies \mathbb{R} , and the pre and postconditions (denoted \mathbf{p} , \mathbf{q} , \mathbf{r}) become functions from relies to assertions. We shall use λ -calculus notation to describe such functions.

Definition 4. $\mathbb{R}, G \models_{\text{P}} \{\mathbf{p}\} C \{\mathbf{q}\} \stackrel{\text{def}}{\iff} \forall R \in \mathbb{R}. R, G \models \{\mathbf{p}(R)\} C \{\mathbf{q}(R)\}$.

As the definition above shows, a parametric specification represents a family of specifications, one for each rely in \mathbb{R} . A selection of proof rules for parametric specifications are presented in Fig. 4; those not depicted are lifted in the obvious way. (See Appx. C for the full set.)

The P-PAR rule has grown considerably more complex. The reason is that at the fork and join of parallel commands, the rely changes. If the rely is R initially, then within the component commands the rely becomes either $R \cup G_2$ or $R \cup G_1$, and after joining, it reverts to R . Our rule simply reflects this progression.

The P-BASIC and P-SKIP rules both deduce specifications that feature the universal set of relies, which enables their use in *any* environment. The P-WEAKEN rule can then be used to shrink this set, typically removing the bigger relies. Doing so restricts a specification’s reusability, but it enhances the applicability of the $\Rightarrow_{\mathbb{R}}$ relation that allows it to be simplified.

$$\begin{array}{c}
\overline{\vdash \{p\} \text{x}++ \{p[x-1/x]\}} \text{ FLOYD'S ASSIGNMENT AXIOM} \\
\overline{\vdash \{[x=X]_R\} \text{x}++ \{[x=X]_R [x-1/x]\}} \text{ Instantiate } p \text{ to } [x=X]_R \\
\overline{\mathbb{U}, G_{x+} \vdash_P \{\lambda R. [x=X]_R\} \text{x}++ \{\lambda R. [[x=X]_R [x-1/x]]_R\}} \text{ P-BASIC} \\
\overline{\text{comm}(\text{x}++), G_{x+} \vdash_P \{\lambda R. [x=X]_R\} \text{x}++ \{\lambda R. [x=X+1]_R\}} \text{ P-WEAKEN}
\end{array}$$

Figure 5: Derivation of parametric specification for $\mathbf{f}()$

$$\begin{array}{ccc}
\text{comm}(\text{x}++), G_{x+} \vdash_P \{\lambda R. [x=X]_R\} \text{x}++ \{\lambda R. [[x=X]_R [x-1/x]]_R\} & & \\
\text{Set } R \text{ to } \emptyset \swarrow & & \searrow \text{Set } R \text{ to } R_{x+} \\
\emptyset, G_{x+} \vdash \{x=X\} \mathbf{f}() \{x=X+1\} & & R_{x+}, G_{x+} \vdash \{x \geq X\} \mathbf{f}() \{x \geq X+1\}
\end{array}$$

Figure 6: Instantiating the specification

Theorem 5. *The proof rules of parametric stability are sound, that is:*

$$\mathbb{R}, G \vdash_P \{p\} C \{q\} \implies \mathbb{R}, G \models_P \{p\} C \{q\}$$

and they encode the proof rules of Fig. 1 (in which assertions do not contain explicit stabilisation), both completely and soundly, that is:

$$\begin{array}{l}
R, G \vdash \{p\} C \{q\} \implies \mathcal{P}(R), G \vdash_P \{\lambda_. p\} C \{\lambda_. q\} \\
R, G \models \{p\} C \{q\} \iff \mathcal{P}(R), G \models_P \{\lambda_. p\} C \{\lambda_. q\}
\end{array}$$

Here, the use of powersets lets the P-WEAKEN rule emulate the WEAKEN rule.

Figure 5 shows the derivation of our specification for $\mathbf{f}()$. In applying the P-BASIC rule, we utilised the identity $[[x=X]_R]_R \Leftrightarrow [x=X]_R$. The specification on the third line is the most general, as it allows the rely to be instantiated freely. Yet we do not stop there. We restrict the rely to the set $\text{comm}(\text{x}++)$ of those that ‘commute’ with the $\text{x}++$ operation; that is, for which $[p]_R [x-1/x] \Leftrightarrow [p[x-1/x]]_R$ holds for all p . Using this property we can simplify the postcondition.

Figure 6 shows informally how the parametric specification can then be instantiated to two ordinary specifications, for use in proving the two clients $\mathbf{g}()$ and $\mathbf{h}()$. Really, this ‘instantiation’ is an application of the P-WEAKEN rule to restrict \mathbb{R} to the singletons $\{\emptyset\}$ and $\{R_{x+}\}$ respectively.³

In conclusion, we find that the ‘most general’ specifications that our parametric scheme can deduce are, though sometimes desirable, inhibited by their complexity. The specification on the third line of Fig. 5 contains two stabilisation operations in its postcondition – and this is for just a single basic command. A sequence of n basic commands, specified in a similar way, may contain up to $n + 1$ stabilisation operations in the postcondition (modelling the environmental interference before, between and after the commands). The complexity of the specification is thus comparable to the implementation it describes. Accordingly, it is crucial that our scheme allows specifications to be specialised to restricted sets of relies, and thence, simplified.

³Interestingly, although the relies \emptyset and R_{x+} are both in $\text{comm}(\text{x}++)$, the same is not true of all those in $\mathcal{P}(R_{x+})$: for instance, the rely that only increments x from 1 to 2.

$P ::= e \overset{k}{\mapsto} e \mid \mathbf{emp} \mid e = e \mid e > e \mid \mathbf{true} \mid \neg P \mid P \Rightarrow P \mid P * P \mid \exists x. P \mid \lfloor P \rfloor_R \mid \lceil P \rceil_R$
 where $k \in (0, 1]$ and e is a pure expression.

$$\begin{aligned}
 h, i \models_{\text{SL}} e_0 \overset{k}{\mapsto} e_1 & \stackrel{\text{def}}{\iff} h = \{ \llbracket e_0 \rrbracket_i \overset{k}{\mapsto} \llbracket e_1 \rrbracket_i \} \\
 h, i \models_{\text{SL}} \mathbf{emp} & \stackrel{\text{def}}{\iff} h = \emptyset \\
 h, i \models_{\text{SL}} P_0 * P_1 & \stackrel{\text{def}}{\iff} \exists h_0, h_1. h_0 \perp h_1 \wedge h = h_0 \uplus h_1 \wedge h_0, i \models_{\text{SL}} P_0 \wedge h_1, i \models_{\text{SL}} P_1 \\
 h, i \models_{\text{SL}} \lfloor P \rfloor_R & \stackrel{\text{def}}{\iff} \forall h'. (h, h') \in R^* \implies h', i \models_{\text{SL}} P \\
 h, i \models_{\text{SL}} \lceil P \rceil_R & \stackrel{\text{def}}{\iff} \exists h'. (h', h) \in R^* \wedge h', i \models_{\text{SL}} P
 \end{aligned}$$

where $h \perp h'$ means $\text{dom}(h)$ and $\text{dom}(h')$ are disjoint.

Figure 7: Syntax and (selected) semantics of separation logic assertions

5 Explicit Stabilisation for RGSep

We now bring explicit stabilisation to RGSep [20], an RG-style logic that reasons about concurrent heap-manipulating programs by splitting the heap into shared and thread-local parts. The development in this section builds upon our application of explicit stabilisation to RG (Sect. 3), but we shall now leave behind the parametric specifications of Sect. 4.

Though designed for concurrency, we show (Sect. 5.3) how RGSep can be applied to sequential modules by reinterpreting the ‘shared’ heap as that part owned by the module (its so-called ‘internal heap’). Our extension of RGSep with explicit stabilisation enables an INFOHIDING proof rule, by which a module can hide from clients the interference that affects its internal heap. We demonstrate our approach in Sect. 6, by verifying the Version 7 Unix memory manager.

5.1 Introduction to RGSep

RGSep extends ordinary RG reasoning with conceptual divisions of the heap into thread-local and shared parts. The rely and guarantee need specify only changes to the shared part, and thus become far more compact.

RGSep inherits its ability to reason naturally about heap-manipulating programs from separation logic [13, 18], the assertion language of which is presented in Fig. 7. States comprise a heap h mapping locations to values and a store i mapping variables to values. The $*$ operator attempts to split the heap using the \uplus operator, such that the two (disjoint) parts respectively satisfy its two operands. We use the fractional permissions model [3], in which a heap may describe some locations only partially. For instance, the assertion $x \overset{1}{\mapsto} 3$ describes a heap comprising a single location x with value 3, and confers full (write) permission on that location. It may be split into several read-only permissions (e.g. $x \overset{5}{\mapsto} 3 * x \overset{5}{\mapsto} 3$) which may be shared between different threads. Threads communicate only via the heap, so the stabilisation operators can ignore the store.

Figure 8 presents the assertion language of RGSep, augmented with explicit stabilisation. The heap is split into disjoint local and shared regions, l and s , which are described by unboxed and boxed assertions respectively. The $*$ operator now splits only the local heap. The shared heap is never split, in order that all threads share the same view of it. For instance, if one thread’s view of the overall state is described by $\boxed{P_s} * P_l$, and

$$\begin{aligned}
p &::= P \mid \boxed{P} \mid p * p \mid p \wedge p \mid p \vee p \mid \exists x. p \mid \forall x. p \mid \lfloor p \rfloor_R \mid \lceil p \rceil_R \\
l, s, i \models P &\stackrel{\text{def}}{\iff} l, i \models_{\text{SL}} P \\
l, s, i \models \boxed{P} &\stackrel{\text{def}}{\iff} l = \emptyset \wedge s, i \models_{\text{SL}} P \\
l, s, i \models p_0 * p_1 &\stackrel{\text{def}}{\iff} \exists s_0, s_1. s_0 \perp s_1 \wedge s = s_0 \uplus s_1 \wedge l, s_0, i \models p_0 \wedge l, s_1, i \models p_1 \\
l, s, i \models \lfloor p \rfloor_R &\stackrel{\text{def}}{\iff} \forall s'. (s, s') \in (R \setminus l)^* \implies l, s', i \models p \\
l, s, i \models \lceil p \rceil_R &\stackrel{\text{def}}{\iff} \exists s'. (s', s) \in (R \setminus l)^* \wedge l, s', i \models p
\end{aligned}$$

Figure 8: Syntax and (selected) semantics of RGSep assertions

another's by $\boxed{Q_s} * Q_l$, then the $*$ operator combines them thus: $\boxed{P_s \wedge Q_s} * P_l * Q_l$.

Definition 6 (RGSep actions). The *action* $P \rightsquigarrow Q$, which is defined as $\{(s \uplus s_0, s' \uplus s_0) \mid \exists i. s, i \models_{\text{SL}} P \wedge s', i \models_{\text{SL}} Q\}$, replaces a part of the shared heap satisfying P with one satisfying Q .

Definition 7 (Contextual actions). The *contextual action* $P \rightsquigarrow Q \mid F$, defined as $\{(s \uplus_{S_F} s_0, s' \uplus_{S_F} s_0) \mid \exists i. s, i \models_{\text{SL}} P \wedge s', i \models_{\text{SL}} Q \wedge s_F, i \models_{\text{SL}} F\}$, requires a separate (unaffected) part of the heap that satisfies F to catalyse it.

5.2 RGSep and stabilisation

Our semantics of $\lfloor p \rfloor_R$ and $\lceil p \rceil_R$ (Fig. 8) imposes the following restriction on R :

Definition 8 (Restricting the rely). $R \setminus l \stackrel{\text{def}}{=} \{(s, s') \in R \mid l \perp s \wedge l \perp s'\}$

The $R \setminus l$ operation removes from R impossible environmental actions that would make the shared heap overlap the current thread's local heap l .⁴

All of the properties detailed in Sect. 3.1 continue to hold. The following series of lemmas describe some additional RGSep-specific properties. Lemma 9 asserts that local assertions are vacuously stable.

Lemma 9 (Local assertions). $\lfloor P \rfloor_R \Leftrightarrow \lceil P \rceil_R \Leftrightarrow P$.

The next lemma says that we need not restrict the rely when stabilising a shared assertion. Such assertions imply that the local heap is empty (see Fig. 8), and thus unable to conflict with the shared heap.

Lemma 10 (Shared assertions). $\lfloor \boxed{P} \rfloor_R \Leftrightarrow \boxed{\lfloor P \rfloor_R}$ and $\lceil \boxed{P} \rceil_R \Leftrightarrow \boxed{\lceil P \rceil_R}$.

Finally, we describe the distributivity of the stabilisation operators over $*$.

Lemma 11 (Separately-conjoined assertions). $\lfloor p \rfloor_R * \lfloor q \rfloor_R \Rightarrow \lfloor p * q \rfloor_R$ and $\lceil p * q \rceil_R \Rightarrow \lceil p \rceil_R * \lceil q \rceil_R$.

Remark. Neither converse implication holds. Obtain a counterexample for the first from p as $\boxed{\mathfrak{t} \mapsto 0} * \mathfrak{x} \mapsto 0 \vee \boxed{\mathfrak{t} \mapsto 1} * \mathfrak{y} \mapsto 0$, q the same but with \mathfrak{x} and \mathfrak{y} swapped, and R as the single action $\mathfrak{t} \mapsto 0 \rightsquigarrow \mathfrak{t} \mapsto 1$. For the second, take p as $\boxed{\exists n. \mathfrak{t} \mapsto n \wedge n < 0}$, q as $\boxed{\exists n. \mathfrak{t} \mapsto n \wedge n > 0}$, and R able to increase \mathfrak{t} 's value.

⁴This approach slightly refines the presentation of stability in [19, Lem. 15], which did not consider such conflicts between shared and local heaps.

The proof rules of RGSep can be adapted to use explicit stabilisation. Figure 9 shows the replacement for RGSep’s frame rule (see Appx. D for the complete set of new rules). The original rule required the frame r (which must not mention any local variables modified by C) to be stable under both R and G in case any shared heap it specifies is mutated by either the environment or C itself. In the new rule, this check becomes an explicit stabilisation on r in the postcondition. As in the SKIP-S rule (Fig. 3), the stabilisation could equally be done in the precondition instead.

$$\text{FRAME-S} \quad \frac{R, G \vdash \{p\} C \{q\} \quad \text{fv}(r) \cap \text{mods}(C) = \emptyset}{R, G \vdash \{p * r\} C \{q * [r]_{RUG}\}}$$

Figure 9: New frame rule

5.3 RGSep and sequential modules

This discussion lays the groundwork for the verification of a memory manager presented in Sect. 6. We shall assume a module comprises some state, including several heap locations, plus a collection of public routines that can manipulate this so-called ‘internal heap’. A sequential module is one designed for single-threaded machines: its routines and all of its clients are sequential.

Sequential modules are analogous to the concurrent programs that RGSep was designed to verify. The RG method, of abstracting a command’s environment by a rely, applies to both, albeit for different reasons. For concurrent programs, we must abstract the concurrently-running threads in order to avoid the combinatorial explosion that results from considering each possible interleaving of commands individually. For sequential modules, we must abstract clients’ actions between module calls because we cannot know what clients will do. To verify sequential modules, we redeploy RGSep’s ‘shared’ and ‘thread-local’ heaps to model the module’s internal heap and, respectively, the heaps of its clients.

Consider a module M with several routines. A client first calls `init()`, which prepares part of M ’s state for this client, and may transfer ownership of some of M ’s heap cells. The return value x identifies subsequent calls in this sequence. The client then invokes some other routines of M – passing x as a parameter each time – before calling `finalise(x)` so that its parts of M ’s state can later be used for another client. We use ‘client’ here to refer to a sequence of calls parameterised on the same x .

The crux is to show that several interleaved clients can all interact with M safely. For instance: if one client executes `x := init()`, then another executes `y := init()` followed by a sequence of calls parameterised on y , can the first client be sure that M is still in a state of readiness for a sequence of calls parameterised on x , and that the intervening events have not affected its part of M ’s state?

This is actually a matter of stability: we are seeking to prove that the postcondition of `x := init()` is stable under an environment that can execute M ’s routines arbitrarily (excepting those parameterised on x). We need only consider an environment that calls M ’s routines: other activities do not affect M ’s internal state, so can be deemed local.

To define such an environment, we require `x := init()` to return a `token(x)` predicate, to reside in the client’s local heap. The predicate is *abstract* [16], which means that its definition is out of scope. Later module calls by this client (which we name C_x) shall require the token’s presence in its local heap, and the `finalise(x)` call shall confiscate it. The postcondition of `x := init()` is thus of the form $\boxed{P(x)} * \text{token}(x)$, where $P(x)$

describes an internal heap with a part initialised for C_x . Let G be the set of RGSep actions by which M 's routines can mutate its internal heap. Alone, $\boxed{P(\mathbf{x})}$ is not stable under G , for G includes actions that mutate C_x 's part of the internal heap. Yet it becomes stable when combined with the local assertion $token(\mathbf{x})$. Why? Because the presence of the $token(\mathbf{x})$ in C_x 's local state prohibits any *other* client having it and thus being able to continue the sequence of calls parameterised on \mathbf{x} . It is vital that our refined notion of stability considers such conflicts between local and shared heaps (Definition 8). Since stability occupies such a central role here, perhaps explicit stabilisation can be usefully applied? It can, in the following two ways.

5.3.1 Clarifying the stable parts of assertions.

We have claimed $\boxed{P(\mathbf{x})} * token(\mathbf{x})$ to be a suitable – and stable – postcondition for `init`. Using explicit stabilisation, we now propose $\lfloor \boxed{P(\mathbf{x})} * token(\mathbf{x}) \rfloor_G$ instead. Strengthening the postcondition in this way *is* sound here, because the stabilisation has no effect on the already-stable assertion. Thus, the presence of $\lfloor - \rfloor$ operators in the postcondition (and, dually, $\lceil - \rceil$ in the precondition) serves to assert that their operands are stable. (In fact, $p \Leftrightarrow \lfloor p \rfloor_R$ exactly characterises those assertions that are stable under R .) We arrive at the following prototype specification:

$$G \vdash \{ \lceil \boxed{P} \rceil_G \} \quad \mathbf{x} := \text{init}() \quad \{ \lfloor \boxed{P(\mathbf{x})} * token(\mathbf{x}) \rfloor_G * Q \}.$$

We omit here and henceforth the rely from specifications, there being only one thread. We retain the guarantee, whose abstraction of the module calls that the thread may make is utilised by the FRAME-S rule. The unparameterised P describes any valid internal heap of the module. See how the assertion Q , which describes cells that are transferred into the client's local heap, can be added outside the stabilised part: a client can mutate this part of the heap without concern for stability, the changes being purely local (see Lem. 9). Not all local changes can be treated so flippantly – indeed, the local assertion $token(\mathbf{x})$ is crucial to stability – but by delimiting the important assertions with the stabilisation syntax, we certify exactly which bits can and cannot be touched. Clients who obey this can be free of stability considerations, and instead rely on general properties of stabilisation, such as those detailed in Sect. 3.1.

5.3.2 Information hiding.

Because the clients need not perform stabilisation, they need not even know the set of actions under which the assertions must be stable. That is, the definition of G can be kept internal to the module. This observation inspires the following proof rule.

$$\text{INFOHIDING} \frac{\begin{array}{l} \text{Module:} \quad (\Delta, G \vdash \{p_i\} \langle C_i \rangle \{q_i\})_{i=0}^n \\ \text{Client:} \quad \Delta' \subseteq \Delta \quad \Delta', (G \vdash \{p_i\} f_i \{q_i\})_{i=0}^n, G \vdash \{p\} C \{q\} \end{array}}{\text{Whole system:} \quad \vdash \{p\} \text{let } (f_i = C_i)_{i=0}^n \text{ in } C \{q\}}$$

The rule concerns a sequential module comprising routines f_1 to f_n with implementations C_1 to C_n . The first line specifies each routine, in which G is the set of actions that clients of the module can perform. (In order to be able to access the module's internal heap, RGSep requires C_i to appear in angled brackets.) Δ denotes a set of predicate

definitions, including the definition of *token* for instance. It also includes the definition of G , which we shall treat as an abstract predicate too. The second line specifies a client of the module, C . The Δ' it uses excludes the definitions of any predicates that are to remain abstract, and crucially, omits G 's definition. Doing so makes the specification more reusable – even in the event that G changes – and hence more conducive to modular reasoning. Explicit stabilisation is vital here: the stabilisation operations in the p_i 's and q_i 's refer to a particular G in the module specifications, and an arbitrary G in the client specification.

Theorem 12. *The INFOHIDING rule is sound.*

Proof. The only departure from a typical rule for `let` commands is to remove G 's definition from the client's specification, which logically strengthens one of the rule's assumptions. \square

6 Case study: Verification of a memory manager

We now reify the concepts of Sect. 5 by verifying the Version 7 Unix memory manager. This illustrates both our extension of explicit stabilisation to RGSep, and the use of the INFOHIDING rule to hide a sequential module's internal interference from its clients. The verification itself is not only believed to be the first for this program; it also reveals a latent bug. The proof is one of safety: we prove neither termination nor that blocks are allocated in any particular fashion.

To begin, consider the following natural specifications, from [16], for `malloc` and `free`. Assume `malloc` cannot fail, and suppose a word is `WORD` bytes long.

$$\{\text{emp}\} \quad x := \text{malloc}(n \times \text{WORD}) \quad \{ \text{token}(x, n) * x \mapsto _ * \dots * x + n - 1 \mapsto _ \}$$

$$\{ \exists n. \text{token}(x, n) * x \mapsto _ * \dots * x + n - 1 \mapsto _ \} \quad \text{free}(x) \quad \{\text{emp}\}$$

The `malloc` routine gives each client an abstract *token* predicate, which the client later uses to certify to `free` that the block being returned was truly allocated by `malloc` (`free`'s behaviour being undefined otherwise). These specifications could be realised naïvely by implementing $\text{token}(x, n)$ as $x - 1 \mapsto n$; that is, by storing the length of each block in the preceding cell.

Real memory managers are far more complex. The one we shall examine forms the cells that precede each block into a monotonically-increasing chain of pointers, linking all the allocated and free blocks. Such a manager must maintain in its internal heap the pointer chain, plus any free blocks, while the allocated blocks are conceptually held by each respective client. For a token, we can now afford only *half* of the cell preceding the block, because the manager must retain at least read-permission on this cell for later traversals of the pointer chain. Note that by creating the token from part of the existing datastructure, our proof avoids the need for auxiliary state.

The crux of the verification is to prove that a block allocated to a client remains allocated until, and only until, that client frees it; that is, it is not invalidated by other calls to `malloc` and `free`. Defining G as the set of actions of `malloc` and `free`, we are asking if `malloc`'s postcondition is stable under G .

$$\begin{array}{l}
G \vdash \{ \llbracket \boxed{\text{arena}} \rrbracket_G \} \quad \mathbf{x} := \text{malloc}(n \times \text{WORD}) \quad \left\{ \begin{array}{l} \llbracket \boxed{\text{arena}} \rrbracket_G * \llbracket \text{arenatoken}(\mathbf{x}, n) \rrbracket_G \\ * \mathbf{x} \mapsto _ * \dots * \mathbf{x} + n - 1 \mapsto _ \end{array} \right\} \\
G \vdash \left\{ \begin{array}{l} \exists n. \llbracket \boxed{\text{arena}} \rrbracket_G * \llbracket \text{arenatoken}(\mathbf{x}, n) \rrbracket_G \\ * \mathbf{x} \mapsto _ * \dots * \mathbf{x} + n - 1 \mapsto _ \end{array} \right\} \quad \mathbf{free}(\mathbf{x}) \quad \{ \llbracket \boxed{\text{arena}} \rrbracket_G \}
\end{array}$$

Figure 10: Specifications of malloc and free

$$\begin{array}{l}
G \vdash \{ \llbracket \boxed{\text{arena}} \rrbracket_G \} \\
\quad \mathbf{x} := \text{malloc}(2 * \text{WORD}); \\
3 \quad \{ \llbracket \boxed{\text{arena}} \rrbracket_G * \llbracket \text{arenatoken}(\mathbf{x}, 2) \rrbracket_G * \mathbf{x} \mapsto _, _ \} \\
\quad \Longrightarrow \{ \llbracket \boxed{\text{arena}} \rrbracket_G * \llbracket \text{arenatoken}(\mathbf{x}, 2) \rrbracket_G * \mathbf{x} \mapsto _, _ \} \\
\quad \mathbf{y} := \text{malloc}(3 * \text{WORD}); \\
6 \quad \{ \llbracket \boxed{\text{arena}} \rrbracket_G * \llbracket \text{arenatoken}(\mathbf{x}, 2) \rrbracket_G * \mathbf{x} \mapsto _, _ * \llbracket \text{arenatoken}(\mathbf{y}, 3) \rrbracket_G * \mathbf{y} \mapsto _, _, _ \} \\
\quad \llbracket \mathbf{y} + 1 \rrbracket := 7; \\
\quad \{ \llbracket \boxed{\text{arena}} \rrbracket_G * \llbracket \text{arenatoken}(\mathbf{x}, 2) \rrbracket_G * \mathbf{x} \mapsto _, _ * \llbracket \text{arenatoken}(\mathbf{y}, 3) \rrbracket_G * \mathbf{y} \mapsto _, 7, _ \} \\
9 \quad \Longrightarrow \{ \llbracket \boxed{\text{arena}} \rrbracket_G * \llbracket \text{arenatoken}(\mathbf{x}, 2) \rrbracket_G * \mathbf{x} \mapsto _, _ * \llbracket \text{arenatoken}(\mathbf{y}, 3) \rrbracket_G * \mathbf{y} \mapsto _, 7, _ \} \\
\quad \mathbf{free}(\mathbf{x}); \\
\quad \{ \llbracket \boxed{\text{arena}} \rrbracket_G * \llbracket \text{arenatoken}(\mathbf{y}, 3) \rrbracket_G * \mathbf{y} \mapsto _, 7, _ \}
\end{array}$$

Figure 11: Verification of a simple client

It is easy to show that it is unaffected when these actions are applied to blocks other than the current one. And although the environment is *allowed* to apply these actions to the current block, it is actually *unable* to do so. Why? Because the current block cannot be accidentally re-allocated, since to do so would give the client a duplicate token, which the $*$ operator forbids. And neither can it be accidentally freed, without yielding its token.

Using explicit stabilisation, here is a first attempt to specify malloc:

$$G \vdash \{ \llbracket \boxed{\text{arena}} \rrbracket_G \} \quad \mathbf{x} := \text{malloc}(n \times \text{WORD}) \quad \left\{ \begin{array}{l} \llbracket \boxed{\text{arena}(\mathbf{x}, n)} * \text{token}(\mathbf{x}, n) \rrbracket_G \\ * \mathbf{x} \mapsto _ * \dots * \mathbf{x} + n - 1 \mapsto _ \end{array} \right\}$$

The *arena* predicate asserts that the manager's internal heap is valid, while *arena*(x, n) additionally asserts that the block at x is missing. Note that the stability of $\llbracket \boxed{\text{arena}(\mathbf{x}, n)} \rrbracket_G$ relies on the *token*(x, n) predicate in the local heap.

This specification exposes too much of the manager's innards. We address this in the improved specifications in Fig. 10, by collapsing $\llbracket \boxed{\text{arena}(\mathbf{x}, n)} * \text{token}(\mathbf{x}, n) \rrbracket_G$ into a single abstract predicate, *arenatoken*(x, n). We also append the $\llbracket \boxed{\text{arena}} \rrbracket_G$ predicate to both malloc's postcondition and free's precondition. Strictly, this is redundant, for $\llbracket \boxed{\text{arena}} \rrbracket_G$ is entailed by *arenatoken*, but having malloc's postcondition reestablish its precondition simplifies the verification of successive calls to malloc and allows the predicates to remain fully abstract.

Now consider the simple client in Fig. 11. Because the *content* of the block lies outside the scope of the stabilisation, the client can mutate it (line 7) without having to reconsider stability. The allocation of the block at \mathbf{y} (line 5) does not affect the block at \mathbf{x} : such a deduction is enabled by the FRAME-S rule of Fig. 9. (Although this rule imposes a

stabilisation on the entire frame, we can leave this implicit for the local parts, by Lem. 9.) See how the use of explicit stabilisation allows the client’s verifier to rely only on general properties of stabilisation: for instance, the deduction of the assertion on line 4 follows straight from $\lfloor p \rfloor_R \Rightarrow p \Rightarrow \lceil p \rceil_R$. The definition of \mathbf{G} is thus not needed by the client, so we can use our INFOHIDING rule to keep it internal to the module.

The rest of this section concerns the implementation (Sect. 6.1) and verification (Sect. 6.2) of the memory manager. The source code and our full proof are provided in Appx. E. We omit an optimisation that tells `malloc` where to begin its search, because it contains a bug, which we explain in Sect. 6.3. Section 6.4 describes some peripheral details of the implementation and the verification.

6.1 Implementation of the memory manager

The memory manager controls the allocation and deallocation of blocks of main memory to and from client processes. The portion of memory it controls (shown in Fig. 12) contains both free and allocated blocks. The grey cells form a cyclic chain of pointers and the white blocks in between can be allocated to clients. Since blocks are word-aligned, the least significant bit in each pointer is redundant, and is hence employed to signal the availability of the following block. In the figure, black and white squares indicate that this so-called ‘busy’ bit is set and, respectively, unset. The module-level variables `s` and `t` respectively identify the first and last pointers in the arena. Because it is not followed by an allocatable block, the last pointer’s busy bit is permanently set.

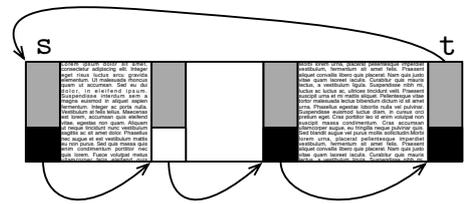


Figure 12: An arena

A client requests a block of n bytes by calling `malloc(n)`. For clarity of exposition we shall keep n a multiple of the word size, `WORD`. The routine traverses pointers until it finds a free block that is sufficiently large, returning the null pointer in the case of failure. It coalesces consecutive free blocks throughout the search. Should the block it finds be exactly the right size, a pointer to it is returned, and should it be too large, it is divided into two and a pointer to the first is returned. The client can later invoke `free(x)`, x being the address of the first cell in the block. Observe that `free` is not parameterised by the length of the block, because the length was recorded when `malloc` allocated it.

6.2 Details of the verification

Figure 13 defines some auxiliary predicates used in the specifications and proof. $x \dashv\vdash y$ describes an unallocated block between x and y . Upon being allocated a block of size n with first cell x , the client is also given $token(x, n)$, which contains a half permission on the block’s pointer; the manager retains the other half. We write $x \mapsto y_{busy}$ to mean that upon unsetting x ’s busy bit, it would contain the address of y . $x \curvearrowright y$ says that y is the special pointer at the end of the arena that points back to x , the start of the arena. $x \twoheadrightarrow y$ denotes a possibly-empty monotonically-increasing chain of pointers from x to y (including any unallocated blocks), the definition of which abbreviates $x \rightarrow x' * x' \rightarrow y$ to $x \rightarrow x' \twoheadrightarrow y$.

$$\begin{aligned}
x \xrightarrow{u} y &\stackrel{\text{def}}{=} x < y \wedge x \mapsto y * (x+1) \mapsto _ * \dots * (y-1) \mapsto _ \\
x \xrightarrow{a} y &\stackrel{\text{def}}{=} x < y \wedge x \xrightarrow{5} y_{\text{busy}} \\
x \rightarrow y &\stackrel{\text{def}}{=} x \xrightarrow{u} y \vee x \xrightarrow{a} y \\
x \curvearrowright y &\stackrel{\text{def}}{=} x < y \wedge y \mapsto x_{\text{busy}} \\
x \rightarrow y &\stackrel{\text{def}}{=} (\exists x'. x \rightarrow x' \rightarrow y) \vee (x = y \wedge \text{emp}) \\
\text{arena} &\stackrel{\text{def}}{=} \mathbf{s} \rightarrow \mathbf{t} * \mathbf{s} \curvearrowright \mathbf{t} \\
\text{arena}(x, n) &\stackrel{\text{def}}{=} \mathbf{s} \rightarrow (x-1) \xrightarrow{a} (x+n) \rightarrow \mathbf{t} * \mathbf{s} \curvearrowright \mathbf{t} \\
\text{token}(x, n) &\stackrel{\text{def}}{=} (x-1) \xrightarrow{5} (x+n)_{\text{busy}} \\
\text{arenatoken}(x, n) &\stackrel{\text{def}}{=} \boxed{\text{arena}(x, n)} * \text{token}(x, n)
\end{aligned}$$

Figure 13: Predicates

Coalesce:	$a \xrightarrow{u} b \xrightarrow{u} c$	\rightsquigarrow	$a \xrightarrow{u} c$		$\mathbf{s} \rightarrow a$
AllocateEntire:	$a \xrightarrow{u} b$	\rightsquigarrow	$a \xrightarrow{a} b$		$\mathbf{s} \rightarrow a$
AllocatePart:	$a \xrightarrow{u} b$	\rightsquigarrow	$a \xrightarrow{a} (b-n) \xrightarrow{u} b$		$\mathbf{s} \rightarrow a$
Free:	$a \xrightarrow{a} b$	\rightsquigarrow	$a \xrightarrow{u} b$		$\mathbf{s} \rightarrow a$

Figure 14: Main actions

Figure 14 formalises the ways in which the internal heap of the module may be mutated by clients calling `malloc` and `free`. Only one routine can execute at once, so it would suffice to list a single action for each. We prefer to split them into several simple actions. The first coalesces two consecutive free blocks. The second allocates an entire block to a client, while the third allocates just the initial part. The fourth frees a block. The context $\mathbf{s} \rightarrow a$ ensures that the blocks that are acted upon are really in the arena. \mathbf{G} is the union of all these actions.

6.3 A (faulty) optimisation

The following bug was discovered during the verification process.

The manager maintains a global variable `p` (named `allocp` in the original source code) that, after a block is allocated, is pointed to the successive block, and after a block is freed, is pointed to that block. It serves to identify a good place for the next call to `malloc` to begin its search. The implementation does not update `p` if allocation fails, however, and therein lies the bug: `p` *should* be updated in case the block to which it points has been coalesced with its predecessor, lest it be left pointing inside a block.

Figure 15 demonstrates how this bug could wreak havoc. Our contrived arena contains just two one-word blocks, both of which are free, and `p` initially points to the second. The first `malloc` call fails, but has the side-effect of leaving `p` inside the coalesced block. We then allocate a small block at `x`, before wrapping around to the start of the arena and allocating a larger block at `y`, thereby reaching a situation in which the contents of the smaller block is

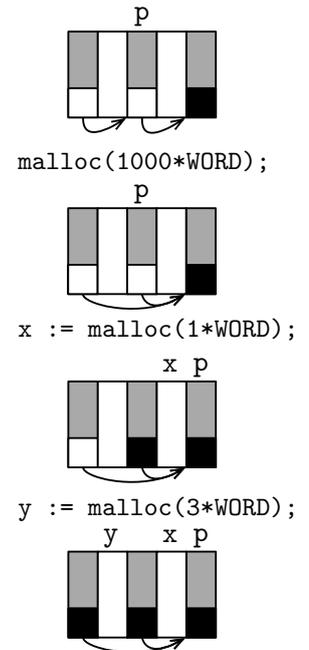


Figure 15: The bug

allocated twice.

The discovery of this bug was prompted by the failure of the invariant $\mathbf{s} \rightarrow \mathbf{p}$, which states that \mathbf{p} identifies a valid pointer in the arena. We have successfully executed our exploit to confirm that the bug is real.

6.4 Other issues

There are several other issues involved in the implementation and verification of the memory manager, which we explain now. These issues have been sidestepped so far in order to focus on the crucial parts of the verification.

Allocation failure To handle the case where `malloc` fails, its postcondition should be disjointed with the following assertion: $\lfloor \boxed{\text{arena}} \rfloor_{\mathbf{G}} \wedge \mathbf{x}=0$.

Extending the arena Once the search for a block has exhausted the arena, `malloc` invokes `sbrk` to ask the system for another block of memory. This block will be located at an address above \mathbf{t} because, in Version 7 Unix, memory allocated via `sbrk` is never returned. The following three actions should be added to \mathbf{G} , to formalise these calls to `sbrk`:⁵

$$\begin{aligned} \text{ExtendGap:} & \quad \&\mathbf{t} \mapsto t * \mathbf{s} \frown t * \text{brk}(b) \wedge b > t + 1 \rightsquigarrow \&\mathbf{t} \mapsto t' * t \xrightarrow{\mathbf{a}} b \xrightarrow{\mathbf{u}} t' * \mathbf{s} \frown t' * \text{brk}(t' + 1) \\ \text{ExtendNoGap:} & \quad \&\mathbf{t} \mapsto t * \mathbf{s} \frown t * \text{brk}(t + 1) \rightsquigarrow \&\mathbf{t} \mapsto t' * t \xrightarrow{\mathbf{u}} (t + 1) \xrightarrow{\mathbf{u}} t' * \mathbf{s} \frown t' * \text{brk}(t' + 1) \\ \text{AdvBreak:} & \quad \text{brk}(b) \rightsquigarrow \exists n > 0. \text{brk}(b + n) \end{aligned}$$

The first extends the arena with a new block, leaving a gap that is filled with an unfreeable dummy block to maintain the illusion of a contiguous arena. The second is similar, but without the gap. The third action, which advances the ‘break value’ (the cell at which the next successful call to `sbrk` will return a block), is kept distinct to reflect that it may be performed in other situations.

An issue with dummy blocks When the arena is extended via the `ExtendGap` action, the resulting gap is filled with a dummy block that is permanently allocated. In order to allocate such a block, we need to hand the caller the *token* predicate, yet there is no client in this situation. We thus add a `true` predicate to the arena, which can ‘soak up’ these spare tokens. Considering this and the previous points, the arena (see Fig. 13) can be more precisely defined as follows:

$$\text{arena} \stackrel{\text{def}}{=} \exists s, t, b. \text{true} * \&\mathbf{s} \mapsto s * \&\mathbf{t} \mapsto t * s \rightarrow t * \mathbf{s} \frown t * \text{brk}(b) \wedge t < b$$

7 Related Work

Explicit stabilisation arose out of ‘mid stability’ [19, §4.1], a variation of RG reasoning that places stability checks not on the pre and postconditions of basic commands, but at the points of sequential and parallel composition instead. This more strategic placement eliminates redundant checks, and also allows libraries comprising just one basic command

⁵We are now treating module-level variables more carefully: the variable \mathbf{t} is modelled as a heap cell at address $\&\mathbf{t}$, thus allowing its value to be altered by these actions.

to be verified without considering stability. Our parametric proof system (Fig. 4) extends this to *all* library functions (and encodes mid stability soundly and completely).

RG-style reasoning has been used before to verify concurrent library code (e.g. [10]). The specifications of that approach involve a particular rely, whereas our parametric specifications do not require a particular rely to be instantiated.

RG has also furnished proofs of sequential modules before (e.g. [22]), but we believe ours to be the first that hides the module’s internal interference. The INFOHIDING rule that enables this feat is related to the hypothetical frame rule [15]: the latter rule hides the module’s *state* from the client, while ours hides the module’s *interference*. Perhaps the hypothetical frame rule could be used to remove the `arena` predicate from the verification given in Fig. 11, thus revealing to the client neither the module’s state nor its internal interference.

SAGL [9], like RGSep, is a descendant of RG and separation logic, to which explicit stabilisation could also be applied. Local Rely-Guarantee (LRG) [8] is a third descendant that addresses an inherent flaw in the modularity of its siblings: that the shared heap must be globally known. It defines a $*$ operator over interference, which allows the shared heap to be split into portions that are shared between just a few threads. The application of explicit stabilisation to LRG could simplify the verification of clients that invoke multiple modules, for our approach currently handles only one.

Explicit stabilisation can be seen as a bridge between theory and implementation: tools, such as SmallfootRG [4], that automate RG-style reasoning may defer stability checks rather than perform them at the point of rule application, and explicit stabilisation can help to formalise this ‘lazy’ approach. We have not considered the implementation of stabilisation; this issue is explored in [2].

8 Conclusion

We have proposed explicit stabilisation as a new way to deal with stability in RG reasoning. The central idea is to record information about an assertion’s stability into its syntactic form. The main benefits are in modular reasoning:

Library code can be verified independently of clients. In Sect. 4, we showed how an approach based upon explicit stabilisation enables RG reasoning to verify concurrent library code. Essentially, the stabilisation in the library’s specification is evaluated so lazily that it actually becomes an obligation of the client.

Client code can be verified independently of a sequential module. We showed in Sect. 5 how the application of explicit stabilisation to RGSep gives rise to an INFOHIDING rule that allows a sequential module to hide its internal interference from its clients. Such information hiding is crucial for modular reasoning, because it allows the specification of a client to be reused, even despite changes to the specification of this internal interference. Section 6 demonstrated this reasoning by verifying a memory manager.

It would be interesting to investigate whether these two forms of modularity can be combined; that is, can we verify both a library and its clients, modularly, at the same time? It looks feasible. The specification for the library in Sect. 4 used explicit stabilisation with an arbitrary rely R , which became specific for each client in turn. Meanwhile,

the specifications for the memory manager in Sect. 6 used explicit stabilisation with the specific G of the module, which was then generalised to an arbitrary G for the clients, so as to provide information hiding. Perhaps a combination of these approaches would parameterise on both the rely and the guarantee?

We also plan to apply explicit stabilisation to more advanced logics based on RG, such as LRG, Deny-Guarantee [7], and the logic of Gotsman et al. for proving liveness [11]. The notions of stability in such logics are becoming ever more demanding, so it is increasingly important to have a solid basis upon which to reason about stability. We believe explicit stabilisation provides such a basis.

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References

- [1] M. Abadi, L. Cardelli, P.-L. Curien, and J.-J. Lévy. Explicit substitutions. In *POPL '90: Proc. 17th symposium on Principles of programming languages*. ACM, 1990.
- [2] H. Amjad and R. Bornat. Towards automatic stability analysis for rely-guarantee proofs. In *VMCAI '09: Proc. 10th International Conference on Verification, Model Checking, and Abstract Interpretation*, volume 5403 of *LNCS*, pages 14–28. Springer, 2009.
- [3] R. Bornat, C. Calcagno, P. O’Hearn, and M. Parkinson. Permission accounting in separation logic. In J. Palsberg and M. Abadi, editors, *POPL '05: Proc. 32nd symposium on Principles of programming languages*, pages 259–270. ACM, 2005.
- [4] C. Calcagno, M. Parkinson, and V. Vafeiadis. Modular safety checking for fine-grained concurrency. In H. R. Nielson and G. Filé, editors, *SAS '07: 14th International Symposium on Static Analysis*, volume 4634 of *LNCS*, pages 233–248. Springer, 2007.
- [5] J. W. Coleman. Expression decomposition in a Rely/Guarantee context. In *VSTTE '08: Verified Software: Theories, Tools, Experiments*, pages 146–160. Springer-Verlag, 2008.
- [6] E. W. Dijkstra. *A Discipline of Programming*. Prentice Hall, Inc., 1976.
- [7] M. Dodds, X. Feng, M. Parkinson, and V. Vafeiadis. Deny-Guarantee reasoning. In G. Castagna, editor, *ESOP '09: Proc. 18th European Symposium on Programming*, volume 5502 of *LNCS*, pages 363–377. Springer, 2009.

- [8] X. Feng. Local rely-guarantee reasoning. In Z. Shao and B. C. Pierce, editors, *POPL '09: Proc. 36th symposium on Principles of programming languages*, pages 315–327. ACM, 2009.
- [9] X. Feng, R. Ferreira, and Z. Shao. On the relationship between concurrent separation logic and assume-guarantee reasoning. In R. D. Nicola, editor, *ESOP '07: Proc. 16th European Symposium on Programming*, volume 4421 of *LNCS*, pages 173–188. Springer, 2007.
- [10] C. Flanagan, S. N. Freund, S. Qadeer, and S. A. Seshia. Modular verification of multithreaded programs. *Theor. Comput. Sci.*, 338(1-3), 2005.
- [11] A. Gotsman, B. Cook, M. Parkinson, and V. Vafeiadis. Proving that non-blocking algorithms don't block. In Z. Shao and B. C. Pierce, editors, *POPL '09: Proc. 36th symposium on Principles of programming languages*, pages 16–28. ACM, 2009.
- [12] C. A. R. Hoare. An axiomatic basis for computer programming. *Commun. ACM*, 12(10), 1969.
- [13] S. S. Ishtiaq and P. W. O'Hearn. BI as an assertion language for mutable data structures. In *POPL '01: Proc. 28th symposium on Principles of programming languages*, pages 14–26. ACM, 2001.
- [14] C. B. Jones. *Development methods for computer programs including a notion of interference*. PhD thesis, University of Oxford, 1981.
- [15] P. W. O'Hearn, H. Yang, and J. C. Reynolds. Separation and information hiding. In N. D. Jones and X. Leroy, editors, *POPL '04: Proc. 31st symposium on Principles of programming languages*, pages 268–280. ACM, 2004.
- [16] M. Parkinson and G. Bierman. Separation logic and abstraction. In J. Palsberg and M. Abadi, editors, *POPL '05: Proc. 32nd symposium on Principles of programming languages*, pages 247–258. ACM, 2005.
- [17] L. Prensa Nieto. The Rely-Guarantee method in Isabelle/HOL. In P. Degano, editor, *ESOP '03: Proc. 12th European Symposium on Programming*, volume 2618 of *LNCS*, pages 348–362. Springer, 2003.
- [18] J. C. Reynolds. Separation logic: A logic for shared mutable data structures. In *LICS '02: Proc. 17th Annual IEEE Symposium on Logic in Computer Science*, pages 55–74. IEEE Computer Society, 2002.
- [19] V. Vafeiadis. *Modular fine-grained concurrency verification*. PhD thesis, University of Cambridge, July 2007.
- [20] V. Vafeiadis and M. Parkinson. A marriage of Rely/Guarantee and separation logic. In L. Caires and V. T. Vasconcelos, editors, *CONCUR '07: 18th international conference on concurrency theory*, volume 4703 of *LNCS*, pages 256–271. Springer, 2007.

- [21] J. Wickerson, M. Dodds, and M. Parkinson. Explicit stabilisation for modular Rely-Guarantee reasoning. In A. D. Gordon, editor, *ESOP '10: Proc. 19th European Symposium on Programming*, volume 6012 of *LNCS*, pages 611–630. Springer-Verlag, 2010.
- [22] G. Yorsh, A. Skidanov, T. Reps, and M. Sagiv. Automatic assume/guarantee reasoning for heap-manipulating programs: Ongoing work. In *AIOOL '05: Proc. 1st International Workshop on Abstract Interpretation of Object-oriented Languages*, volume 131 of *Electr. Notes Theor. Comput. Sci.*, pages 125–138, 2005.

A Semantics of commands

This section provides a formal semantics for the programming language introduced in Sect. 2. The syntax of the language is as follows.

$$C ::= \text{skip} \mid C; C \mid C \parallel C \mid C + C \mid C^+ \mid c$$

Below we show some basic commands and derived commands. Note that since our language does not consider faults, the basic command `assert` writes to the special program variable `fault` instead.

$$\begin{aligned}
x := e &\stackrel{\text{def}}{=} \{(\sigma, \sigma[x \mapsto v]) \mid x \in \text{dom}(\sigma) \wedge v = \llbracket e \rrbracket \sigma\} \\
x++ &\stackrel{\text{def}}{=} x := x+1 \\
\text{lock } \ell &\stackrel{\text{def}}{=} \{(\sigma, \sigma[\ell \mapsto \text{true}]) \mid \sigma(\ell) = \text{false}\} \\
\text{unlock } \ell &\stackrel{\text{def}}{=} \{(\sigma, \sigma[\ell \mapsto \text{false}]) \mid \sigma(\ell) = \text{true}\} \\
\text{assume } b &\stackrel{\text{def}}{=} \{(\sigma, \sigma) \mid \llbracket b \rrbracket \sigma = \text{true}\} \\
\text{assert } b &\stackrel{\text{def}}{=} \{(\sigma, \sigma) \mid \llbracket b \rrbracket \sigma = \text{true}\} \cup \\
&\quad \{(\sigma, \sigma[\text{fault} \mapsto \text{true}]) \mid \llbracket b \rrbracket \sigma = \text{false}\} \\
\text{if } b \text{ then } C_1 \text{ else } C_2 &\stackrel{\text{def}}{=} (\text{assume}(b); C_1) + (\text{assume}(\neg b); C_2) \\
\text{while } b \text{ do } C &\stackrel{\text{def}}{=} (\text{assume}(b); C)^*; \text{assume}(\neg b)
\end{aligned}$$

An operational semantics is given below in terms of an inductively-defined transition relation between configurations. We write $\langle C, \sigma \rangle \xrightarrow[\lambda]{R} \langle C', \sigma' \rangle$ to denote a single step of computation that reduces the command C in state σ to the command C' in state σ' , in the presence of environmental interference R . The label λ indicates whether the step is performed by the program itself (**p**) or the environment (**e**). We write $\xrightarrow{R,*}$ for the reflexive transitive closure of $\xrightarrow[\mathbf{p}]{R} \cup \xrightarrow[\mathbf{e}]{R}$.

$$\begin{array}{c}
\frac{R(\sigma, \sigma')}{\langle C, \sigma \rangle \xrightarrow[\mathbf{e}]{R} \langle C, \sigma' \rangle} \qquad \frac{c(\sigma, \sigma')}{\langle c, \sigma \rangle \xrightarrow[\mathbf{p}]{R} \langle \text{skip}, \sigma' \rangle} \qquad \frac{}{\langle \text{skip}; C, \sigma \rangle \xrightarrow[\mathbf{p}]{R} \langle C, \sigma \rangle} \\
\\
\frac{}{\langle \text{skip} \parallel \text{skip}, \sigma \rangle \xrightarrow[\mathbf{p}]{R} \langle \text{skip}, \sigma \rangle} \qquad \frac{}{\langle C_1 + C_2, \sigma \rangle \xrightarrow[\mathbf{p}]{R} \langle C_1, \sigma \rangle} \qquad \frac{}{\langle C_1 + C_2, \sigma \rangle \xrightarrow[\mathbf{p}]{R} \langle C_2, \sigma \rangle} \\
\\
\frac{}{\langle C^+, \sigma \rangle \xrightarrow[\mathbf{p}]{R} \langle C + (C; C^+), \sigma \rangle} \qquad \frac{\langle C, \sigma \rangle \xrightarrow[\mathbf{p}]{R} \langle C', \sigma' \rangle}{\langle \mathcal{E}[C], \sigma \rangle \xrightarrow[\mathbf{p}]{R} \langle \mathcal{E}[C'], \sigma' \rangle}
\end{array}$$

$$\text{where } \mathcal{E} ::= - \mid \mathcal{E} \parallel C \mid C \parallel \mathcal{E} \mid \mathcal{E}; C$$

B Rely-Guarantee specifications and proof rules

The following definitions provide a formal semantics for RG specifications. Sect. 2 contains further information.

Definition 13 (Meeting the guarantee). $(C, \sigma, R) \text{ guar}_n G$ expresses that no execution of command C , starting in state σ , under environmental interference R , will violate its guarantee G in fewer than n steps.

$$\begin{aligned} (C, \sigma, R) \text{ guar}_0 G &\stackrel{\text{def}}{\iff} \text{true} \\ (C, \sigma, R) \text{ guar}_{n+1} G &\stackrel{\text{def}}{\iff} \forall C', \sigma', \lambda. \text{if } \langle C, \sigma \rangle \xrightarrow{\lambda} \langle C', \sigma' \rangle \\ &\quad \text{then } (C', \sigma', R) \text{ guar}_n G \\ &\quad \text{and } (\lambda = \mathbf{p} \implies G(\sigma, \sigma')) \end{aligned}$$

Definition 14 (Semantics of RG specifications).

$$\begin{aligned} R, G \models \{p\} C \{q\} &\stackrel{\text{def}}{\iff} \forall \sigma, \sigma'. \text{if } \sigma \models p \\ &\quad \text{then } \forall n \geq 0. (C, \sigma, R) \text{ guar}_n G \\ &\quad \text{and } (\langle C, \sigma \rangle \xrightarrow{R, *} \langle \text{skip}, \sigma' \rangle \implies \sigma' \models q) \end{aligned}$$

Here is a complete set of rules for proving RG specifications.

<p style="text-align: center;">DISJ</p> $\frac{R, G \vdash \{p_1\} C \{q\} \quad R, G \vdash \{p_2\} C \{q\}}{R, G \vdash \{p_1 \vee p_2\} C \{q\}}$	<p style="text-align: center;">CONJ</p> $\frac{R, G \vdash \{p\} C \{q_1\} \quad R, G \vdash \{p\} C \{q_2\}}{R, G \vdash \{p\} C \{q_1 \wedge q_2\}}$	<p style="text-align: center;">WEAKEN</p> $\frac{R', G' \vdash \{p'\} C \{q'\} \quad p \Rightarrow p' \quad q' \Rightarrow q \quad R \subseteq R' \quad G' \subseteq G}{R, G \vdash \{p\} C \{q\}}$
<p style="text-align: center;">BASIC</p> $\frac{\vdash \{p\} c \{q\} \quad \overline{p} \cap c \subseteq G \quad p \text{ stab } R \quad q \text{ stab } R}{R, G \vdash \{p\} c \{q\}}$	<p style="text-align: center;">SKIP</p> $\frac{p \text{ stab } R}{R, G \vdash \{p\} \text{skip} \{p\}}$	<p style="text-align: center;">CHOICE</p> $\frac{R, G \vdash \{p\} C_1 \{q\} \quad R, G \vdash \{p\} C_2 \{q\}}{R, G \vdash \{p\} C_1 + C_2 \{q\}}$
<p style="text-align: center;">SEQ</p> $\frac{R, G \vdash \{p\} C_1 \{r\} \quad R, G \vdash \{r\} C_2 \{q\}}{R, G \vdash \{p\} C_1 ; C_2 \{q\}}$	<p style="text-align: center;">LOOP</p> $\frac{R, G \vdash \{p\} C \{p\}}{R, G \vdash \{p\} C^+ \{p\}}$	
<p style="text-align: center;">PAR</p> $\frac{R \cup G_2, G_1 \vdash \{p_1\} C_1 \{q_1\} \quad R \cup G_1, G_2 \vdash \{p_2\} C_2 \{q_2\}}{R, G_1 \cup G_2 \vdash \{p_1 \wedge p_2\} C_1 \parallel C_2 \{q_1 \wedge q_2\}}$		

C Proof rules for parametric specifications

Here is a complete set of rules for proving parametric specifications. Sect. 4 contains further information.

$$\begin{array}{c}
\text{P-DISJ} \\
\frac{\mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}_1\} C \{\mathbf{q}\} \quad \mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}_2\} C \{\mathbf{q}\}}{\mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}_1 \vee \mathbf{p}_2\} C \{\mathbf{q}\}} \\
\\
\text{P-CONJ} \\
\frac{\mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}\} C \{\mathbf{q}_1\} \quad \mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}\} C \{\mathbf{q}_2\}}{\mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}\} C \{\mathbf{q}_1 \wedge \mathbf{q}_2\}} \\
\\
\text{P-WEAKEN} \\
\frac{\mathbb{R}', G' \vdash_{\text{P}} \{\mathbf{p}'\} C \{\mathbf{q}'\} \quad \mathbf{p} \Rightarrow_{\mathbb{R}} \mathbf{p}' \quad \mathbf{q}' \Rightarrow_{\mathbb{R}} \mathbf{q} \quad \mathbb{R} \subseteq \mathbb{R}' \quad G' \subseteq G}{\mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}\} C \{\mathbf{q}\}} \\
\\
\text{P-BASIC} \\
\frac{\vdash \{p\} c \{q\} \quad \overline{p} \cap c \subseteq G}{\mathbb{U}, G \vdash_{\text{P}} \{\lambda R. [p]_R\} c \{\lambda R. [q]_R\}} \\
\\
\text{P-SKIP} \\
\frac{}{\mathbb{U}, G \vdash_{\text{P}} \{\lambda _ . p\} \text{skip} \{\lambda R. [p]_R\}} \\
\\
\text{P-LOOP} \\
\frac{\mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}\} C \{\mathbf{p}\}}{\mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}\} C^+ \{\mathbf{p}\}} \\
\\
\text{P-CHOICE} \\
\frac{\mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}\} C_1 \{\mathbf{q}\} \quad \mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}\} C_2 \{\mathbf{q}\}}{\mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}\} C_1 + C_2 \{\mathbf{q}\}} \\
\\
\text{P-SEQ} \\
\frac{\mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}\} C_1 \{\mathbf{r}\} \quad \mathbb{R}, G \vdash_{\text{P}} \{\mathbf{r}\} C_2 \{\mathbf{q}\}}{\mathbb{R}, G \vdash_{\text{P}} \{\mathbf{p}\} C_1 ; C_2 \{\mathbf{q}\}} \\
\\
\text{P-PAR} \\
\frac{\mathbb{R} \cup G_2, G_1 \vdash_{\text{P}} \{\mathbf{p}_1\} C_1 \{\mathbf{q}_1\} \quad \mathbb{R} \cup G_1, G_2 \vdash_{\text{P}} \{\mathbf{p}_2\} C_2 \{\mathbf{q}_2\}}{\mathbb{R}, G_1 \cup G_2 \vdash_{\text{P}} \{\mathbf{p}_1 \text{ }_{G_2} \parallel_{G_1} \mathbf{p}_2\} C_1 \parallel C_2 \{\mathbf{q}_1 \text{ }_{G_2} \parallel_{G_1} \mathbf{q}_2\}}
\end{array}$$

Abbreviations used in the rules:

$$\begin{array}{lcl}
\mathbf{p}_1 \vee \mathbf{p}_2 & \stackrel{\text{def}}{=} & \lambda R. \mathbf{p}_1(R) \vee \mathbf{p}_2(R) \\
\mathbf{p}_1 \wedge \mathbf{p}_2 & \stackrel{\text{def}}{=} & \lambda R. \mathbf{p}_1(R) \wedge \mathbf{p}_2(R) \\
\mathbf{p}_1 \Rightarrow_{\mathbb{R}} \mathbf{p}_2 & \stackrel{\text{def}}{=} & \forall R \in \mathbb{R}. \mathbf{p}_1(R) \Rightarrow \mathbf{p}_2(R) \\
\mathbf{p}_1 \text{ }_{R_1} \parallel_{R_2} \mathbf{p}_2 & \stackrel{\text{def}}{=} & \lambda R. \mathbf{p}_1(R \cup R_1) \wedge \mathbf{p}_2(R \cup R_2) \\
\mathbb{R} \cup R & \stackrel{\text{def}}{=} & \{R' \cup R \mid R' \in \mathbb{R}\} \\
\mathbb{U} & \stackrel{\text{def}}{=} & \text{universal set of all relies}
\end{array}$$

D RGSep proof rules with explicit stabilisation

Here is a complete set of rules for proving RGSep specifications. Sect. 5.2 contains further information. Basic commands can access only local state. Upon entering an atomic block, the relevant part of the shared state becomes local, which effectively allows basic commands that are enclosed in $\langle - \rangle$ to access any shared state they require.

$$\begin{array}{c}
\text{BASIC} \\
\frac{\vdash \{P\} c \{Q\}}{R, G \vdash \{P\} c \{Q\}} \\
\\
\text{PAR} \\
\frac{R \cup G_2, G_1 \vdash \{p_1\} C_1 \{q_1\} \quad R \cup G_1, G_2 \vdash \{p_2\} C_2 \{q_2\}}{R, G_1 \cup G_2 \vdash \{p_1 * p_2\} C_1 \parallel C_2 \{q_1 * q_2\}} \\
\\
\text{FRAME-S} \\
\frac{R, G \vdash \{p\} C \{q\}}{R, G \vdash \{p * r\} C \{q * [r]_{R \cup G}\}} \\
\\
\text{ATOMR-S} \\
\frac{\emptyset, G \vdash \{p\} \langle C \rangle \{q\}}{R, G \vdash \{[p]_R\} \langle C \rangle \{[q]_R\}} \\
\\
\text{ATOM} \\
\frac{P, Q \text{ precise} \quad \emptyset, \emptyset \vdash \{P * P'\} C \{Q * Q'\} \quad P \rightsquigarrow Q \subseteq G}{\emptyset, G \vdash \{\overline{P * F} * P'\} \langle C \rangle \{\overline{Q * F} * Q'\}}
\end{array}$$

The DISJ, CONJ, WEAKEN, SKIP, CHOICE, SEQ and LOOP rules carry over from Appx. B unchanged. Note that only the FRAME-S and ATOMR-S rules differ from the presentation of RGSep in [19].

E Complete verification of the memory manager

This section provides our proofs of the `malloc` and `free` routines that were discussed in Sect. 6. The source code is available from the Unix Heritage Society.⁶ It has been abridged and corrected: some variables have been renamed, code for initialising the arena and debugging has been omitted, and most significantly, we have removed the optimisation that gives rise to the bug discussed in Sect. 6. Note that in the assertions, \otimes is the iterated version of the $*$ operator, and that henceforth $\lceil - \rceil$ is arithmetic ceiling, not stabilisation. We assume the following sequential specification for `sbrk`; it is a little informal (the shared state ought to be specified in its entirety) but its meaning should be clear.

$$\{\boxed{brk(b)}\} \ x := \text{sbrk}(n) \ \left\{ \begin{array}{l} (\boxed{brk(b+n)} * b \mapsto _ * \dots * (b+n) \mapsto _ \wedge \mathbf{x}=b) \\ \vee (\boxed{brk(b)} \wedge \mathbf{x}=-1 \wedge \mathbf{n} \neq 0) \end{array} \right\}$$

Proof of the malloc routine

```
#define testbusy(p) ((int)(p)&1)
#define setbusy(p) (struct store *)((int)(p)|1)
#define clearbusy(p) (struct store *)((int)(p)&~1)
```

```
struct store {struct store *ptr;};
static struct store *s; //arena start
static struct store *t; //arena top
```

```
\boxed{arena}
\{\exists b. true * s \rightarrow t * s \frown t * brk(b) \wedge t < b\}
char *malloc(unsigned int nbytes) {
  register struct store *p, *q;
  register nw;
  static temp;
  //omitted: code to initialise arena
  nw=(nbytes+WORD+WORD-1)/WORD; //where WORD=sizeof(struct store)
  \{\exists b. true * s \rightarrow t * s \frown t * brk(b) \wedge t < b\} \wedge P_{nw} \} where P_{nw} \stackrel{\text{def}}{=} nw=1 + \lceil \frac{nbytes}{WORD} \rceil
  for(p=s; ; ) {
    \{\exists b. true * s \rightarrow p \rightarrow t * s \frown t * brk(b) \wedge t < b\} \wedge P_{nw} \}
    for(temp=0; ; ) {
      \{\exists b. true * s \rightarrow p \rightarrow t * s \frown t * brk(b) \wedge t < b\} \wedge P_{nw} \}
      if(!testbusy(p->ptr)) {
        \{\exists q, b. true * s \rightarrow p \rightarrow q \rightarrow t * s \frown t * brk(b) \wedge t < b\} \wedge P_{nw} \}
        while(!testbusy((q=p->ptr)->ptr)) {
```

⁶<http://minnie.tuhs.org/UnixTree/V7/usr/src/libc/gen/malloc.c.html>

```

    { $\exists r, b. \text{true} * s \rightarrow p \rightarrow q \rightarrow r \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw}$ 
    p->ptr = q->ptr; //coalesce consecutive free blocks
    { $\exists r, b. \text{true} * s \rightarrow p \rightarrow r \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw}$ 
}
{ $\exists b. \text{true} * s \rightarrow p \rightarrow q \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw}$ 
if (q>=p+nw && p+nw>=p) {
    { $\exists b. \text{true} * s \rightarrow p \rightarrow q \rightarrow t * s \frown t \wedge \text{brk}(b) \wedge t < b$ }
     $\wedge P_{nw} \wedge q \geq p+nw \wedge p+nw \geq p$ 
}
    goto found;
}
}
//p's block is either allocated or too small
{ $\exists b. \text{true} * s \rightarrow p \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw}$ 
q = p;
{ $\exists b. \text{true} * s \rightarrow q \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw} \wedge q=p$ }
p = clearbusy(p->ptr);
{ $\exists b. \text{true} * s \rightarrow q \rightarrow p \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw}$ 
{ $\vee \exists b. \text{true} * s \rightarrow q * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw} \wedge q=t \wedge p=s$ 
}
if (p>q) {
    { $\exists b. \text{true} * s \rightarrow q \rightarrow p \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw}$ 
} else if (q!=t || p!=s) {
    { $\exists b. \text{true} * s \rightarrow q * s \frown t * \text{brk}(b) \wedge t < b$ }
     $\wedge P_{nw} \wedge q=t \wedge p=s \wedge (q \neq t \vee p \neq s)$ 
}
    return 0;
    {false}
} else if (++temp>1) {
    { $\exists b. \text{true} * s \rightarrow q * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw} \wedge q=t \wedge p=s$ 
}
    break;
}
{ $\exists b. \text{true} * s \rightarrow p \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw}$ 
}
{ $\exists b. \text{true} * s \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw}$ 
temp = ((nw+BLOCK/WORD)/(BLOCK/WORD))*(BLOCK/WORD);
//where BLOCK defaults to 1024
{ $\exists b. \text{true} * s \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }
 $\wedge P_{nw} \wedge P_{temp}$  } where  $P_{temp} \stackrel{\text{def}}{=} temp > nw$ 
q = (struct store *)sbrk(0);
{ $\exists b. \text{true} * s \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw} \wedge P_{temp} \wedge q=b$ 
}
if (q+temp < q) {
    return 0;
    { $\exists b. \text{true} * s \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge ret=0$ 
}
}
{ $\exists b. \text{true} * s \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b$ }  $\wedge P_{nw} \wedge P_{temp} \wedge q=b$ 
}
q = (struct store *)sbrk(temp * WORD);

```

$$\left\{ \begin{array}{l} \exists b. \text{true} * s \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b \wedge P_{\text{nw}} \wedge P_{\text{temp}} \wedge q = -1 \\ \vee \exists b. \left[\begin{array}{l} \text{true} * s \rightarrow t * s \frown t * \otimes_{0 \leq i < \text{temp}} (b+i) \mapsto _ \\ * \text{brk}(b + \text{temp}) \wedge t < b \end{array} \right] \wedge P_{\text{nw}} \wedge P_{\text{temp}} \wedge q = b \end{array} \right\}$$

if((INT)q == -1) {

$$\left\{ \exists b. \text{true} * s \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b \wedge P_{\text{nw}} \wedge P_{\text{temp}} \wedge q = -1 \right\}$$

return 0;

$$\left\{ \exists b. \text{true} * s \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b \wedge \text{ret} = 0 \right\}$$

}

$$\left\{ \begin{array}{l} \text{true} * s \rightarrow t * s \frown t * \otimes_{0 \leq i < \text{temp}} (q+i) \mapsto _ \\ * \text{brk}(q + \text{temp}) \wedge t < q \end{array} \wedge P_{\text{nw}} \wedge P_{\text{temp}} \right\}$$

t->ptr = q;

$$\left\{ \begin{array}{l} \text{true} * s \rightarrow t * t \mapsto q * \otimes_{0 \leq i < \text{temp}} (q+i) \mapsto _ \\ * \text{brk}(q + \text{temp}) \wedge t < q \end{array} \wedge P_{\text{nw}} \wedge P_{\text{temp}} \right\}$$

if(q != t+1) {

$$\left\{ \begin{array}{l} \text{true} * s \rightarrow t * t \mapsto q * \otimes_{0 \leq i < \text{temp}} (q+i) \mapsto _ \\ * \text{brk}(q + \text{temp}) \wedge t+1 < q \end{array} \wedge P_{\text{nw}} \wedge P_{\text{temp}} \right\}$$

t->ptr = setbusy(t->ptr);

$$\left\{ \begin{array}{l} \text{true} * s \rightarrow t * t \mapsto q_{\text{busy}} * \otimes_{0 \leq i < \text{temp}} (q+i) \mapsto _ \\ * \text{brk}(q + \text{temp}) \wedge t+1 < q \end{array} \wedge P_{\text{nw}} \wedge P_{\text{temp}} \right\}$$

//lose half a pointer here - 'true' absorbs it

$$\left\{ \begin{array}{l} \text{true} * s \rightarrow t \xrightarrow{a} q * \otimes_{0 \leq i < \text{temp}} (q+i) \mapsto _ \\ * \text{brk}(q + \text{temp}) \wedge t+1 < q \end{array} \wedge P_{\text{nw}} \wedge P_{\text{temp}} \right\}$$

}

$$\left\{ \text{true} * s \rightarrow t \rightarrow q * \otimes_{0 \leq i < \text{temp}} (q+i) \mapsto _ * \text{brk}(q + \text{temp}) \wedge P_{\text{nw}} \wedge P_{\text{temp}} \right\}$$

t = q->ptr = q+temp-1;

$$\left\{ \exists u. \text{true} * s \rightarrow u \rightarrow q * q \mapsto t * \otimes_{q < i < t} i \mapsto _ * \text{brk}(t+1) \wedge q < t \wedge P_{\text{nw}} \right\}$$

//make new unallocated block

$$\left\{ \exists u. \text{true} * s \rightarrow u \rightarrow q \xrightarrow{u} t * \text{brk}(t+1) * t \mapsto _ \wedge P_{\text{nw}} \right\}$$

t->ptr = setbusy(s);

$$\left\{ \exists u. \text{true} * s \rightarrow u \rightarrow q \xrightarrow{u} t * s \frown t * \text{brk}(t+1) \wedge P_{\text{nw}} \right\}$$

//restore loop invariant

$$\left\{ \exists b. \text{true} * s \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b \wedge P_{\text{nw}} \right\}$$

}

found:

$$\left\{ \exists b. \text{true} * s \rightarrow p \xrightarrow{u} q \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b \wedge P_{\text{nw}} \wedge q \geq p + \text{nw} \wedge p + \text{nw} \geq p \right\}$$

if(q > p+nw) {

(p+nw)->ptr = p->ptr;

$$\left\{ \begin{array}{l} \exists b. \text{true} * s \rightarrow p * p \mapsto q * \otimes_{0 \leq i < \text{nw}} (p+i) \mapsto _ \\ * (p + \text{nw}) \xrightarrow{u} q \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b \end{array} \wedge P_{\text{nw}} \wedge q > p + \text{nw} \wedge p + \text{nw} \geq p \right\}$$

$$\left\{ \begin{array}{l} \exists b. \text{true} * s \rightarrow p * p \mapsto q * \otimes_{0 \leq i < \text{nw}} (p+i) \mapsto _ \\ * (p + \text{nw}) \rightarrow t * s \frown t * \text{brk}(b) \wedge t < b \end{array} \wedge P_{\text{nw}} \wedge q > p + \text{nw} \wedge p + \text{nw} \geq p \right\}$$

}

p->ptr = setbusy(p+nw);

$$\left\{ \begin{array}{l} \boxed{\exists b. \text{true} * s \rightarrow p \xrightarrow{a} (p+nw) \rightarrow t * s \frown t * brk(b) \wedge t < b} \\ * \otimes_{0 < i < nw} (p+i) \mapsto _ * p \xrightarrow{5} (p+nw)_{\text{busy}} \wedge P_{nw} \end{array} \right\}$$

return((char *) (p+1));

$$\left\{ \begin{array}{l} \boxed{\exists p, nw. \exists b. \text{true} * s \rightarrow p \xrightarrow{a} (p+nw) \rightarrow t * s \frown t * brk(b) \wedge t < b} \\ * \otimes_{0 < i < nw} (p+i) \mapsto _ * p \xrightarrow{5} (p+nw)_{\text{busy}} \\ \wedge (nw-1) = \lceil \text{nbytes/WORD} \rceil \wedge \text{ret} = p+1 \end{array} \right\}$$

}

$$\left\{ \begin{array}{l} \boxed{\exists p, nw. \exists b. \text{true} * s \rightarrow p \xrightarrow{a} (p+nw) \rightarrow t * s \frown t * brk(b) \wedge t < b} \\ * \otimes_{0 < i < nw} (p+i) \mapsto _ * p \xrightarrow{5} (p+nw)_{\text{busy}} \\ \wedge (nw-1) = \lceil \text{nbytes/WORD} \rceil \wedge \text{ret} = p+1 \\ \vee \boxed{\exists b. \text{true} * s \rightarrow t * s \frown t * brk(b) \wedge t < b} \wedge \text{ret} = 0 \end{array} \right\}$$

$$\left\{ \left(\boxed{\text{arena}} * \text{arenatoken}(\text{ret}, \lceil \text{nbytes/WORD} \rceil) \right) \vee \boxed{\text{arena}} \wedge \text{ret} = 0 \right\}$$

$$\left\{ * \text{ret} \mapsto _ * \dots * \text{ret} + \lceil \text{nbytes/WORD} \rceil - 1 \mapsto _ \right\}$$

Proof of the free routine

$$\left\{ \begin{array}{l} \boxed{\exists nw. \text{arena}} * \text{arenatoken}(\text{ap}, nw) * \otimes_{0 \leq i < nw} (\text{ap}+i) \mapsto _ \\ \left\{ \begin{array}{l} \boxed{\exists p, q, nw. \exists b. \text{true} * s \rightarrow p \xrightarrow{a} q \rightarrow t * s \frown t * brk(b) \wedge t < b} \\ * \otimes_{0 \leq i < nw} (\text{ap}+i) \mapsto _ * p \xrightarrow{5} q_{\text{busy}} \wedge p = \text{ap} - 1 \wedge q = \text{ap} + nw \end{array} \right\} \end{array} \right\}$$

free(register char *ap) {

 register struct store *p = (struct store *)ap;

 --p;

$$\left\{ \begin{array}{l} \boxed{\exists q, nw. \exists b. \text{true} * s \rightarrow p \xrightarrow{a} q \rightarrow t * s \frown t * brk(b) \wedge t < b} \\ * \otimes_{0 < i < nw} (p+i) \mapsto _ * p \xrightarrow{5} q_{\text{busy}} \wedge q = p + nw + 1 \end{array} \right\}$$

$$\left\{ \begin{array}{l} \boxed{\exists q, b. \text{true} * s \rightarrow p * p \mapsto q_{\text{busy}} * \otimes_{p < i < q} i \mapsto _} \\ * q \rightarrow t * s \frown t * brk(b) \wedge t < b \wedge p < q \end{array} \right\}$$

 p->ptr = clearbusy(p->ptr);

$$\left\{ \begin{array}{l} \boxed{\exists q, b. \text{true} * s \rightarrow p * p \mapsto q * \otimes_{p < i < q} i \mapsto _} \\ * q \rightarrow t * s \frown t * brk(b) \wedge t < b \wedge p < q \end{array} \right\}$$

$$\left\{ \boxed{\exists q, b. \text{true} * s \rightarrow p \xrightarrow{a} q \rightarrow t * s \frown t * brk(b) \wedge t < b} \right\}$$

}

$$\left\{ \boxed{\exists b. \text{true} * s \rightarrow t * s \frown t * brk(b) \wedge t < b} \right\}$$

$$\left\{ \boxed{\text{arena}} \right\}$$