2003 Paper 5 Question 2

Computer Design

(*a*) Name and describe *three* reasons for a cache miss.   [6 marks]

(*b*) For *each* reason, suggest a technique for reducing the number of misses.
                                                                      [6 marks]

(*c*) Why might it be advantageous to use a set-associative cache instead of a fully
       associative one?   [4 marks]

(*d*) Describe *two* techniques for reducing the miss penalty.   [4 marks]