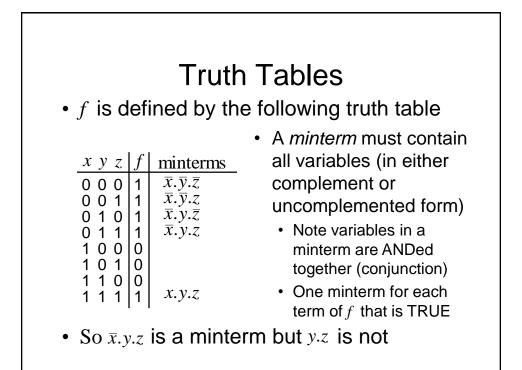
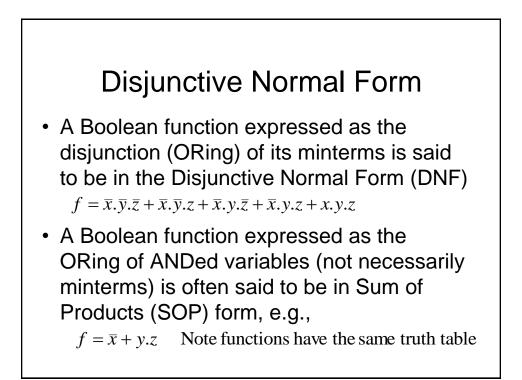
Digital Electronics: Combinational Logic

Logic Minimisation

Introduction

- Any Boolean function can be implemented directly using combinational logic (gates)
- However, simplifying the Boolean function will enable the number of gates required to be reduced. Techniques available include:
 - Algebraic manipulation (as seen in examples)
 - Karnaugh (K) mapping (a visual approach)
 - Tabular approaches (usually implemented by computer, e.g., Quine-McCluskey)
- K mapping is the preferred technique for up to about 5 variables





Maxterms

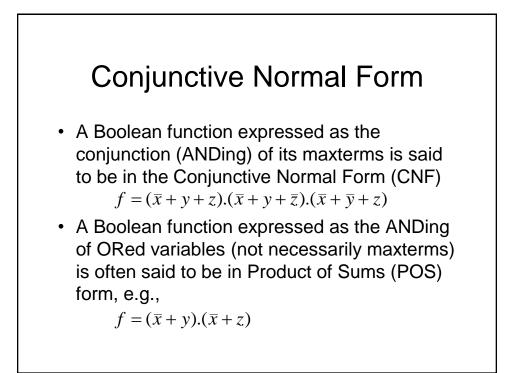
- A maxterm of *n* Boolean variables is the disjunction (ORing) of all the variables either in complemented or uncomplemented form.
 - Referring back to the truth table for *f*, we can write,

$$\overline{f} = x.\overline{y}.\overline{z} + x.\overline{y}.z + x.y.\overline{z}$$

Applying De Morgan (and complementing) gives

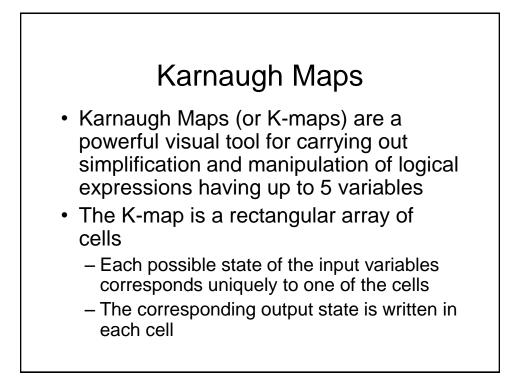
$$f = (\overline{x} + y + z).(\overline{x} + y + \overline{z}).(\overline{x} + \overline{y} + z)$$

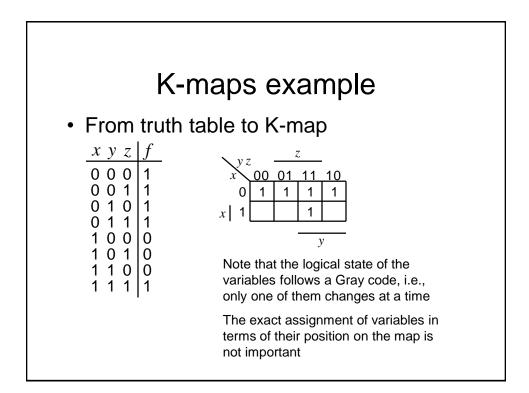
So it can be seen that the maxterms of f are effectively the minterms of \bar{f} with each variable complemented

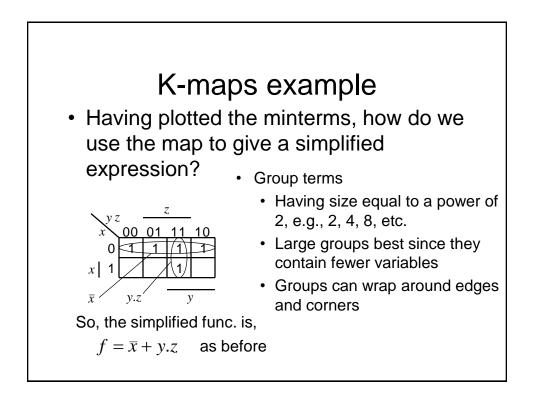


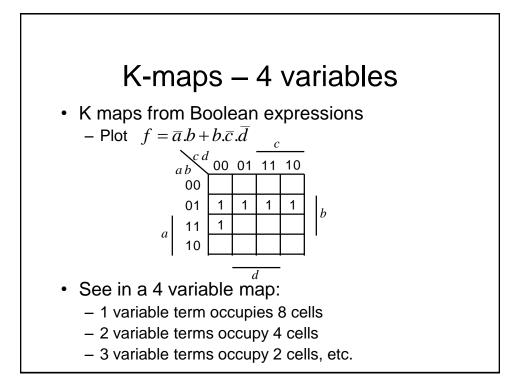
Logic Simplification

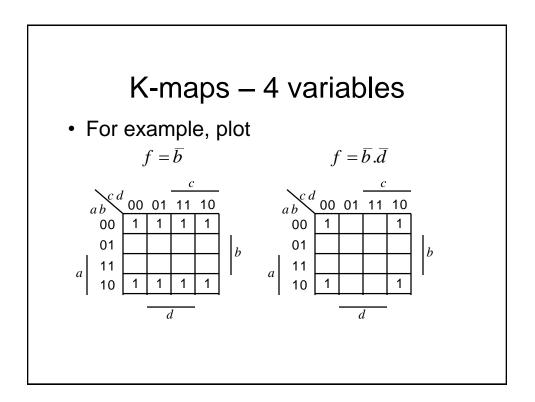
- As we have seen previously, Boolean algebra can be used to simplify logical expressions. This results in easier implementation Note: The DNF and CNF forms are not
- simplified.
 However, it is often easier to use a technique known as Karnaugh mapping

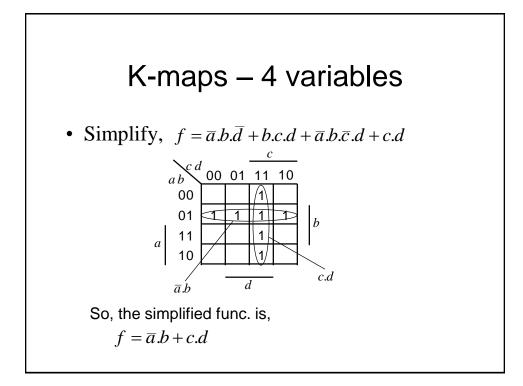


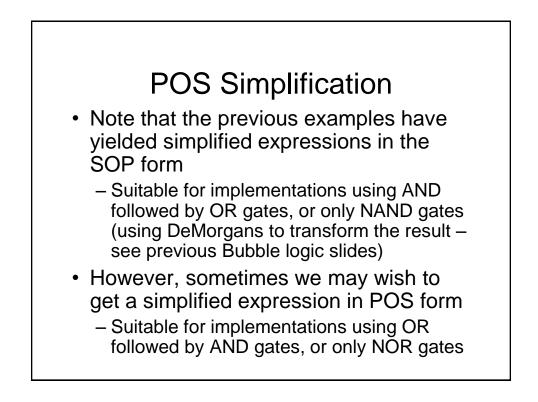


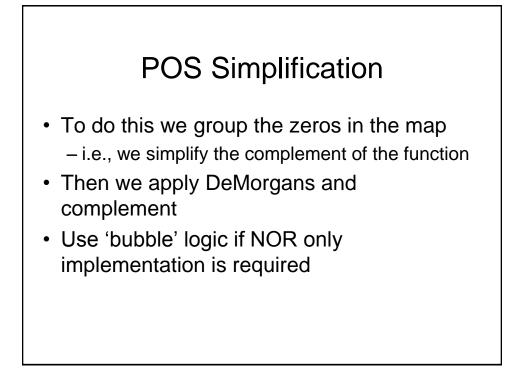


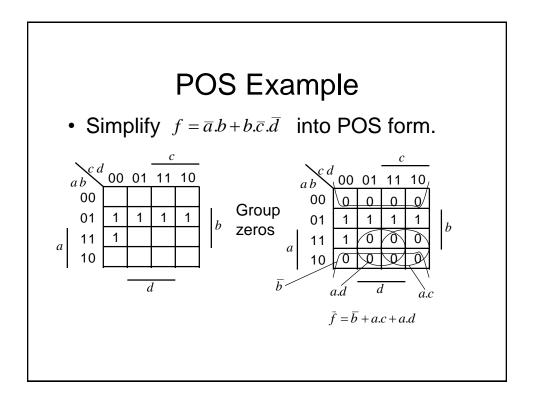


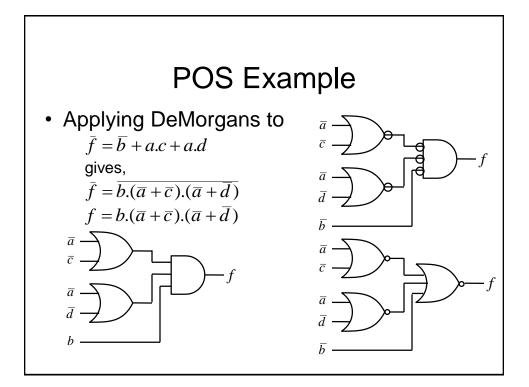


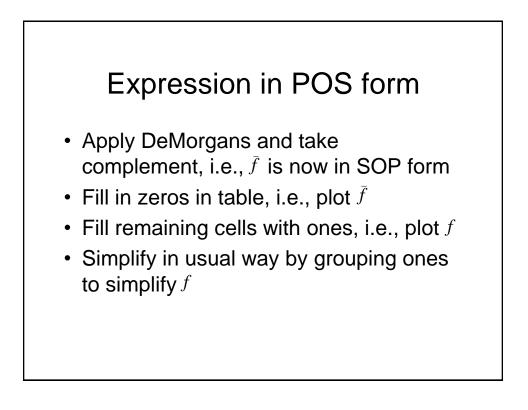






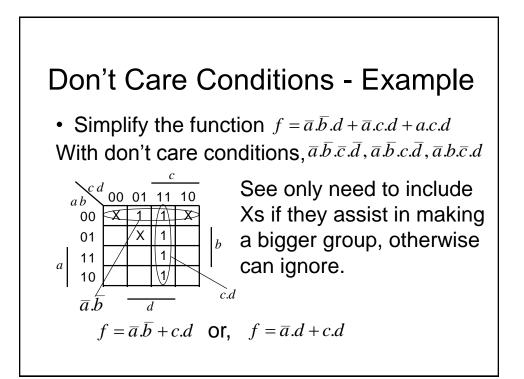


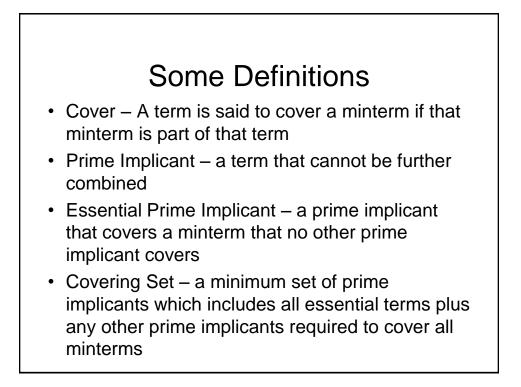


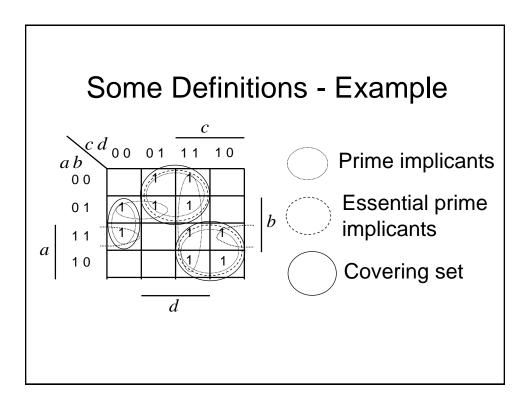


Don't Care Conditions

- Sometimes we do not care about the output value of a combinational logic circuit, i.e., if certain input combinations can never occur, then these are known as *don't care conditions*.
- In any simplification they may be treated as 0 or 1, depending upon which gives the simplest result.
 - For example, in a K-map they are entered as Xs

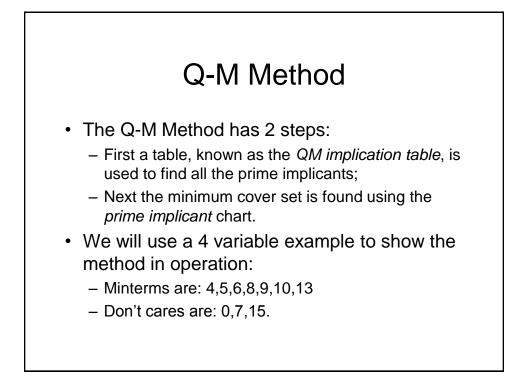






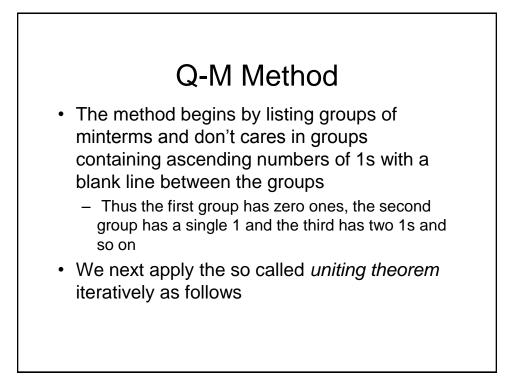
Tabular Simplification

- Except in special cases or for sparse truth tables, the K-map method is not practical beyond 6 variables
- A systematic approach known as the *Quine-McCluskey (Q-M) Method* finds the minimised representation of any Boolean expression
- It is a tabular method that ensures all the prime implicants are found and can be automated for use on a computer



Q-M Method

- The first step is to list all the minterms and don't cares in terms of their minterm indices represented as a binary number
 - Note the entries are grouped according to the number of 1s in the binary representation
 - The 1st column contains the minterms
 - After applying the method, the 2nd column will contain 3 variable terms. Similarly for subsequent columns.



Q-M Method – Uniting Theorem

- Compare elements in the 1st group (no 1s) with all elements in the 2nd group. If they differ by a single bit, it means the terms are adjacent (think K-map)
- Adjacent terms are placed in the 2nd column with the single bit that differs replaced by a dash (-). Terms in the 1st column that contribute to a term in the second are *ticked*, i.e., they are *not* prime implicants.
- Now repeat for the groups in the 2nd column
- As before groups must differ only by a single bit but they must also have a – in the same position
- Groups in 2nd column that do not contribute to the 3rd column are marked with an asterix (*), i.e., they are prime implicants

