II. Matrix Multiplication

Thomas Sauerwald

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UNIVERSITY OF CAMBRIDGE

Outline

Introduction

Serial Matrix Multiplication

Digression: Multithreading

Multithreaded Matrix Multiplication

Matrix Multiplication

Remember: If $A = (a_{ij})$ and $B = (b_{ij})$ are square $n \times n$ matrices, then the matrix product $C = A \cdot B$ is defined by

$$c_{ij} = \sum_{k=1}^{n} a_{ik} \cdot b_{kj} \qquad \forall i, j = 1, 2, \ldots, n.$$

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SQUARE-MATRIX-MULTIPLY(A, B)
n = A.rows
let C be a new n × n matrix
for i = 1 to n
    for j = 1 to n
        c_ij = 0
        for k = 1 to n
            c_ij = c_ij + a_ik · b_kj
return C
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$\boxed{\text{Square-Matrix-Multiply}(A, B) \text{ takes time } \Theta(n^3).}$

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$$c_{ij} = \sum_{k=1}^{n} a_{ik} \cdot b_{kj} \qquad \forall i, j = 1, 2, \ldots, n.$$

This definition suggests that $n^2 \cdot n = n^3$ arithmetic operations are necessary.

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SQUARE-MATRIX-MULTIPLY(A, B) takes time $\Theta(n^3)$.

Outline

Divide & Conquer: First Approach

Assumption: *n* is always an exact power of 2.

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Partition A, B, and C into four $n/2 \times n/2$ matrices:

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Partition A, B, and C into four $n/2 \times n/2$ matrices:

$$A = \begin{pmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{pmatrix}, \quad B = \begin{pmatrix} B_{11} & B_{12} \\ B_{21} & B_{22} \end{pmatrix}, \quad C = \begin{pmatrix} C_{11} & C_{12} \\ C_{21} & C_{22} \end{pmatrix}.$$

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Hence the equation $C = A \cdot B$ becomes:

$$\begin{pmatrix} C_{11} & C_{12} \\ C_{21} & C_{22} \end{pmatrix} = \begin{pmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{pmatrix} \cdot \begin{pmatrix} B_{11} & B_{12} \\ B_{21} & B_{22} \end{pmatrix}$$

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This corresponds to the four equations:

$$\begin{aligned} C_{11} &= A_{11} \cdot B_{11} + A_{12} \cdot B_{21} \\ C_{12} &= A_{11} \cdot B_{12} + A_{12} \cdot B_{22} \\ C_{21} &= A_{21} \cdot B_{11} + A_{22} \cdot B_{21} \\ C_{22} &= A_{21} \cdot B_{12} + A_{22} \cdot B_{22} \end{aligned}$$

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Partition A, B, and C into four $n/2 \times n/2$ matrices:

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Hence the equation $C = A \cdot B$ becomes:

$$\begin{pmatrix} C_{11} & C_{12} \\ C_{21} & C_{22} \end{pmatrix} = \begin{pmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{pmatrix} \cdot \begin{pmatrix} B_{11} & B_{12} \\ B_{21} & B_{22} \end{pmatrix}$$

This corresponds to the four equations:

$$\begin{aligned} C_{11} &= A_{11} \cdot B_{11} + A_{12} \cdot B_{21} \\ C_{12} &= A_{11} \cdot B_{12} + A_{12} \cdot B_{22} \\ C_{21} &= A_{21} \cdot B_{11} + A_{22} \cdot B_{21} \\ C_{22} &= A_{21} \cdot B_{12} + A_{22} \cdot B_{22} \end{aligned}$$

Each equation specifies two multiplications of $n/2 \times n/2$ matrices and the addition of their products.

Divide & Conquer: First Approach (Pseudocode)

$$C_{11} = A_{11} \cdot B_{11} + A_{12} \cdot B_{21}$$
$$C_{12} = A_{11} \cdot B_{12} + A_{12} \cdot B_{22}$$
$$C_{21} = A_{21} \cdot B_{11} + A_{22} \cdot B_{21}$$
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Divide & Conquer: First Approach (Pseudocode)

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SQUARE-MATRIX-MULTIPLY-RECURSIVE(A, B)
n = A.rows
let C be a new n × n matrix
if n == 1
    c11 = a11 · b11
else partition A, B, and C as in equations (4.9)
    C11 = SQUARE-MATRIX-MULTIPLY-RECURSIVE(A11, B11)
          + SQUARE-MATRIX-MULTIPLY-RECURSIVE(A12, B21)
    C12 = SQUARE-MATRIX-MULTIPLY-RECURSIVE(A11, B12)
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$$C_{11} = A_{11} \cdot B_{11} + A_{12} \cdot B_{21}$$
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Divide & Conquer: First Approach (Pseudocode)

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SQUARE-MATRIX-MULTIPLY-RECURSIVE(A, B)
1  n = A.rows
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6      C11 = SQUARE-MATRIX-MULTIPLY-RECURSIVE(A11, B11)
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10 return C
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Line 5: Handle submatrices implicitly through index calculations instead of creating them.

$$C_{11} = A_{11} \cdot B_{11} + A_{12} \cdot B_{21}$$
$$C_{12} = A_{11} \cdot B_{12} + A_{12} \cdot B_{22}$$
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Divide & Conquer: First Approach (Pseudocode)

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Let $T(n)$ be the runtime of this procedure.

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Let $T(n)$ be the runtime of this procedure. Then:

$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1, \\ & \text{if } n > 1. \end{cases}$$

Divide & Conquer: First Approach (Pseudocode)

SQUARE-MATRIX-MULTIPLY-RECURSIVE(A, B)

$n = A.rows$
let C be a new $n \times n$ matrix
if $n == 1$
    $c_{11} = a_{11} \cdot b_{11}$
else partition A, B, and C as in equations (4.9)
    C_{11} = SQUARE-MATRIX-MULTIPLY-RECURSIVE(A_{11}, B_{11})
        + SQUARE-MATRIX-MULTIPLY-RECURSIVE(A_{12}, B_{21})
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    C_{22} = SQUARE-MATRIX-MULTIPLY-RECURSIVE(A_{21}, B_{12})
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8 Multiplications

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    $C_{22} =$ SQUARE-MATRIX-MULTIPLY-RECURSIVE(A_{21}, B_{12})
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return C

Let $T(n)$ be the runtime of this procedure. Then:

$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1, \\ 8 \cdot T(n/2) & \text{if } n > 1. \end{cases}$$

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SQUARE-MATRIX-MULTIPLY-RECURSIVE(A, B)
n = A.rows
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4 Additions and Partitioning

Divide & Conquer: First Approach (Pseudocode)

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return C

Let $T(n)$ be the runtime of this procedure. Then:

$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1, \\ 8 \cdot T(n/2) + \Theta(n^2) & \text{if } n > 1. \end{cases}$$

8 Multiplications

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Solution: $T(n) =$

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Solution: $T(n) = \Theta(8^{\log_2 n})$

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Solution: $T(n) = \Theta(8^{\log_2 n}) = \Theta(n^3)$

No improvement over the naive algorithm!

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Solution: $T(n) = \Theta(8^{\log_2 n}) = \Theta(n^3)$

Goal: Reduce the number of multiplications

Divide & Conquer: Second Approach

Idea: Make the recursion tree less bushy by performing only **7** recursive multiplications of $n/2 \times n/2$ matrices.

Divide & Conquer: Second Approach

Idea: Make the recursion tree less bushy by performing only **7** recursive multiplications of $n/2 \times n/2$ matrices.

Strassen's Algorithm (1969)

1. Partition each of the matrices into four $n/2 \times n/2$ submatrices
2. Create 10 matrices $S_1, S_2, \ldots, S_{10}$. Each is $n/2 \times n/2$ and is the sum or difference of two matrices created in the previous step.
3. Recursively compute 7 matrix products $P_1, P_2, \ldots, P_7$, each $n/2 \times n/2$
4. Compute $n/2 \times n/2$ submatrices of C by adding and subtracting various combinations of the P_i.

Divide & Conquer: Second Approach

Idea: Make the recursion tree less bushy by performing only **7** recursive multiplications of $n/2 \times n/2$ matrices.

Strassen's Algorithm (1969)

1. Partition each of the matrices into four $n/2 \times n/2$ submatrices
2. Create 10 matrices $S_1, S_2, \dots, S_{10}$. Each is $n/2 \times n/2$ and is the sum or difference of two matrices created in the previous step.
3. Recursively compute 7 matrix products $P_1, P_2, \dots, P_7$, each $n/2 \times n/2$
4. Compute $n/2 \times n/2$ submatrices of C by adding and subtracting various combinations of the P_i.

Time for steps 1,2,4: $\Theta(n^2)$, hence $T(n) = 7 \cdot T(n/2) + \Theta(n^2) \Rightarrow T(n) = \Theta(n^{\log 7})$.

Solving the Recursion

$T(n) = 7 \cdot T(n/2) + c \cdot n^2$

Details of Strassen's Algorithm

The 10 Submatrices and 7 Products

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\begin{aligned}
P_1 &= A_{11} \cdot S_1 = A_{11} \cdot (B_{12} - B_{22}) \\
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Claim

$$\begin{pmatrix} A_{11}B_{11} + A_{12}B_{21} & A_{11}B_{12} + A_{12}B_{21} \\ A_{21}B_{11} + A_{22}B_{21} & A_{21}B_{12} + A_{22}B_{22} \end{pmatrix} = \begin{pmatrix} P_5 + P_4 - P_2 + P_6 & P_1 + P_2 \\ P_3 + P_4 & P_5 + P_1 - P_3 - P_7 \end{pmatrix}$$

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Other three blocks can be verified similarly.

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□

Current State-of-the-Art

Open Problem: Is there an algorithm with quadratic complexity?

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Asymptotic Complexities:

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- $O(n^{2.517})$, Romani (1982)
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Outline

Memory Models

Distributed Memory

- Each processor has its private memory
- Access to memory of another processor via messages

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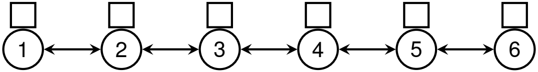

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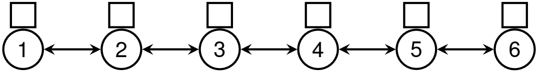

Shared Memory

- Central location of memory
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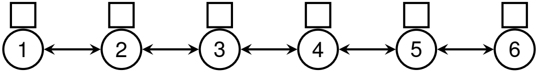

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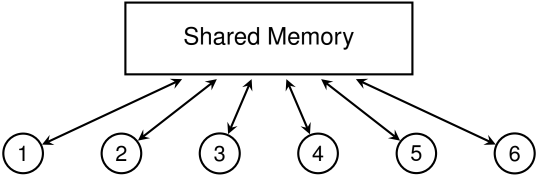

Dynamic Multithreading

- Programming shared-memory parallel computer difficult

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- Use concurrency platform which coordinates all resources

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Scheduling jobs, communication protocols, load balancing etc.

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Functionalities:

Dynamic Multithreading

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- `spawn`

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  - (optional) prefix to a procedure call statement
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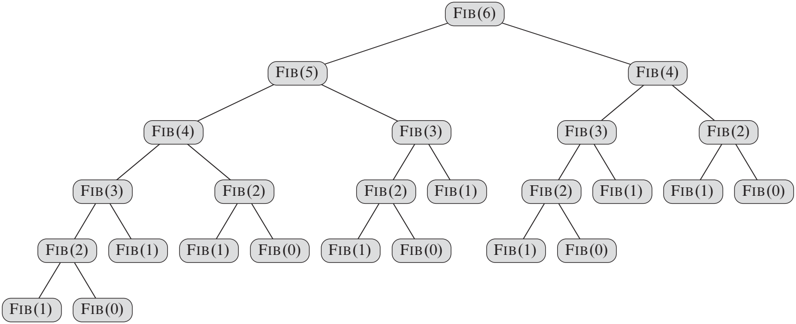
  - each iteration is called in its own thread

Only logical parallelism, but not actual!
Need a scheduler to map threads to processors.

Computing Fibonacci Numbers Recursively (Fig. 27.1)

```
FIB(n)
  if n<=1 return n
  else x=FIB(n-1)
       y=FIB(n-2)
       return x+y
```

Computing Fibonacci Numbers Recursively (Fig. 27.1)

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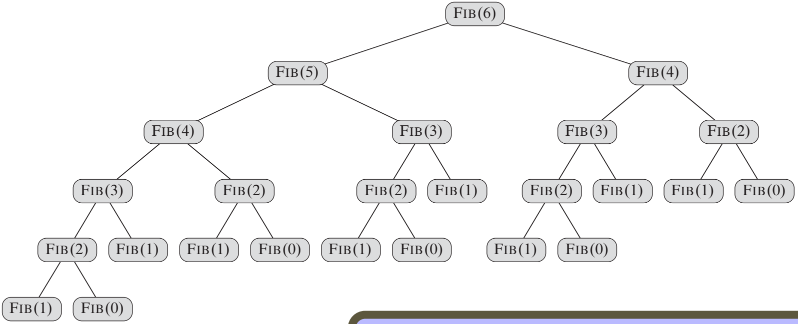

Very inefficient – exponential time!

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FIB(n)
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Computing Fibonacci Numbers in Parallel (Fig. 27.2)

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P-FIB(n)
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Computing Fibonacci Numbers in Parallel (Fig. 27.2)

- Without **spawn** and **sync** same pseudocode as before
- **spawn** does not imply parallel execution (depends on scheduler)

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Computation Dag $G = (V, E)$

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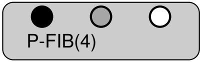


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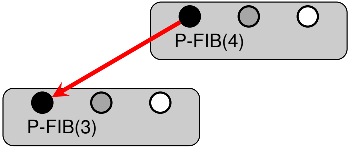


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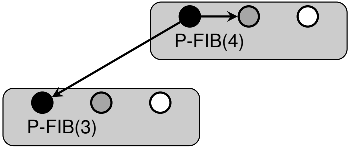


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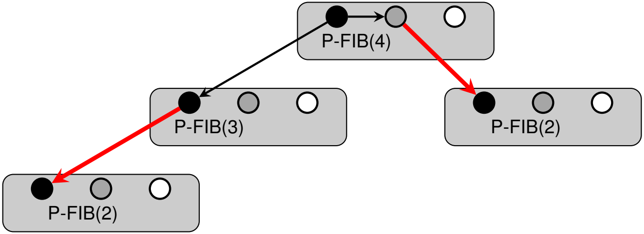


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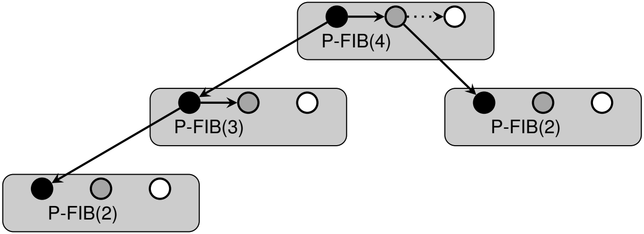


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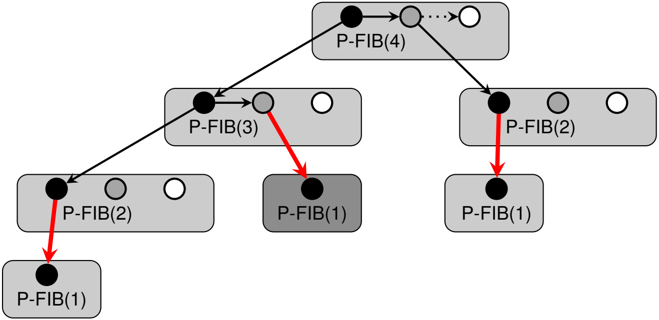


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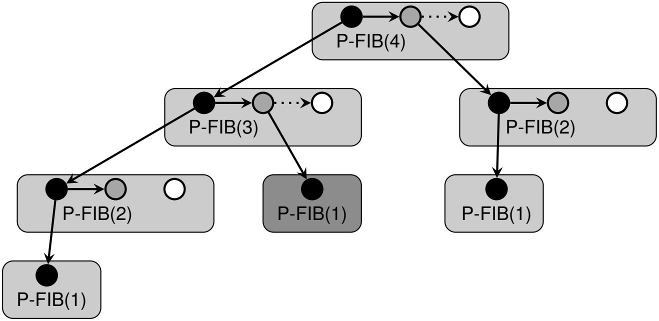


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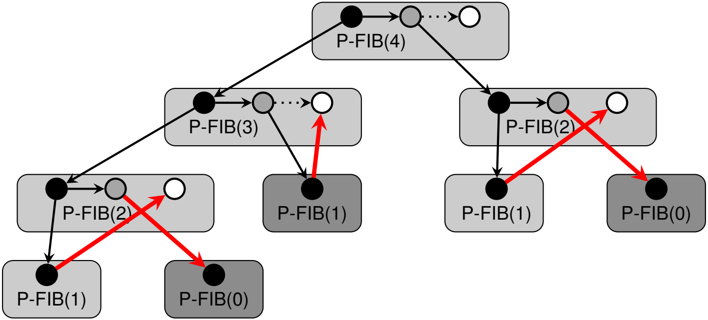


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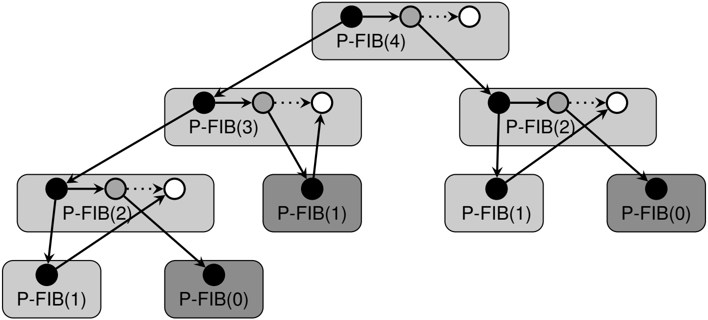


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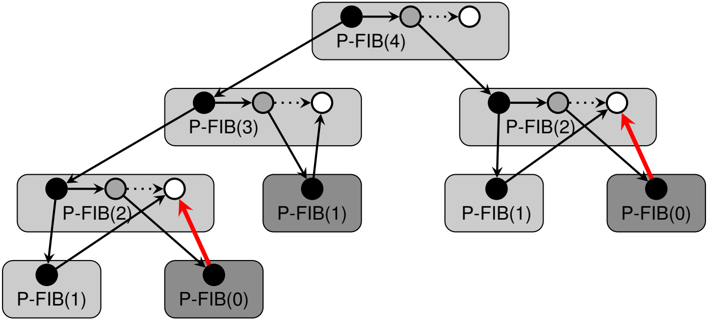


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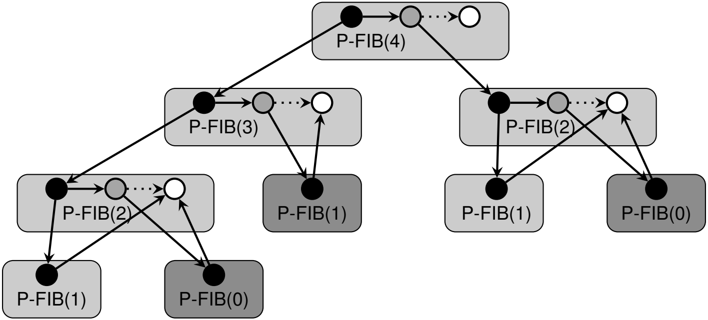


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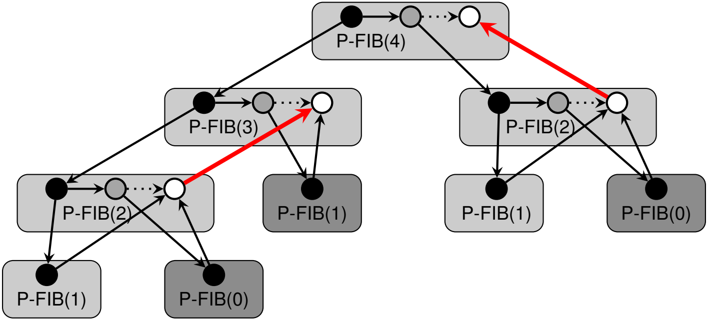


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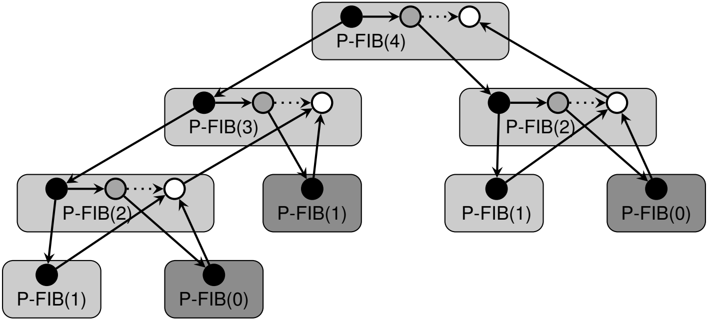


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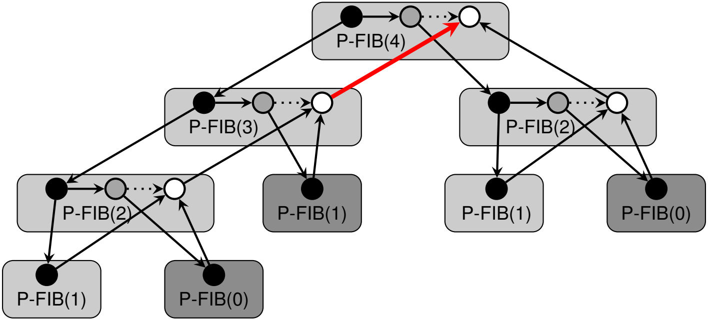


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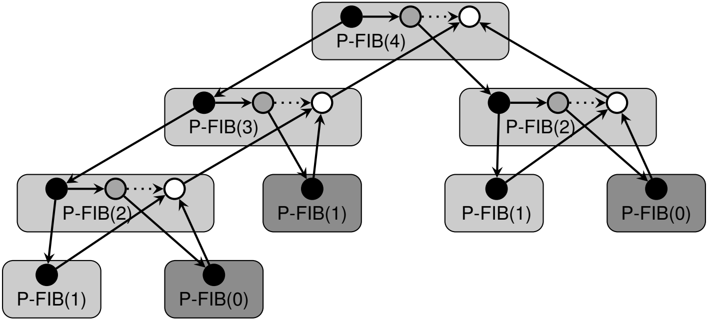


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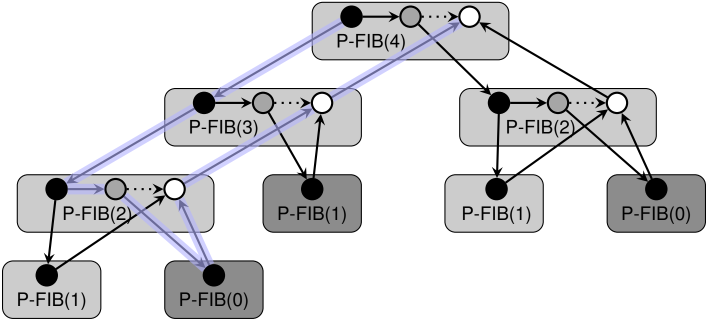


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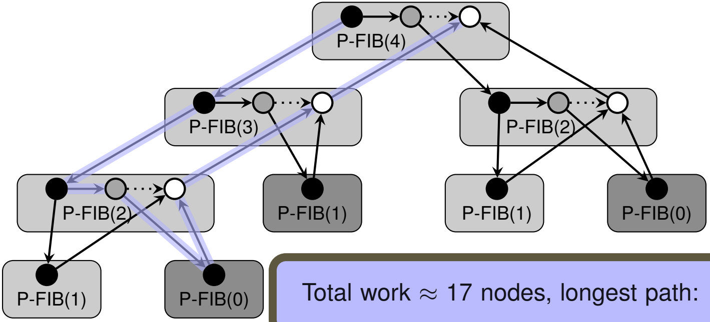

Total work ≈ 17 nodes, longest path: 8 nodes

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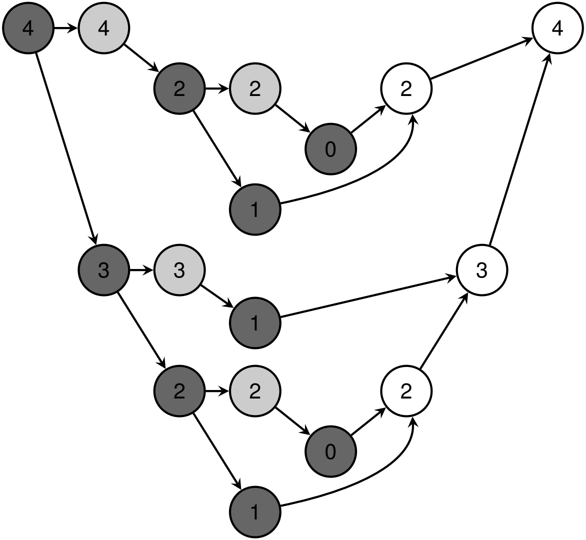
Computing Fibonacci Numbers in Parallel (DAG Perspective)



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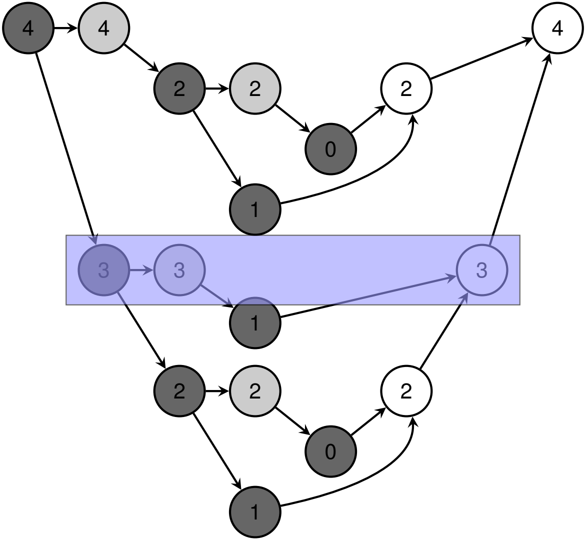

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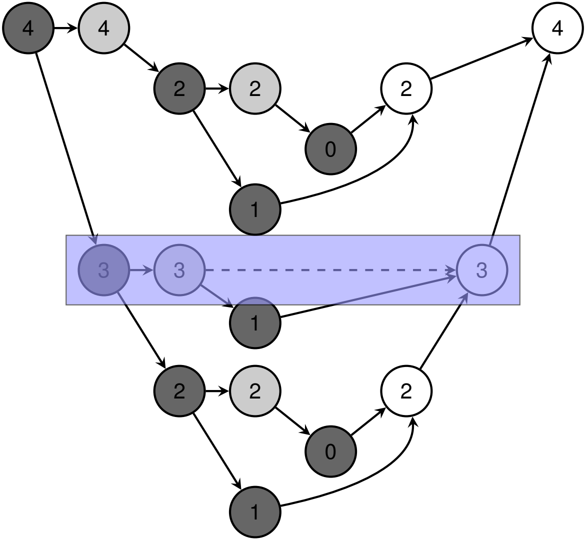

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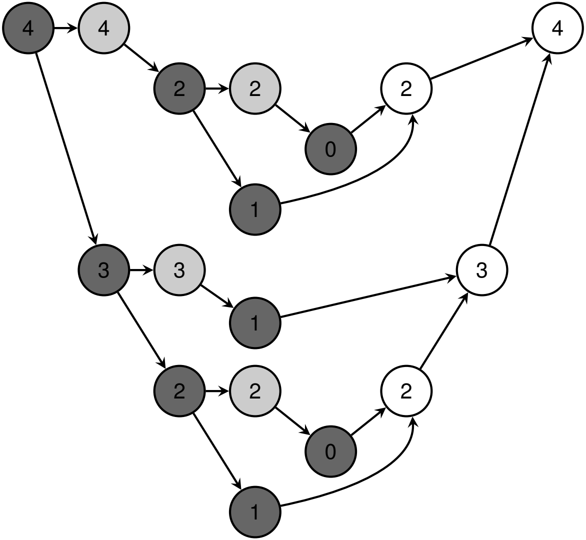

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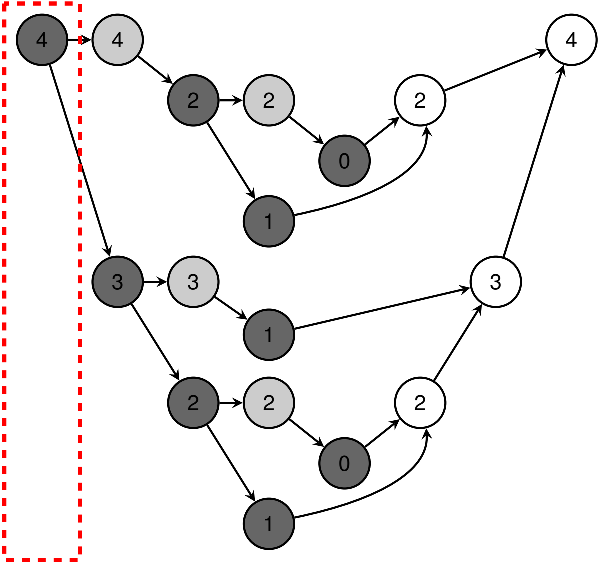

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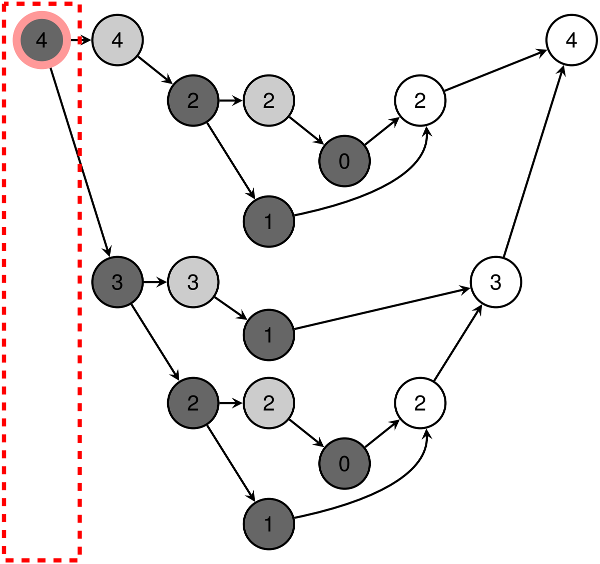

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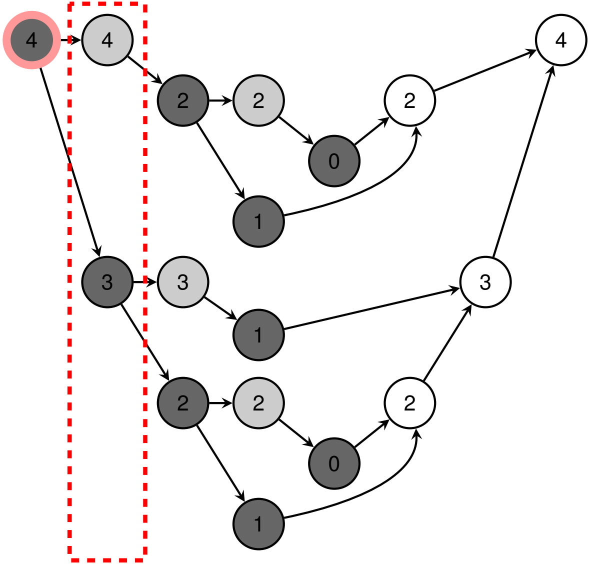

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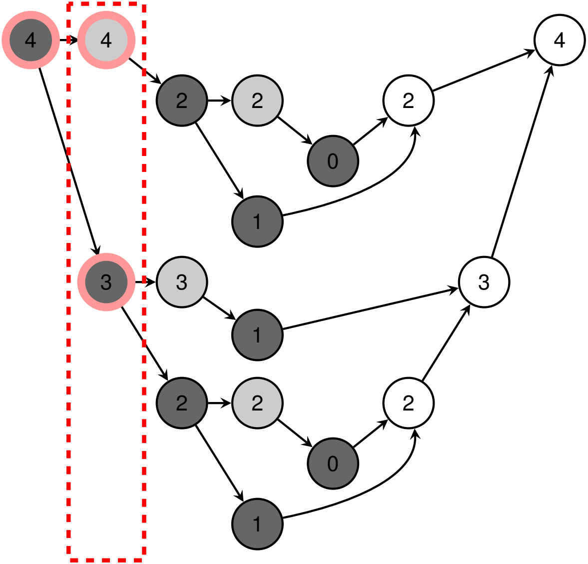

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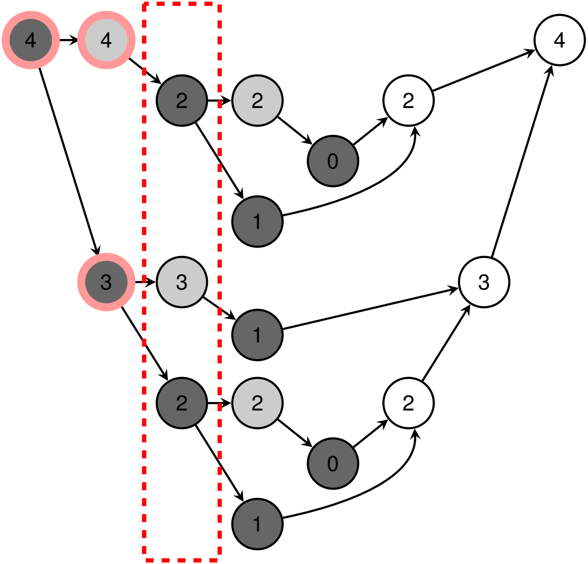

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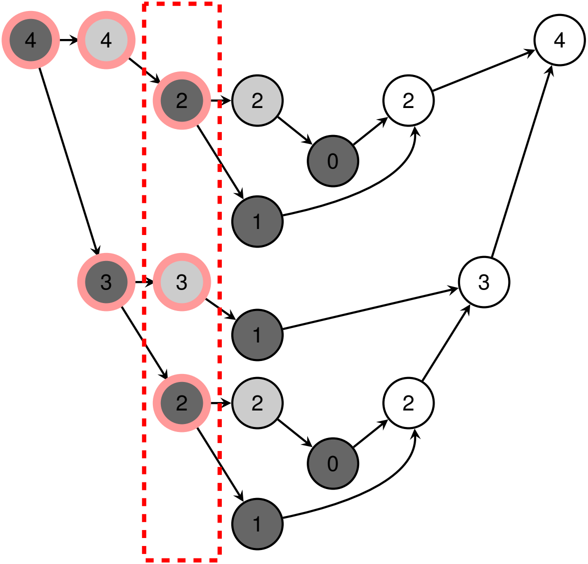
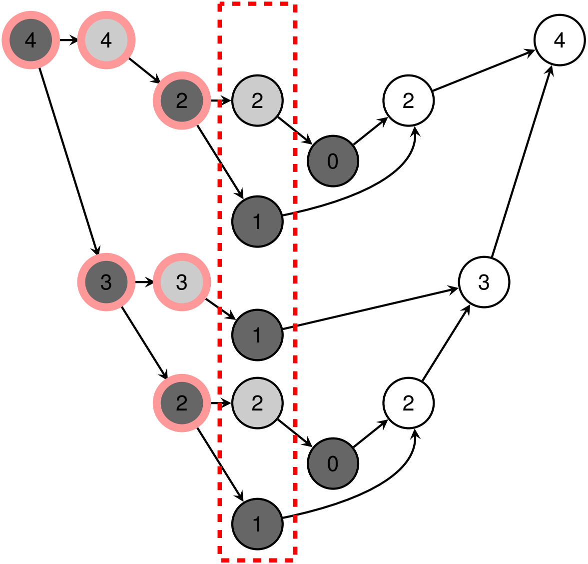


Computing Fibonacci Numbers in Parallel (DAG Perspective)



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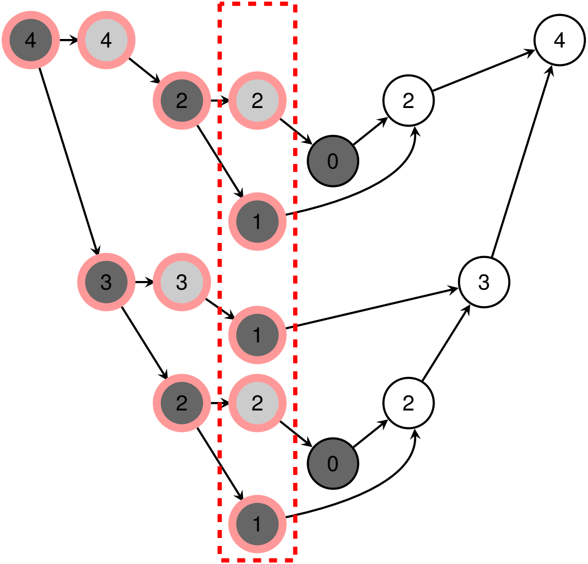

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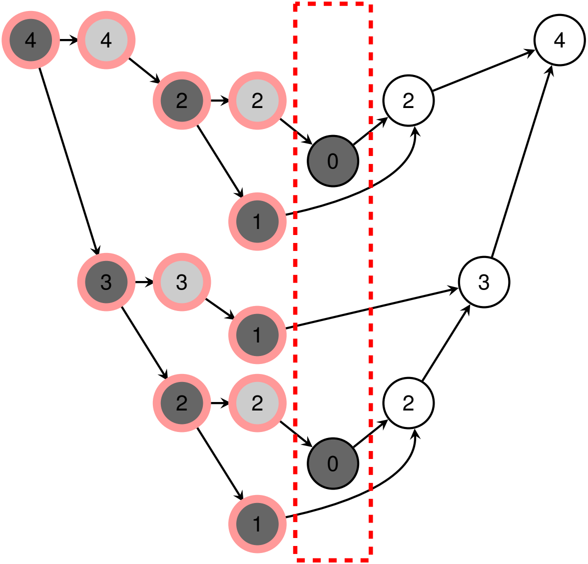

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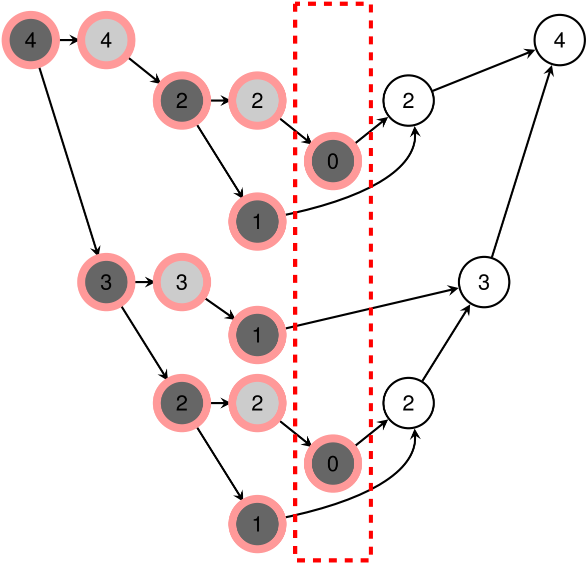

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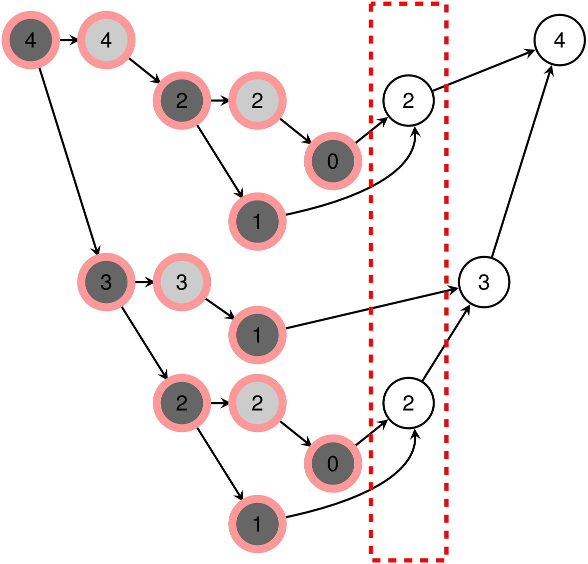

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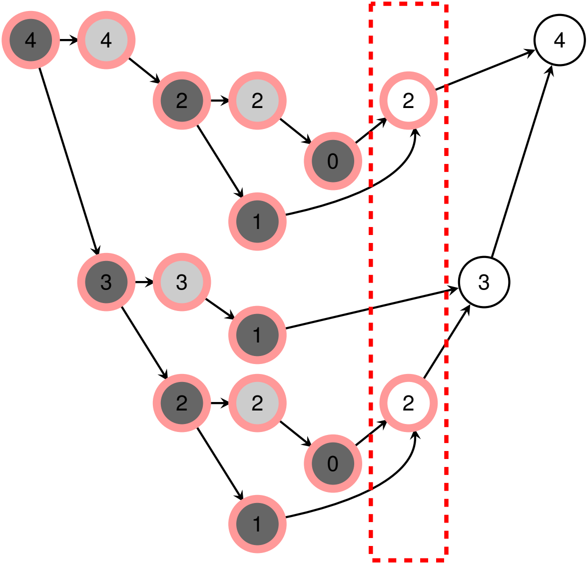

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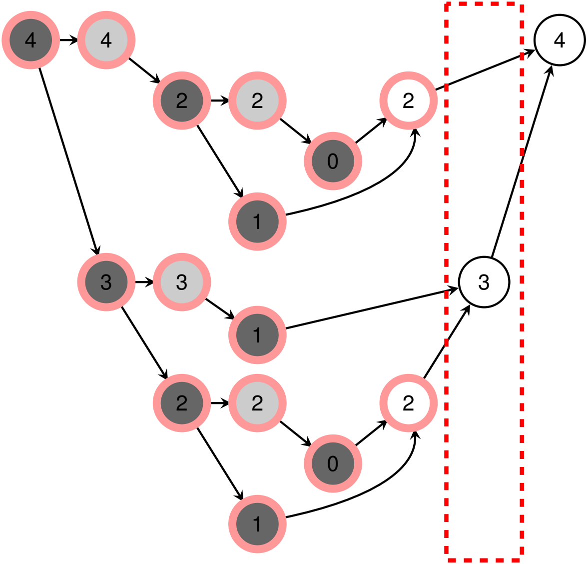

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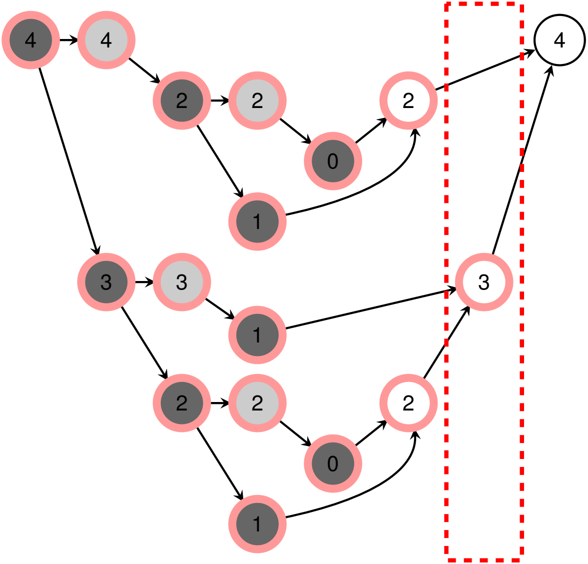

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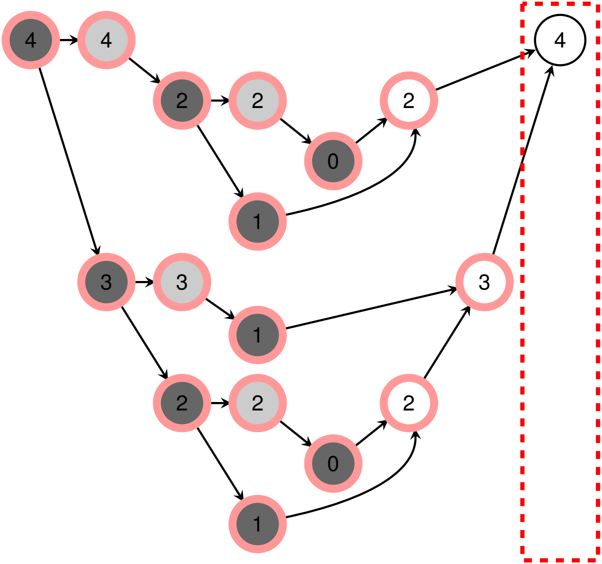

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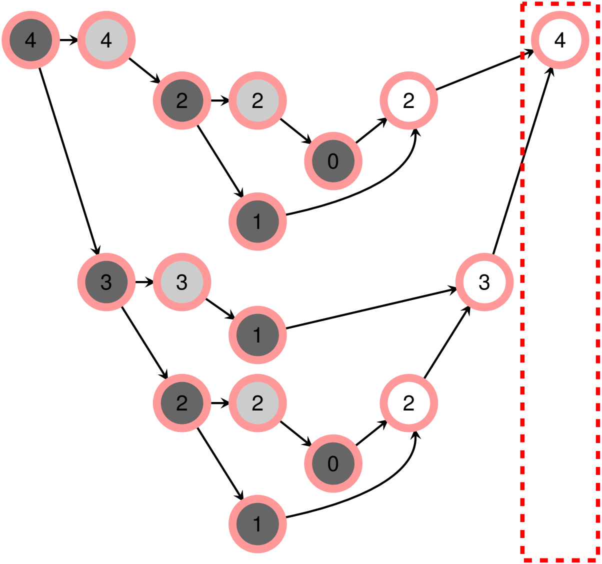

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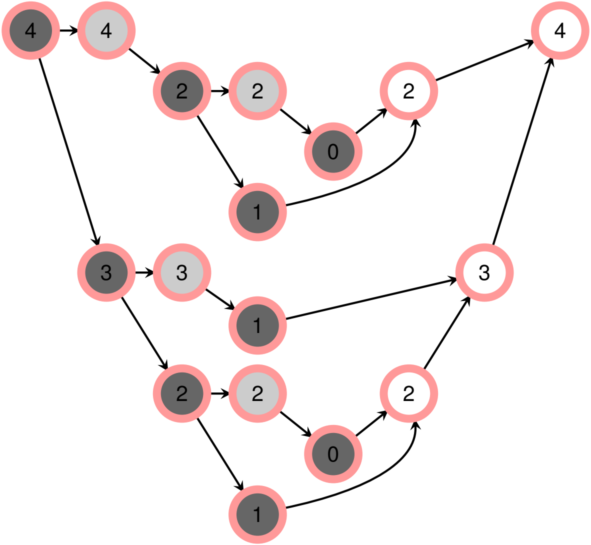

Performance Measures

Work

Total time to execute everything on a single processor.

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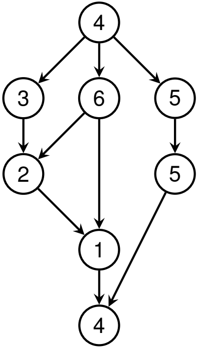
Total time to execute everything on a single processor.

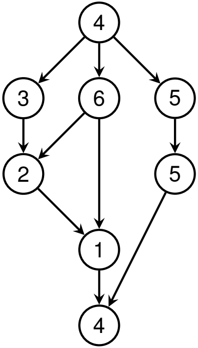


Performance Measures

Work

Total time to execute everything on a single processor.

$$\sum = 30$$



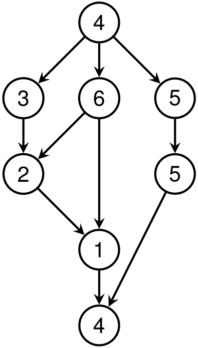
Performance Measures

Work

Total time to execute everything on a single processor.

Span

Longest time to execute the threads along any path.



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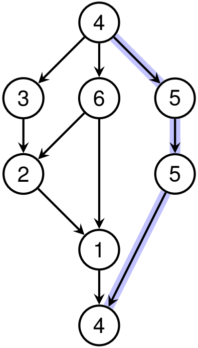


Performance Measures

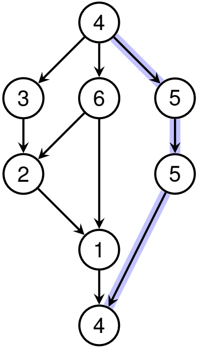
Work

Total time to execute everything on a single processor.

Span

Longest time to execute the threads along any path.

$$\sum = 18$$



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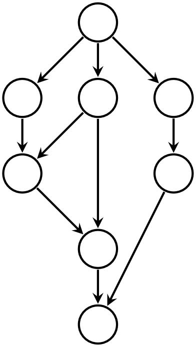


Performance Measures

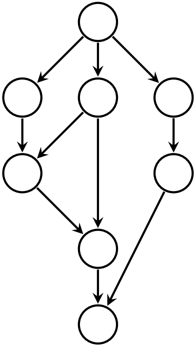
Work

Total time to execute everything on a single processor.

Span

Longest time to execute the threads along any path.

If each thread takes unit time, span is the length of the critical path.



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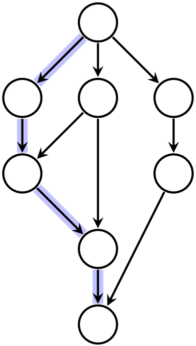

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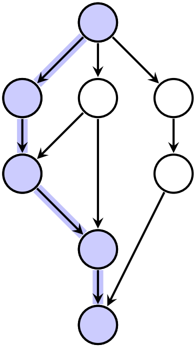
$\#\text{nodes} = 5$



Work Law and Span Law

Work Law and Span Law

- $T_1 =$ work, $T_\infty =$ span

Work Law and Span Law

- T_1 = work, T_∞ = span
- P = number of (identical) processors
- T_P = running time on P processors

Work Law and Span Law

- $T_1 = \text{work}$, $T_\infty = \text{span}$
- $P = \text{number of (identical) processors}$
- $T_P = \text{running time on } P \text{ processors}$

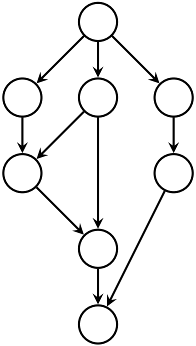
Running time actually also depends on scheduler etc.!

Work Law and Span Law

- T_1 = work, T_∞ = span
- P = number of (identical) processors
- T_P = running time on P processors

Work Law

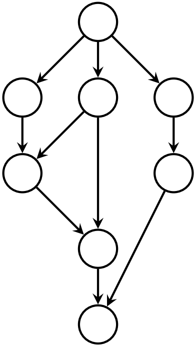
$$T_P \geq \frac{T_1}{P}$$



Work Law and Span Law

- T_1 = work, T_∞ = span
- P = number of (identical) processors
- T_P = running time on P processors

$T_1 = 8$, $P = 2$



Work Law

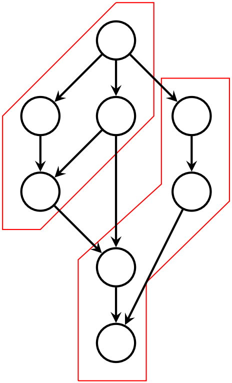
$$T_P \geq \frac{T_1}{P}$$

Time on P processors can't be shorter than if all work all time

Work Law and Span Law

- T_1 = work, T_∞ = span
- P = number of (identical) processors
- T_P = running time on P processors

$T_1 = 8$, $P = 2$



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Time on P processors can't be shorter than if all work all time

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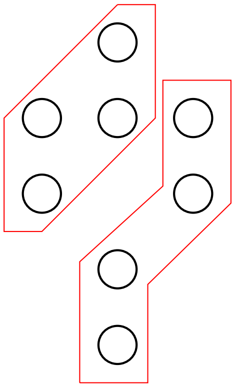
- T_P = running time on P processors

$T_1 = 8,\ P = 2$



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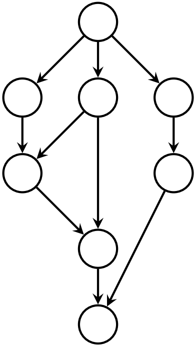

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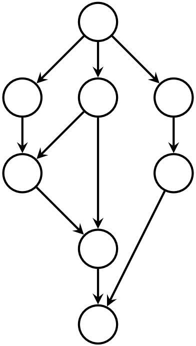
Work Law

$$T_P \geq \frac{T_1}{P}$$

Span Law

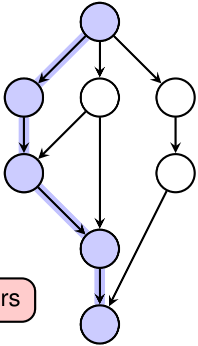
$$T_P \geq T_\infty$$



Work Law and Span Law

- T_1 = work, T_∞ = span
- P = number of (identical) processors
- T_P = running time on P processors

$T_\infty = 5$



Work Law

$$T_P \geq \frac{T_1}{P}$$

Span Law

$$T_P \geq T_\infty$$

Time on P processors can't be shorter than time on ∞ processors

Work Law and Span Law

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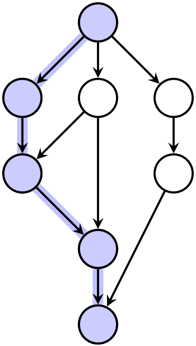

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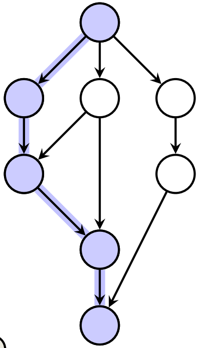

Work Law

$$T_P \geq \frac{T_1}{P}$$

Span Law

$$T_P \geq T_\infty$$

- Speed-Up: $\frac{T_1}{T_P}$

Maximum Speed-Up bounded by P!

Work Law and Span Law

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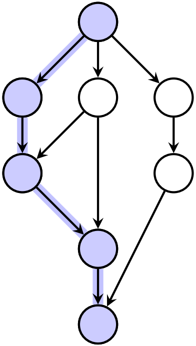

Work Law

$$T_P \geq \frac{T_1}{P}$$

Span Law

$$T_P \geq T_\infty$$

- Speed-Up: $\frac{T_1}{T_P}$
- Parallelism: $\frac{T_1}{T_\infty}$

Work Law and Span Law

- T_1 = work, T_∞ = span
- P = number of (identical) processors
- T_P = running time on P processors

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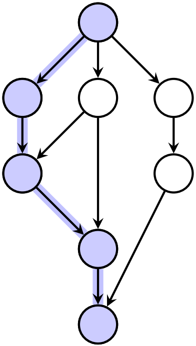

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$$T_P \geq T_\infty$$

- Speed-Up: $\frac{T_1}{T_P}$
- Parallelism: $\frac{T_1}{T_\infty}$

Maximum Speed-Up for ∞ processors!

Outline

Introduction

Serial Matrix Multiplication

Digression: Multithreading

Multithreaded Matrix Multiplication

Warmup: Matrix Vector Multiplication

Remember: Multiplying an $n \times n$ matrix $A = (a_{ij})$ and n-vector $x = (x_j)$ yields an n-vector $y = (y_i)$ given by

$$y_i = \sum_{j=1}^{n} a_{ij} x_j \qquad \text{for } i = 1, 2, \ldots, n.$$

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n = A.rows
let y be a new vector of length n
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The **`parallel for`**-loops can be used since different entries of y can be computed concurrently.

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The **parallel for**-loops can be used since different entries of y can be computed concurrently.

How can a compiler implement the **parallel for**-loop?

Implementing `parallel for` based on Divide-and-Conquer

MAT-VEC-MAIN-LOOP(A, x, y, n, i, i')

if $i == i'$
    for $j = 1$ **to** n
        $y_i = y_i + a_{ij} x_j$
else $mid = \lfloor (i + i')/2 \rfloor$
    spawn MAT-VEC-MAIN-LOOP(A, x, y, n, i, mid)
    MAT-VEC-MAIN-LOOP$(A, x, y, n, mid + 1, i')$
    sync

MAT-VEC(A, x)

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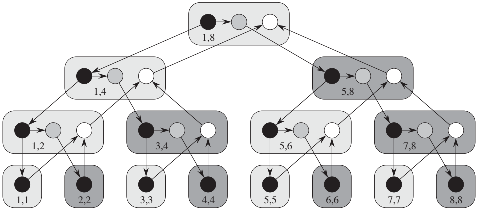

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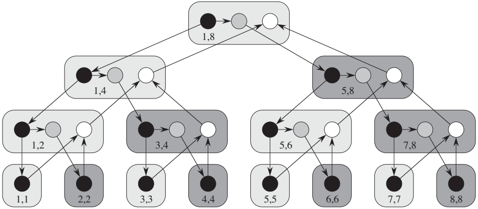


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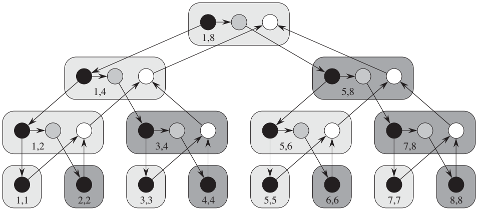
$T_1(n) =$

Implementing `parallel for` based on Divide-and-Conquer

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Work is equal to running time of its serialization; overhead of recursive spawning does not change asymptotics.

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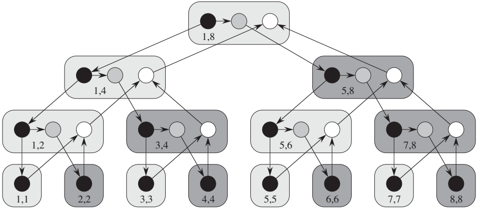

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$$T_1(n) = \Theta(n^2)$$

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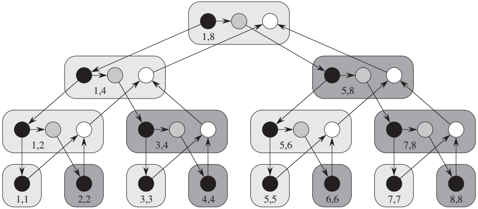

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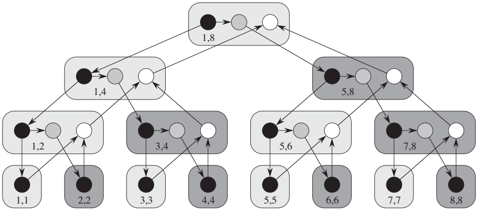
$T_1(n) = \Theta(n^2)$

Work is equal to running time of its serialization; overhead of recursive spawning does not change asymptotics.

$T_\infty(n) =$

Span is the depth of recursive callings plus the maximum span of any of the n iterations.

Implementing `parallel for` based on Divide-and-Conquer

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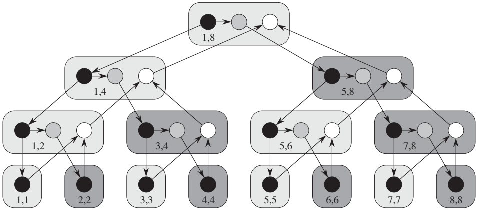

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return y

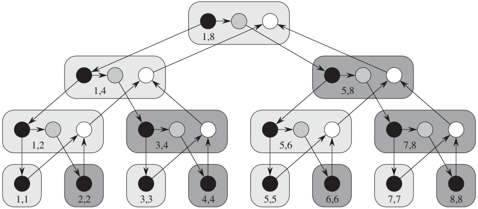
$$T_1(n) = \Theta(n^2)$$

Work is equal to running time of its serialization; overhead of recursive spawning does not change asymptotics.

$$T_\infty(n) = \Theta(\log n) + \max_{1 \le i \le n} \mathrm{iter}(n)$$

Span is the depth of recursive callings plus the maximum span of any of the n iterations.

Implementing `parallel for` based on Divide-and-Conquer

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return y
```

$$T_1(n) = \Theta(n^2)$$

Work is equal to running time of its serialization; overhead of recursive spawning does not change asymptotics.

$$T_\infty(n) = \Theta(\log n) + \max_{1 \leq i \leq n} \text{iter}(n)$$
$$= \Theta(n).$$

Span is the depth of recursive callings plus the maximum span of any of the n iterations.

Naive Algorithm in Parallel

```
P-SQUARE-MATRIX-MULTIPLY(A, B)
n = A.rows
let C be a new n × n matrix
parallel for i = 1 to n
    parallel for j = 1 to n
        c_ij = 0
        for k = 1 to n
            c_ij = c_ij + a_ik · b_kj
return C
```

Naive Algorithm in Parallel

P-SQUARE-MATRIX-MULTIPLY(A, B)

```
n = A.rows
let C be a new n × n matrix
parallel for i = 1 to n
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        c_ij = 0
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return C
```

With a more careful implementation, $T_\infty(n) = O(\log n)$ (CLRS, Exercise 27.2-3)

P-SQUARE-MATRIX-MULTIPLY(A, B) has work $T_1(n) = \Theta(n^3)$ and span $T_\infty(n) = \Theta(n)$.

The first two nested for-loops parallelise perfectly.

The Simple Divide&Conquer Approach in Parallel

```
P-MATRIX-MULTIPLY-RECURSIVE(C, A, B)
n = A.rows
if n == 1
    c11 = a11 b11
else let T be a new n × n matrix
    partition A, B, C, and T into n/2 × n/2 submatrices
        A11, A12, A21, A22; B11, B12, B21, B22; C11, C12, C21, C22;
        and T11, T12, T21, T22; respectively
    spawn P-MATRIX-MULTIPLY-RECURSIVE(C11, A11, B11)
    spawn P-MATRIX-MULTIPLY-RECURSIVE(C12, A11, B12)
    spawn P-MATRIX-MULTIPLY-RECURSIVE(C21, A21, B11)
    spawn P-MATRIX-MULTIPLY-RECURSIVE(C22, A21, B12)
    spawn P-MATRIX-MULTIPLY-RECURSIVE(T11, A12, B21)
    spawn P-MATRIX-MULTIPLY-RECURSIVE(T12, A12, B22)
    spawn P-MATRIX-MULTIPLY-RECURSIVE(T21, A22, B21)
    P-MATRIX-MULTIPLY-RECURSIVE(T22, A22, B22)
    sync
    parallel for i = 1 to n
        parallel for j = 1 to n
            cij = cij + tij
```

The Simple Divide&Conquer Approach in Parallel

```
P-MATRIX-MULTIPLY-RECURSIVE(C, A, B)
n = A.rows
if n == 1
    c_11 = a_11 b_11
else let T be a new n × n matrix
    partition A, B, C, and T into n/2 × n/2 submatrices
        A_11, A_12, A_21, A_22; B_11, B_12, B_21, B_22; C_11, C_12, C_21, C_22;
        and T_11, T_12, T_21, T_22; respectively
    spawn P-MATRIX-MULTIPLY-RECURSIVE(C_11, A_11, B_11)
    spawn P-MATRIX-MULTIPLY-RECURSIVE(C_12, A_11, B_12)
    spawn P-MATRIX-MULTIPLY-RECURSIVE(C_21, A_21, B_11)
    spawn P-MATRIX-MULTIPLY-RECURSIVE(C_22, A_21, B_12)
    spawn P-MATRIX-MULTIPLY-RECURSIVE(T_11, A_12, B_21)
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    spawn P-MATRIX-MULTIPLY-RECURSIVE(T_21, A_22, B_21)
    P-MATRIX-MULTIPLY-RECURSIVE(T_22, A_22, B_22)
    sync
    parallel for i = 1 to n
        parallel for j = 1 to n
            c_ij = c_ij + t_ij
```

The same as before.

P-MATRIX-MULTIPLY-RECURSIVE has work $T_1(n) = \Theta(n^3)$ and span $T_\infty(n) =$

The Simple Divide&Conquer Approach in Parallel

P-MATRIX-MULTIPLY-RECURSIVE(C, A, B)

$n = A.rows$
if $n == 1$
    $c_{11} = a_{11}b_{11}$
else let T be a new $n \times n$ matrix
    partition A, B, C, and T into $n/2 \times n/2$ submatrices
        $A_{11}, A_{12}, A_{21}, A_{22}$; $B_{11}, B_{12}, B_{21}, B_{22}$; $C_{11}, C_{12}, C_{21}, C_{22}$;
        and $T_{11}, T_{12}, T_{21}, T_{22}$; respectively
    spawn P-MATRIX-MULTIPLY-RECURSIVE(C_{11}, A_{11}, B_{11})
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    P-MATRIX-MULTIPLY-RECURSIVE(T_{22}, A_{22}, B_{22})
    sync
    parallel for $i = 1$ **to** n
        parallel for $j = 1$ **to** n
            $c_{ij} = c_{ij} + t_{ij}$

The same as before.

P-MATRIX-MULTIPLY-RECURSIVE has work $T_1(n) = \Theta(n^3)$ and span $T_\infty(n) =$

$T_\infty(n) = T_\infty(n/2) + \Theta(\log n)$

The Simple Divide&Conquer Approach in Parallel

```
P-MATRIX-MULTIPLY-RECURSIVE(C, A, B)
n = A.rows
if n == 1
    c_11 = a_11 b_11
else let T be a new n × n matrix
    partition A, B, C, and T into n/2 × n/2 submatrices
        A_11, A_12, A_21, A_22; B_11, B_12, B_21, B_22; C_11, C_12, C_21, C_22;
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    spawn P-MATRIX-MULTIPLY-RECURSIVE(T_21, A_22, B_21)
    P-MATRIX-MULTIPLY-RECURSIVE(T_22, A_22, B_22)
    sync
    parallel for i = 1 to n
        parallel for j = 1 to n
            c_ij = c_ij + t_ij
```

The same as before.

P-MATRIX-MULTIPLY-RECURSIVE has work $T_1(n) = \Theta(n^3)$ and span $T_\infty(n) = \Theta(\log^2 n)$.

$T_\infty(n) = T_\infty(n/2) + \Theta(\log n)$

Strassen's Algorithm in Parallel

Strassen's Algorithm (parallelised)

1. Partition each of the matrices into four $n/2 \times n/2$ submatrices

Strassen's Algorithm in Parallel

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   This step takes $\Theta(1)$ work and span by index calculations.

Strassen's Algorithm in Parallel

Strassen's Algorithm (parallelised)

1. Partition each of the matrices into four $n/2 \times n/2$ submatrices

   This step takes $\Theta(1)$ work and span by index calculations.

2. Create 10 matrices $S_1, S_2, \ldots, S_{10}$. Each is $n/2 \times n/2$ and is the sum or difference of two matrices created in the previous step.

Strassen's Algorithm in Parallel

Strassen's Algorithm (parallelised)

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   This step takes $\Theta(1)$ work and span by index calculations.

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$$T_\infty(n) = \Theta(\log^2 n)$$