**Thomas Sauerwald** 





#### **Outline**

#### Introduction

Serial Matrix Multiplication

Reminder: Multithreading

Multithreaded Matrix Multiplication

Remember: If  $A = (a_{ij})$  and  $B = (b_{ij})$  are square  $n \times n$  matrices, then the matrix product  $C = A \cdot B$  is defined by

$$c_{ij} = \sum_{k=1}^{n} a_{ik} \cdot b_{kj} \quad \forall i, j = 1, 2, \dots, n.$$



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SQUARE-MATRIX-MULTIPLY (A, B)
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1 n = A.rows

2 let C be a new n \times n matrix

3 for i = 1 to n

4 for j = 1 to n

5 c_{ij} = 0

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Square-Matrix-Multiply(A, B) takes time  $\Theta(n^3)$ .



This definition suggests that  $n^2 \cdot n = n^3$ 

arithmetic operations are necessary.

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**Assumption:** *n* is always an exact power of 2.



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Partition A, B, and C into four  $n/2 \times n/2$  matrices:



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$$A = \begin{pmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{pmatrix}, \quad B = \begin{pmatrix} B_{11} & B_{12} \\ B_{21} & B_{22} \end{pmatrix}, \quad C = \begin{pmatrix} C_{11} & C_{12} \\ C_{21} & C_{22} \end{pmatrix}.$$

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$$\begin{pmatrix} C_{11} & C_{12} \\ C_{21} & C_{22} \end{pmatrix} = \begin{pmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{pmatrix} \cdot \begin{pmatrix} B_{11} & B_{12} \\ B_{21} & B_{22} \end{pmatrix}$$

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This corresponds to the four equations:

$$C_{11} = A_{11} \cdot B_{11} + A_{12} \cdot B_{21}$$

$$C_{12} = A_{11} \cdot B_{12} + A_{12} \cdot B_{22}$$

$$C_{21} = A_{21} \cdot B_{11} + A_{22} \cdot B_{21}$$

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Hence the equation  $C = A \cdot B$  becomes:

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This corresponds to the four equations:

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$$C_{21} = A_{21} \cdot B_{11} + A_{22} \cdot B_{21}$$

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Each equation specifies two multiplications of  $C_{21} = A_{21} \cdot B_{11} + A_{22} \cdot B_{21}$   $n/2 \times n/2$  matrices and the addition of their products.

$$C_{11} = A_{11} \cdot B_{11} + A_{12} \cdot B_{21}$$

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```
SOUARE-MATRIX-MULTIPLY-RECURSIVE (A, B)
    n = A rows
    let C be a new n \times n matrix
   if n == 1
         c_{11} = a_{11} \cdot b_{11}
    else partition A, B, and C as in equations (4.9)
 6
         C_{11} = \text{SQUARE-MATRIX-MULTIPLY-RECURSIVE}(A_{11}, B_{11})
              + SQUARE-MATRIX-MULTIPLY-RECURSIVE (A_{12}, B_{21})
         C_{12} = \text{SQUARE-MATRIX-MULTIPLY-RECURSIVE}(A_{11}, B_{12})
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    return C
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SQUARE-MATRIX-MULTIPLY-RECURSIVE (A, B)

```
n = A rows
                                    Line 5: Handle submatrices implicitly through
    let C be a new n \times n matrix
                                     index calculations instead of creating them.
   if n == 1
        c_{11} = a_{11} \cdot b_{11}
    else partition A, B, and C as in equations (4.9)
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        C_{11} = \text{SQUARE-MATRIX-MULTIPLY-RECURSIVE}(A_{11}, B_{11})
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$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1\\ & \text{if } n > 1 \end{cases}$$



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8 Multiplications 4 Additions and Partitioning



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Let T(n) be the runtime of this procedure. Then:

$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1, \\ 8 \cdot T(n/2) + \Theta(n^2) & \text{if } n > 1. \end{cases}$$

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Solution: 
$$T(n) = \Theta(8^{\log_2 n})$$



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Let T(n) be the runtime of this procedure. Then:

$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1, \\ 8 \cdot T(n/2) + \Theta(n^2) & \text{if } n > 1. \end{cases}$$

Solution: 
$$T(n) = \Theta(8^{\log_2 n}) = \Theta(n^3) <$$

No improvement over the naive algorithm!



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         C_{22} = \text{SOUARE-MATRIX-MULTIPLY-RECURSIVE}(A_{21}, B_{12})
 9
              + SQUARE-MATRIX-MULTIPLY-RECURSIVE (A_{22}, B_{22})
10
    return C
```

Let T(n) be the runtime of this procedure. Then:

$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1, \\ \frac{8}{3} \cdot T(n/2) + \Theta(n^2) & \text{if } n > 1. \end{cases}$$

Solution: 
$$T(n) = \Theta(8^{\log_2 n}) = \Theta(n^3)$$

Goal: Reduce the number of multiplications



#### **Divide & Conquer: Second Approach**

**Idea**: Make the recursion tree less bushy by performing only **7** recursive multiplications of  $n/2 \times n/2$  matrices.



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#### Strassen's Algorithm (1969)

- 1. Partition each of the matrices into four  $n/2 \times n/2$  submatrices
- 2. Create 10 matrices  $S_1, S_2, \dots, S_{10}$ . Each is  $n/2 \times n/2$  and is the sum or difference of two matrices created in the previous step.
- 3. Recursively compute 7 matrix products  $P_1, P_2, \dots, P_7$ , each  $n/2 \times n/2$
- 4. Compute  $n/2 \times n/2$  submatrices of *C* by adding and subtracting various combinations of the  $P_i$ .

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Time for steps 1,2,4: 
$$\Theta(n^2)$$
, hence  $T(n) = \frac{7}{2} \cdot T(n/2) + \Theta(n^2) \Rightarrow T(n) = \Theta(n^{\log 7})$ .



## **Solving the Recursion**

$$T(n) = \frac{7}{2} \cdot T(n/2) + c \cdot n^2$$



The 10 Submatrices and 7 Products

$$P_{1} = A_{11} \cdot S_{1} = A_{11} \cdot (B_{12} - B_{22})$$

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$$\begin{pmatrix} A_{11}B_{11} + A_{12}B_{21} & A_{11}B_{12} + A_{12}B_{21} \\ A_{21}B_{11} + A_{22}B_{21} & A_{21}B_{12} + A_{22}B_{22} \end{pmatrix} = \begin{pmatrix} P_5 + P_4 - P_2 + P_6 & P_1 + P_2 \\ P_3 + P_4 & P_5 + P_1 - P_3 - P_7 \end{pmatrix}$$

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Proof:

Other three blocks can be verified similarly.

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Open Problem: Is there an algorithm with quadratic complexity?



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- *O*(*n*<sup>2.522</sup>), Schönhage (1981)
- $O(n^{2.517})$ , Romani (1982)
- O(n<sup>2.496</sup>), Coppersmith and Winograd (1982)
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- $O(n^{2.3728642})$ , V. Williams (2011)
- O(n<sup>2.3728639</sup>), Le Gall (2014)
- . .



#### **Outline**

Introduction

Serial Matrix Multiplication

Reminder: Multithreading

Multithreaded Matrix Multiplication



Distributed Memory ——

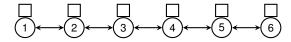
- Each processor has its private memory
- Access to memory of another processor via messages



II. Matrix Multiplication Reminder: Multithreading

Distributed Memory -

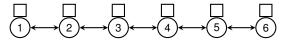
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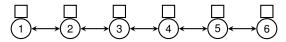
#### Shared Memory -

- Central location of memory
- Each processor has direct access



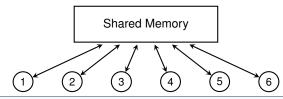
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Reminder: Multithreading

Programming shared-memory parallel computer difficult



- Programming shared-memory parallel computer difficult
- Use concurrency platform which coordinates all resources



II. Matrix Multiplication Reminder: Multithreading

13

- Programming shared-memory parallel computer difficult
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Scheduling jobs, communication protocols, load balancing etc.



II. Matrix Multiplication Reminder: Multithreading

13

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Functionalities:



- Programming shared-memory parallel computer difficult
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#### Functionalities:

spawn



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  - procedure is executed in a separate thread
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Only logical parallelism, but not actual! Need a scheduler to map threads to processors.



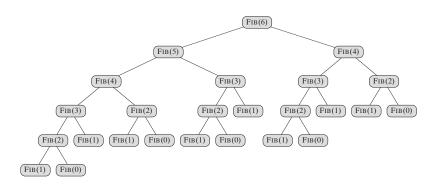
II. Matrix Multiplication Reminder: Multithreading 13

### **Computing Fibonacci Numbers Recursively (Fig. 27.1)**

```
0: FIB(n)
1:    if n<=1 return n
2:    else x=FIB(n-1)
3:        y=FIB(n-2)
4:        return x+y</pre>
```



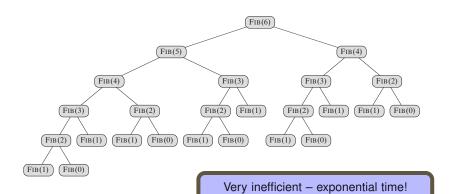
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```
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2: else x=spawn P-FIB(n-1)
3: y=P-FIB(n-2)
4: sync
5: return x+y</pre>
```



15

```
0: P-FIB(n)

1:
2:
• Without spawn and sync same pseudocode as before
• spawn does not imply parallel execution (depends on scheduler)

4: sync

5: return x+y
```



II. Matrix Multiplication Reminder: Multithreading 15



```
0: P-FIB(n)

1: Computation Dag G = (V, E)

2: • V set of threads (instructions/strands without parallel control)

4: return x+y
```





```
Computation Dag G = (V, E)
```

- *V* set of threads (instructions/strands without parallel control)
- E set of dependencies

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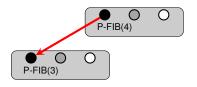


15



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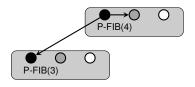
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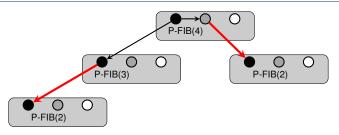
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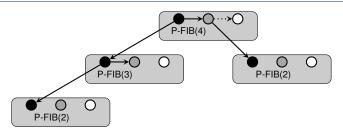




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```



II. Matrix Multiplication Re

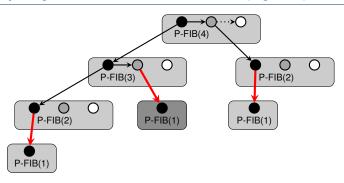


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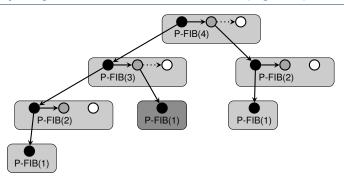
II. Matrix Multiplication Reminder: Multithreading

15



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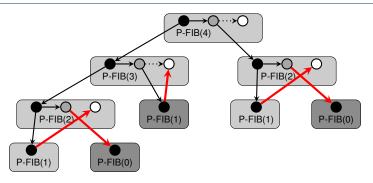




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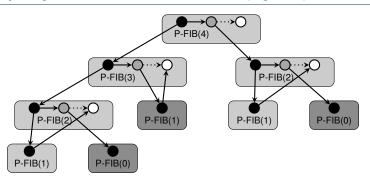
II. Matrix Multiplication



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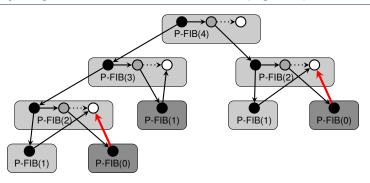


15



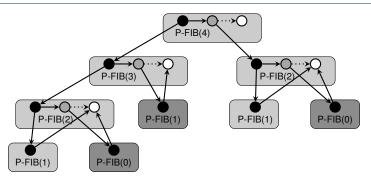
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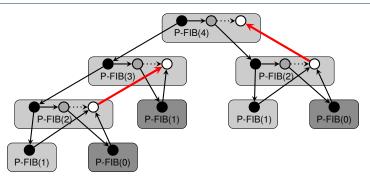
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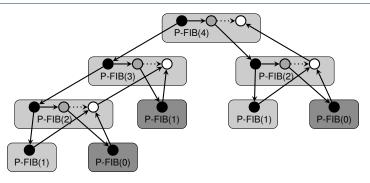




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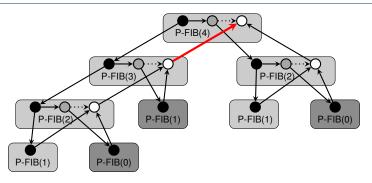


15



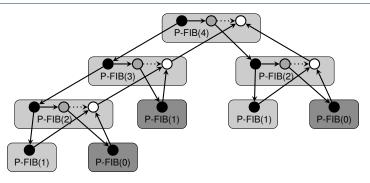
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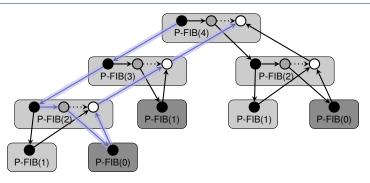
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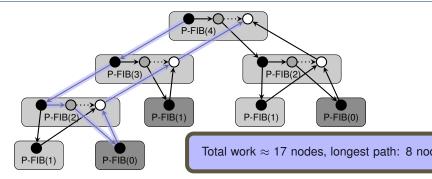
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```
0: P-FIB(n)
```

1: if n<=1 return n

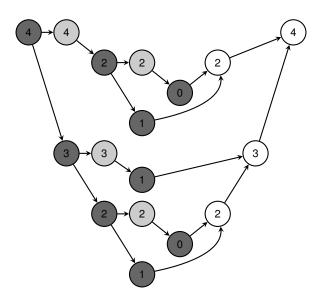
2: else x=spawn P-FIB(n-1)

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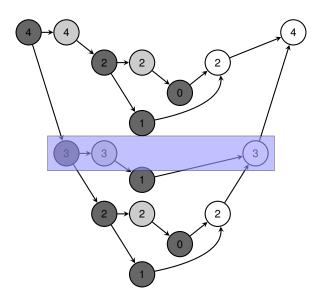
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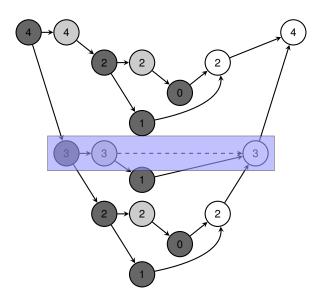




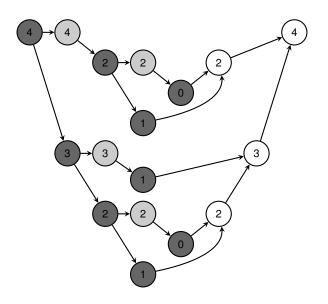




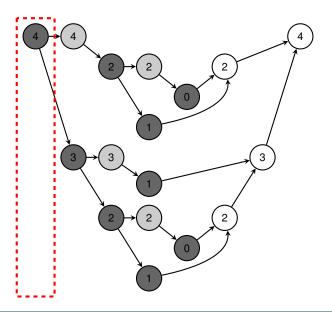




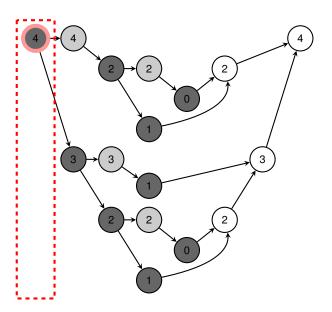




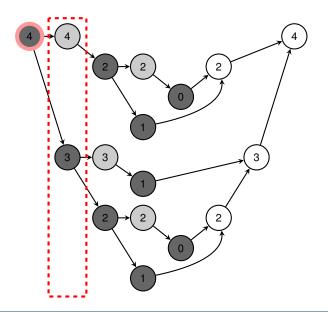




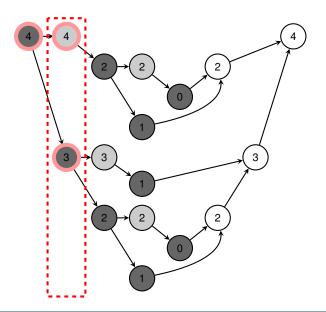




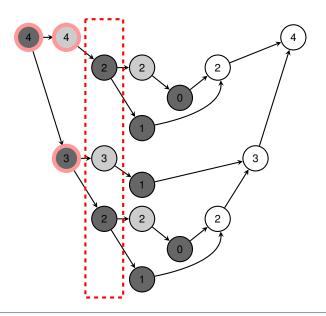






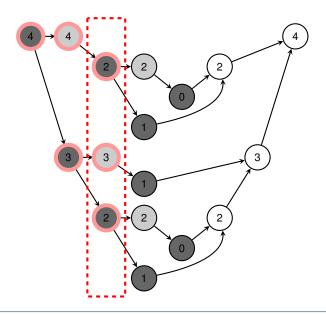




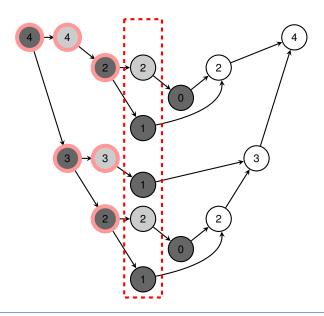




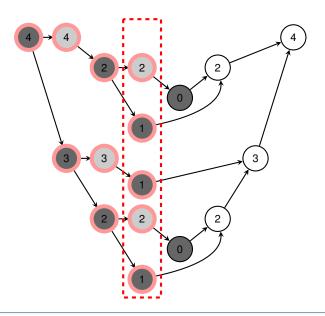
II. Matrix Multiplication



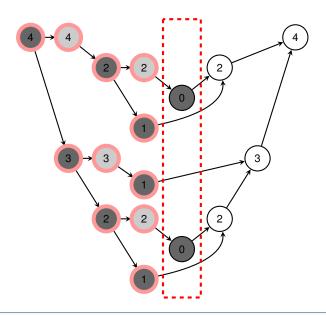




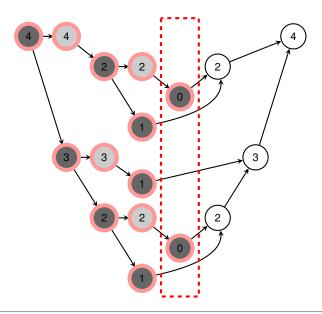






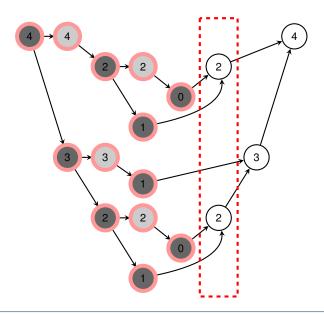




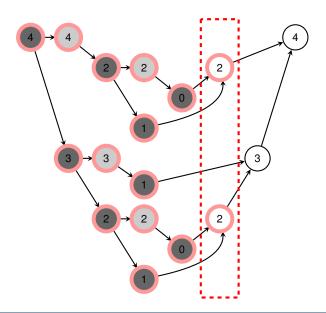




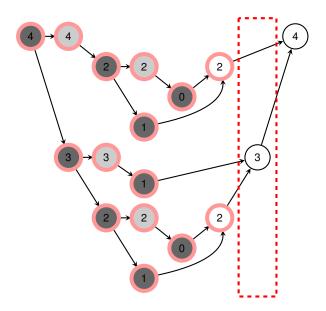
plication Reminder: Multithreading



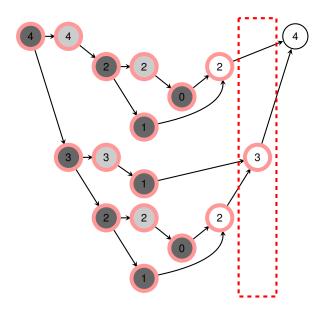




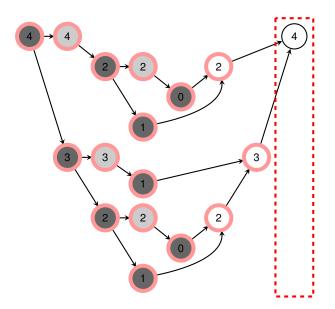




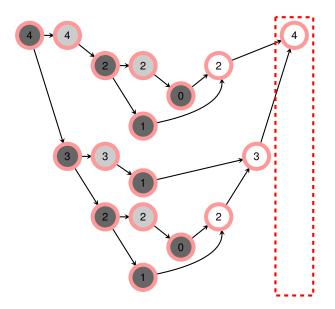




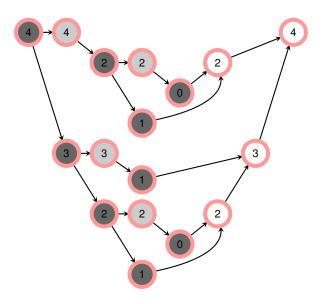














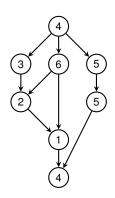
– Work –

Total time to execute everything on a single processor.



– Work –

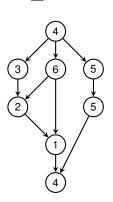
Total time to execute everything on a single processor.



Work -

Total time to execute everything on a single processor.

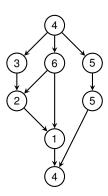
$$\sum = 30$$



— Work —————

Total time to execute everything on a single processor.

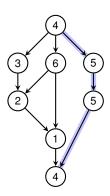
- Span -----



— Work —

Total time to execute everything on a single processor.

- Span ------

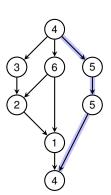


Work —

Total time to execute everything on a single processor.

- Span ------

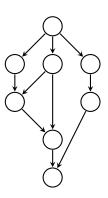




Work —

Total time to execute everything on a single processor.

\_ Span \_\_\_\_\_

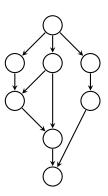


- Work -

Total time to execute everything on a single processor.

If each thread takes unit time, span is the length of the critical path.

Span -

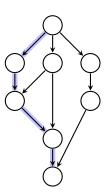


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Total time to execute everything on a single processor.

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Span -



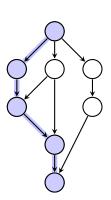
Work -

Total time to execute everything on a single processor.

If each thread takes unit time, span is the length of the critical path.

Span -

$$\#nodes = 5$$





•  $T_1 = \text{work}, T_\infty = \text{span}$ 



- $T_1 = \text{work}, T_\infty = \text{span}$
- P = number of (identical) processors
- $T_P$  = running time on P processors



Reminder: Multithreading

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- $T_1 = \text{work}, T_\infty = \text{span}$
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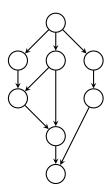
Running time actually also depends on scheduler etc.!



II. Matrix Multiplication Reminder: Multithreading

- $T_1 = \text{work}, T_\infty = \text{span}$
- P = number of (identical) processors
- T<sub>P</sub> = running time on P processors

$$T_P \geq \frac{T_1}{P}$$

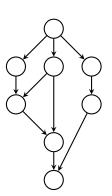




- $T_1 = \text{work}, T_\infty = \text{span}$
- P = number of (identical) processors
- $T_P$  = running time on P processors

$$T_1 = 8, P = 2$$

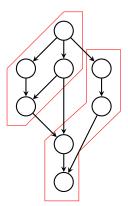
$$T_P \geq \frac{T_1}{P}$$



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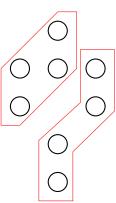
$$T_P \geq \frac{T_1}{P}$$



- $T_1 = \text{work}, T_\infty = \text{span}$
- P = number of (identical) processors
- *T<sub>P</sub>* = running time on *P* processors

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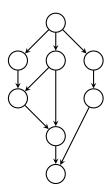
$$T_P \geq \frac{T_1}{P}$$





- $T_1 = \text{work}, T_\infty = \text{span}$
- P = number of (identical) processors
- T<sub>P</sub> = running time on P processors

$$T_P \geq \frac{T_1}{P}$$





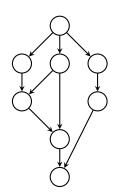
- $T_1 = \text{work}, T_\infty = \text{span}$
- P = number of (identical) processors
- T<sub>P</sub> = running time on P processors

Work Law

$$T_P \geq \frac{T_1}{P}$$

Span Law -

$$T_P \geq T_{\infty}$$



- $T_1 = \text{work}, T_\infty = \text{span}$
- P = number of (identical) processors
- $T_P$  = running time on P processors

 $T_{\infty} = 5$ 

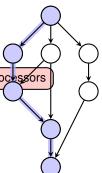
Work Law

$$T_P \geq \frac{T_1}{P}$$

Time on P processors can't be shorter than time on  $\infty$  processors

Span Law -

$$T_P \geq T_{\infty}$$



- $T_1 = \text{work}, T_\infty = \text{span}$
- P = number of (identical) processors
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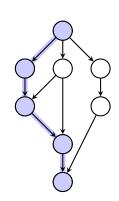
 $T_{\infty} = 5$ 

Work Law

$$T_P \geq \frac{T_1}{P}$$

Span Law -

$$T_P \geq T_{\infty}$$



■ Speed-Up:  $\frac{T_1}{T_P}$ 



- $T_1 = \text{work}, T_\infty = \text{span}$
- P = number of (identical) processors
- $T_P$  = running time on P processors

 $T_{\infty} = 5$ 

Work Law

$$T_P \geq \frac{T_1}{P}$$

Span Law -

$$T_P \geq T_{\infty}$$

Speed-Up: <sup>T1</sup>/<sub>TP</sub>

✓ Maximum Speed-Up bounded by P!

- $T_1 = \text{work}, T_\infty = \text{span}$
- P = number of (identical) processors
- $T_P$  = running time on P processors

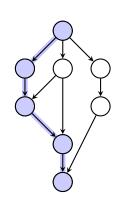
 $T_{\infty}=5$ 

Work Law

$$T_P \geq \frac{T_1}{P}$$

Span Law -

$$T_P \geq T_{\infty}$$



- Speed-Up:  $\frac{T_1}{T_P}$
- Parallelism:  $\frac{T_1}{T_{\infty}}$



- $T_1 = \text{work}, T_\infty = \text{span}$
- P = number of (identical) processors
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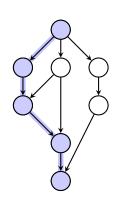
 $T_{\infty}=5$ 

Work Law

$$T_P \geq \frac{T_1}{P}$$

Span Law -

$$T_P \geq T_{\infty}$$



- Speed-Up:  $\frac{T_1}{T_P}$
- Parallelism:  $\frac{T_1}{T_{\infty}}$

Maximum Speed-Up for  $\infty$  processors!



#### **Outline**

Introduction

Serial Matrix Multiplication

Reminder: Multithreading

Multithreaded Matrix Multiplication

Remember: Multiplying an  $n \times n$  matrix  $A = (a_{ij})$  and n-vector  $x = (x_j)$  yields an n-vector  $y = (y_i)$  given by

$$y_i = \sum_{j=1}^n a_{ij} x_j$$
 for  $i = 1, 2, ..., n$ .

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```
MAT-VEC(A, x)

1 n = A.rows

2 let y be a new vector of length n

3 parallel for i = 1 to n

4 y_i = 0

5 parallel for i = 1 to n

6 for j = 1 to n

7 y_i = y_i + a_{ij}x_j

8 return y
```

Remember: Multiplying an  $n \times n$  matrix  $A = (a_{ij})$  and n-vector  $x = (x_j)$  yields an n-vector  $y = (y_i)$  given by

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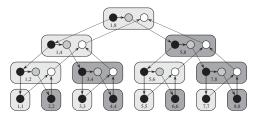
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```

How can a compiler implement the **parallel for**-loop?

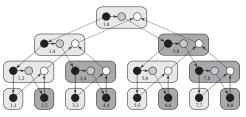


```
\begin{aligned} & \text{Mat-Vec-Main-Loop}(A, x, y, n, i, i') \\ & 1 & \text{ if } i == i' \\ & 2 & \text{ for } j = 1 \text{ to } n \\ & 3 & y_i = y_i + a_{ij}x_j \\ & 4 & \text{ else } mid = \left[ (i+i')/2 \right] \\ & 5 & \text{ spawn Mat-Vec-Main-Loop}(A, x, y, n, i, mid) \\ & 6 & \text{Mat-Vec-Main-Loop}(A, x, y, n, mid + 1, i') \\ & 7 & \text{ sync} \end{aligned}
```



```
 \begin{aligned} & \text{Mat-Vec-Main-Loop}(A, x, y, n, i, i') \\ 1 & & \text{if } i == i' \\ 2 & & \text{for } j = 1 \text{ to } n \\ 3 & & y_i = y_i + a_{ij}x_j \\ 4 & & \text{else } mid = \lfloor (i+i')/2 \rfloor \\ 5 & & \text{spawn Mat-Vec-Main-Loop}(A, x, y, n, i, mid) \\ 6 & & \text{Mat-Vec-Main-Loop}(A, x, y, n, mid + 1, i') \\ 7 & & \text{sync} \end{aligned}
```





```
MAT-VEC-MAIN-LOOP (A, x, y, n, i, i')

1 if i == i'
```

$$\begin{array}{ll}
\mathbf{2} & \mathbf{for} \ j = 1 \ \mathbf{to} \ n \\
y_i = y_i + a_{ij} x_j
\end{array}$$

- 4 else  $mid = \lfloor (i+i')/2 \rfloor$ 5 spawn MAT-VEC-MAIN-LOOP(A, x, y, n, i, mid)
- 6 MAT-VEC-MAIN-LOOP (A, x, y, n, t, mid + 1, i')
- 7 sync

#### MAT-VEC(A, x)

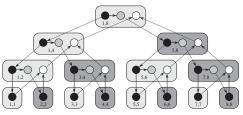
```
1 n = A.rows
2 let y be a new vector of length n
```

parallel for 
$$i = 1$$
 to  $n$ 

$$y_i = 0$$
  
parallel for  $i = 1$  to  $n$ 

for 
$$j = 1$$
 to  $n$ 

$$y_i = y_i + a_{ij}x_j$$



```
\begin{array}{lll} \text{Mat-Vec-Main-Loop}(A, x, y, n, i, i') \\ 1 & \text{if } i = i' \\ 2 & \text{for } j = 1 \text{ to } n \\ 3 & y_i = y_i + a_{ij}x_j \\ 4 & \text{else } mid = \lfloor (i + i')/2 \rfloor \\ 5 & \text{spawn Mat-Vec-Main-Loop}(A, x, y, n, i, mid) \\ 6 & \text{Mat-Vec-Main-Loop}(A, x, y, n, mid + 1, i') \\ 7 & \text{sync} \end{array}
```

```
MAT-VEC(A, x)

1 n = A.rows

2 let y be a new vector of length n

3 parallel for i = 1 to n

4 y_i = 0

5 parallel for i = 1 to n

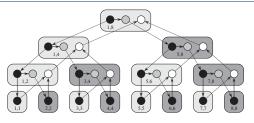
6 for j = 1 to n

7 y_i = y_i + a_{ij}x_j

8 return y
```

# $T_1(n) =$



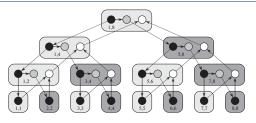


```
MAT-VEC(A, x)
MAT-VEC-MAIN-LOOP(A, x, y, n, i, i')
                                                                   n = A.rows
   if i == i'
                                                                   let y be a new vector of length n
       for i = 1 to n
                                                                   parallel for i = 1 to n
            v_i = v_i + a_{ii}x_i
                                                                        v_i = 0
   else mid = \lfloor (i + i')/2 \rfloor
                                                                   parallel for i = 1 to n
       spawn MAT-VEC-MAIN-LOOP(A, x, y, n, i, mid)
                                                                        for j = 1 to n
6
        MAT-VEC-MAIN-LOOP (A, x, y, n, mid + 1, i')
                                                                            v_i = v_i + a_{ii}x_i
       sync
                                                                   return v
```

$$T_1(n) =$$

Work is equal to running time of its serialization; overhead of recursive spawning does not change asymptotics.

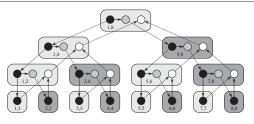




```
MAT-VEC(A, x)
MAT-VEC-MAIN-LOOP(A, x, y, n, i, i')
                                                                   n = A.rows
   if i == i'
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        MAT-VEC-MAIN-LOOP (A, x, y, n, mid + 1, i')
                                                                             v_i = v_i + a_{ii}x_i
       sync
                                                                   return v
```

$$T_1(n) = \Theta(n^2)$$

Work is equal to running time of its serialization; overhead of recursive spawning does not change asymptotics.

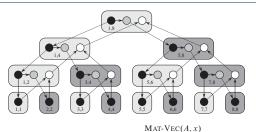


```
MAT-VEC(A, x)
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                                                                   n = A.rows
   if i == i'
                                                                   let y be a new vector of length n
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                                                                   parallel for i = 1 to n
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                                                                        v_i = 0
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                                                                        for j = 1 to n
6
        MAT-VEC-MAIN-LOOP (A, x, y, n, mid + 1, i')
                                                                            v_i = v_i + a_{ii}x_i
       sync
                                                                   return v
```

 $T_1(n) = \Theta(n^2)$  Work is equal to running time of its serialization; overhead of recursive spawning does not change asymptotics.

$$T_{\infty}(n) =$$





```
MAT-VEC-MAIN-LOOP(A, x, y, n, i, i')

1 if i = i'

2 for j = 1 to n

3 y_i = y_i + a_{ij}x_j

4 else mid = \lfloor (i + i')/2 \rfloor

5 spawn MAT-VEC-MAIN-LOOP(A, x, y, n, i, mid)

6 MAT-VEC-MAIN-LOOP(A, x, y, n, i, mid)

7 sync
```

1 
$$n = A.rows$$
  
2 let  $y$  be a new vector of length  $n$   
3 **parallel for**  $i = 1$  **to**  $n$   
4  $y_i = 0$   
5 **parallel for**  $i = 1$  **to**  $n$   
6 **for**  $i = 1$  **to**  $n$ 

for 
$$j = 1$$
 to  $n$   
 $y_i = y_i + a_{ij}x_j$   
return  $y$ 

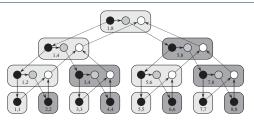
$$T_1(n) = \Theta(n^2)$$

Work is equal to running time of its serialization; overhead of recursive spawning does not change asymptotics.

$$T_{\infty}(n) =$$

Span is the depth of recursive callings plus the maximum span of any of the n iterations.



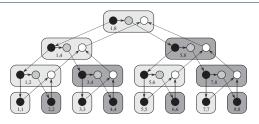


```
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       sync
                                                                 return v
```

 $T_1(n) = \Theta(n^2)$  Work is equal to running time of its serialization; overhead of recursive spawning does not change asymptotics.

$$T_{\infty}(n) = \Theta(\log n) + \max_{1 \le i \le n} \operatorname{iter}(n)$$
 Span is the depth of recursive callings plus the maximum span of any of the  $n$  iterations.





```
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                                                                 return v
```

Work is equal to running time of its serialization; overhead of recursive spawning does not change asymptotics.

$$T_{\infty}(n) = \Theta(\log n) + \max_{1 \le i \le n} \text{iter}(n)$$
  
=  $\Theta(n)$ .

Span is the depth of recursive callings plus the maximum span of any of the n iterations.



 $T_1(n) = \Theta(n^2)$ 

## **Naive Algorithm in Parallel**

```
P-SQUARE-MATRIX-MULTIPLY (A, B)

1  n = A.rows

2  let C be a new n \times n matrix

3  parallel for i = 1 to n

4  parallel for j = 1 to n

5  c_{ij} = 0

6  for k = 1 to n

7  c_{ij} = c_{ij} + a_{ik} \cdot b_{kj}

8  return C
```

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With a more careful implementation,

8  return C
```

```
P-SQUARE-MATRIX-MULTIPLY(A, B) has work T_1(n) = \Theta(n^3) and span T_{\infty}(n) = \Theta(n).
```

The first two nested for-loops parallelise perfectly.

```
P-MATRIX-MULTIPLY-RECURSIVE (C, A, B)
 1 \quad n = A.rows
 2 if n == 1
         c_{11} = a_{11}b_{11}
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          partition A, B, C, and T into n/2 \times n/2 submatrices
               A_{11}, A_{12}, A_{21}, A_{22}; B_{11}, B_{12}, B_{21}, B_{22}; C_{11}, C_{12}, C_{21}, C_{22};
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 8
          spawn P-MATRIX-MULTIPLY-RECURSIVE (C_{21}, A_{21}, B_{11})
          spawn P-MATRIX-MULTIPLY-RECURSIVE (C_{22}, A_{21}, B_{12})
 9
          spawn P-MATRIX-MULTIPLY-RECURSIVE (T_{11}, A_{12}, B_{21})
10
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11
12
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          P-MATRIX-MULTIPLY-RECURSIVE (T_{22}, A_{22}, B_{22})
14
          svnc
          parallel for i = 1 to n
15
16
               parallel for i = 1 to n
17
                   c_{ii} = c_{ii} + t_{ii}
```

```
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              parallel for i = 1 to n
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                  c_{ii} = c_{ii} + t_{ii}
                                                         The same as before.
```

P-MATRIX-MULTIPLY-RECURSIVE has work  $T_1(n) = \Theta(n^3)$  and span  $T_{\infty}(n) =$ 



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```

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Speedups for Matrix Inversion by an equivalence with Matrix Multiplication.



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Theorem 28.1 (Multiplication is no harder than Inversion)

If we can invert an  $n \times n$  matrix in time I(n), where  $I(n) = \Omega(n^2)$  and I(n) satisfies the regularity condition I(3n) = O(I(n)), then we can multiply two  $n \times n$  matrices in time O(I(n)).

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## Proof:

■ Define a 3*n* × 3*n* matrix *D* by:

$$D = \begin{pmatrix} I_n & A & 0 \\ 0 & I_n & B \\ 0 & 0 & I_n \end{pmatrix}$$

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• Matrix *D* can be constructed in  $\Theta(n^2) = O(I(n))$  time,

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If we can invert an  $n \times n$  matrix in time I(n), where  $I(n) = \Omega(n^2)$  and I(n) satisfies the regularity condition I(3n) = O(I(n)), then we can multiply two  $n \times n$  matrices in time O(I(n)).

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• Define a  $3n \times 3n$  matrix *D* by:

$$D = \begin{pmatrix} I_n & A & 0 \\ 0 & I_n & B \\ 0 & 0 & I_n \end{pmatrix} \qquad \Rightarrow \qquad D^{-1} = \begin{pmatrix} I_n & -A & AB \\ 0 & I_n & -B \\ 0 & 0 & I_n \end{pmatrix}.$$

- Matrix *D* can be constructed in  $\Theta(n^2) = O(I(n))$  time,
- and we can invert D in O(I(3n)) = O(I(n)) time.



Speedups for Matrix Inversion by an equivalence with Matrix Multiplication.

Theorem 28.1 (Multiplication is no harder than Inversion) -

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- and we can invert D in O(I(3n)) = O(I(n)) time.
- $\Rightarrow$  We can compute AB in O(I(n)) time.



### The Other Direction

### Theorem 28.1 (Multiplication is no harder than Inversion)

If we can invert an  $n \times n$  matrix in time I(n), where  $I(n) = \Omega(n^2)$  and I(n) satisfies the regularity condition I(3n) = O(I(n)), then we can multiply two  $n \times n$  matrices in time O(I(n)).

## - Theorem 28.2 (Inversion is no harder than Multiplication) -

Suppose we can multiply two  $n \times n$  real matrices in time M(n) and M(n) satisfies the two regularity conditions M(n+k) = O(M(n)) for any  $0 \le k \le n$  and  $M(n/2) \le c \cdot M(n)$  for some constant c < 1/2. Then we can compute the inverse of any real nonsingular  $n \times n$  matrix in time O(M(n)).

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Allows us to use Strassen's Algorithm to invert a matrix!

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