location, x, m integer, n thread_id, t	address integer thread id		
memory, M	::=		memory (function from addresses to integers)
expression, e	::= 	n x $x = e$ $e; e'$ $e + e'$ $lock x$ $unlock x$	expression integer literal read from address x write value of e to address x sequential composition plus lock mutex at address x unlock mutex at address x
process, p	::= 	t:e p p'	process thread parallel composition
state, s	::= 	$\langle p, M \rangle$	state process p and memory M
label, l	::= 	W x=n R x=n LOCK x UNLOCK x τ	label write read lock unlock internal action (tau)
$thread_label, t:$	l ::=	t: l	thread label label

$$e \xrightarrow{l} e'$$

 $\fbox{e \xrightarrow{l} e'} e \text{ does } l \text{ to become } e'$

$$\frac{\overline{x \xrightarrow{\mathbb{R}x=n} n}}{x \xrightarrow{\mathbb{R}x=n} n} \xrightarrow{\text{READ}} WRITE$$

$$\frac{\overline{x=n \xrightarrow{\mathbb{W}x=n} n}}{x = n \xrightarrow{\mathbb{W}x=n} n} WRITE$$

$$\frac{e^{1} \rightarrow e'}{x = e^{-1} x = e^{-1}} \xrightarrow{\text{WRITE}_{-}\text{CONTEXT}}$$

$$\frac{e_{1} \xrightarrow{l} e_{1}'}{e_{1}; e_{2} \xrightarrow{l} e_{1}'; e_{2}} \xrightarrow{\text{SEQ}_{-}\text{CONTEXT}}$$

$$\frac{n = n_{1} + n_{2}}{n_{1} + n_{2} \xrightarrow{\tau} n} \xrightarrow{\text{PLUS}_{-}\text{CONTEXT}_{-1}}$$

$$\frac{e_{1} \xrightarrow{l} e_{1}'}{e_{1} + e_{2} \xrightarrow{l} e_{1}' + e_{2}} \xrightarrow{\text{PLUS}_{-}\text{CONTEXT}_{-1}}$$

$$\frac{e_2 \xrightarrow{l} e'_2}{n_1 + e_2 \xrightarrow{l} n_1 + e'_2} \quad \text{PLUS_CONTEXT_2}$$

$$\frac{1}{\text{lock } x \xrightarrow{\text{LOCK } x} 0} \quad \text{LOCK}$$

$$\frac{1}{\text{unlock } x \xrightarrow{\text{UNLOCK } x} 0} \quad \text{UNLOCK}$$

$$p \xrightarrow{t:l} p'$$

p does t: l to become p'

$$\frac{e \xrightarrow{l} e'}{t:e \xrightarrow{t:l} t:e'} \text{ THREAD}$$

$$\frac{p_1 \xrightarrow{t:l} p_1'}{p_1 | p_2 \xrightarrow{t:l} p_1' | p_2} \text{ PAR_CONTEXT_LEFT}$$

$$\frac{p_2 \xrightarrow{t:l} p_2'}{p_1 | p_2 \xrightarrow{t:l} p_1 | p_2'} \text{ PAR_CONTEXT_RIGHT}$$

$$M \xrightarrow{t:l} M'$$

M does t:l to become M^\prime

$$\frac{M(x) = n}{M \xrightarrow{t: \mathbb{R} x = n} M} \text{ MREAD}$$

$$\overline{M \xrightarrow{t: \mathbb{W} x = n} M \oplus (x \mapsto n)} \text{ MWRITE}$$

$$\frac{M(x) = 1}{M \xrightarrow{t: \text{LOCK } x} M \oplus (x \mapsto 0)} \text{ MLOCK}$$

$$\overline{M \xrightarrow{t: \text{UNLOCK } x} M \oplus (x \mapsto 1)} \text{ MUNLOCK}$$

$$s \xrightarrow{t:l} s'$$

s does t:l to become s'

$$\frac{p \xrightarrow{t:l} p'}{M \xrightarrow{t:l} M'} \quad \text{Ssync}$$

$$\frac{p \xrightarrow{t:l} \langle p', M' \rangle}{\langle p, M \rangle \xrightarrow{t:l} \langle p', M' \rangle} \quad \text{Stau}$$

$$\frac{p \xrightarrow{t:\tau} p'}{\langle p, M \rangle \xrightarrow{t:\tau} \langle p', M \rangle} \quad \text{Stau}$$

Definition rules: 19 good 0 bad Definition rule clauses: 32 good 0 bad