

Multicore Programming

Queues, memory, and the ABA problem

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Fine-grain parallel tasks

Work stealing queues & ABA

Memory management

A really bad way to compute fib(n)

```
int fib(int x) {  
    if (x<2) {  
        return x;  
    } else {  
        int f1 = fib(x-1);  
        int f2 = fib(x-2);  
        return f1+f2;  
    }  
}
```

Base case for $x < 2$

Recursion for $x \geq 2$

A really bad way to compute fib(n)

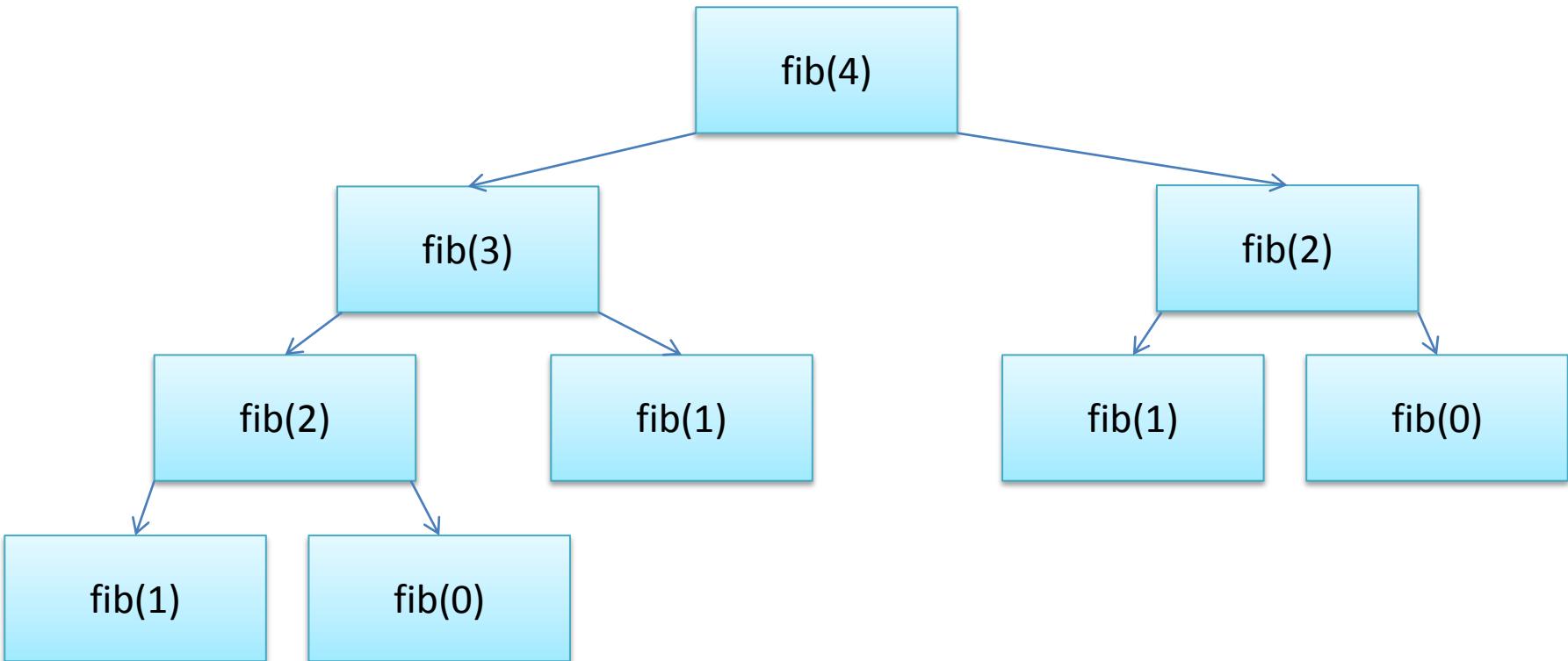
```
int fib(int x) {  
    if (x<2) {  
        return x;  
    } else {  
        int f1 = spawn fib(x-1);  
        int f2 = spawn fib(x-2);  
        sync;  
        return f1+f2;  
    }  
}
```

Spawn: this can run in parallel

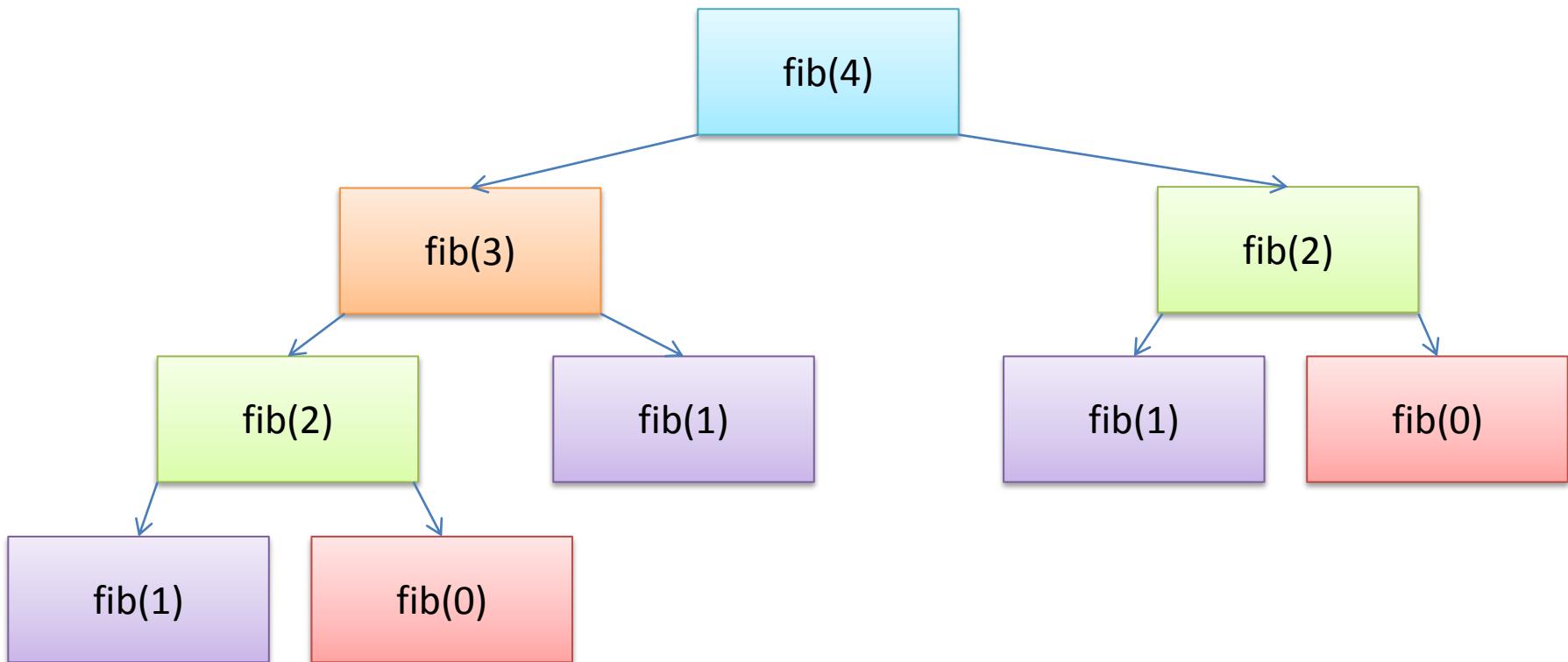
Sync: wait for the spawned work to be done

Recursive fib(n)

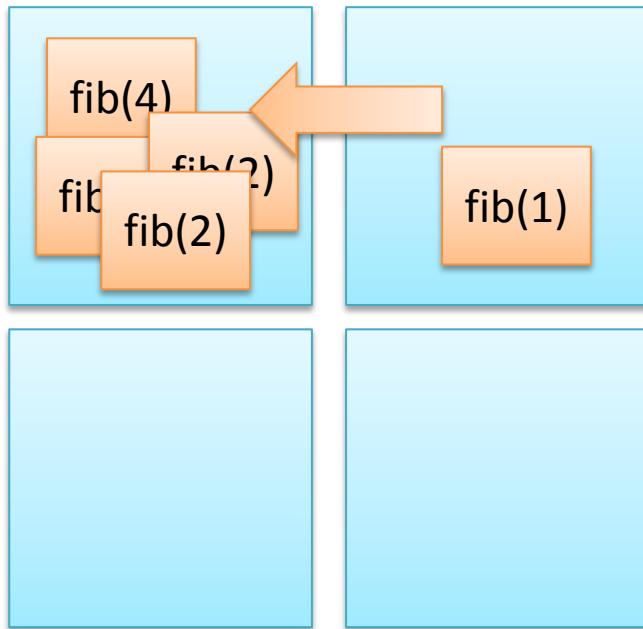
$$T_{\infty} = 4$$
$$T_1 = 9$$



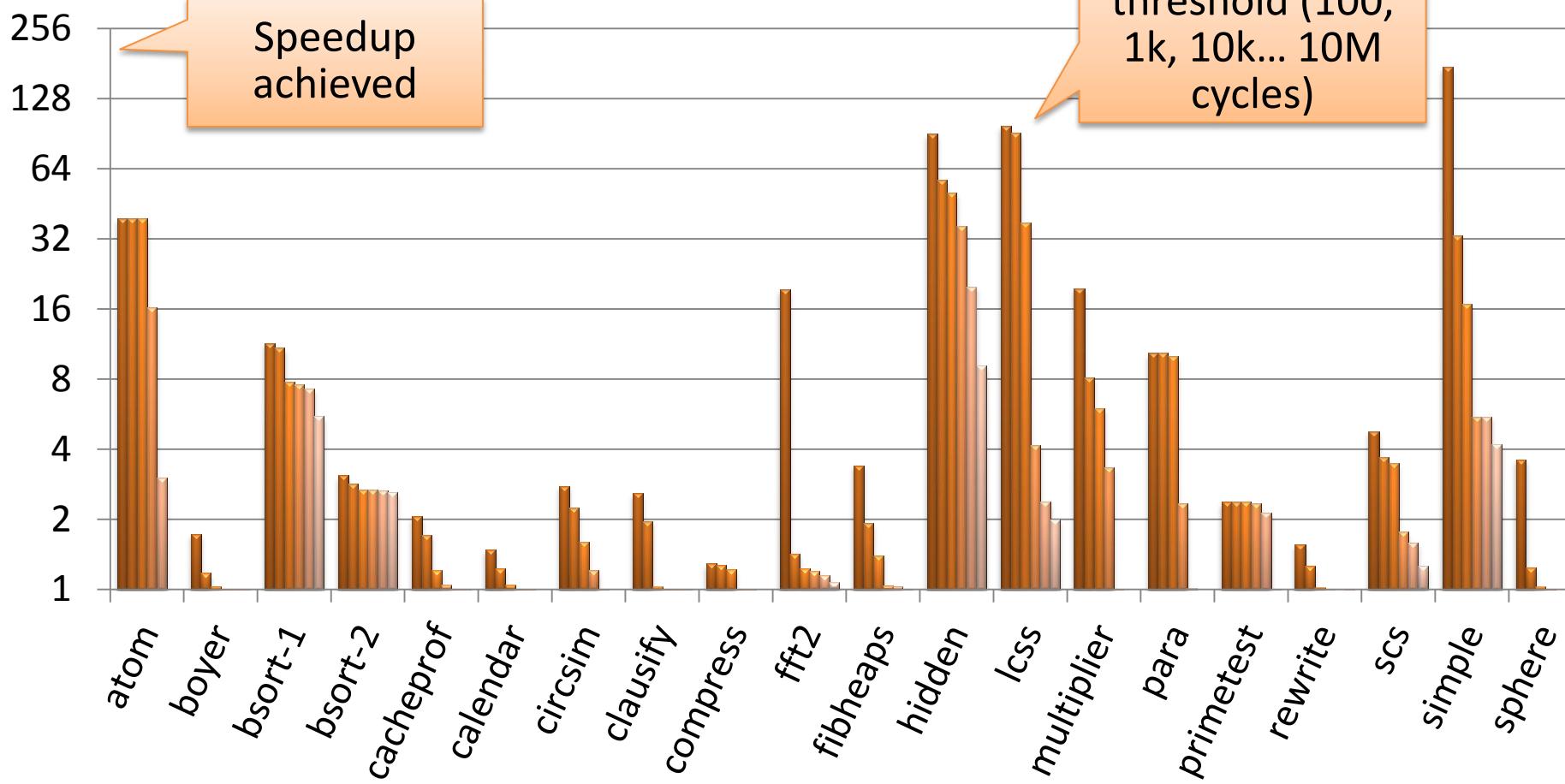
Recursive fib(n)



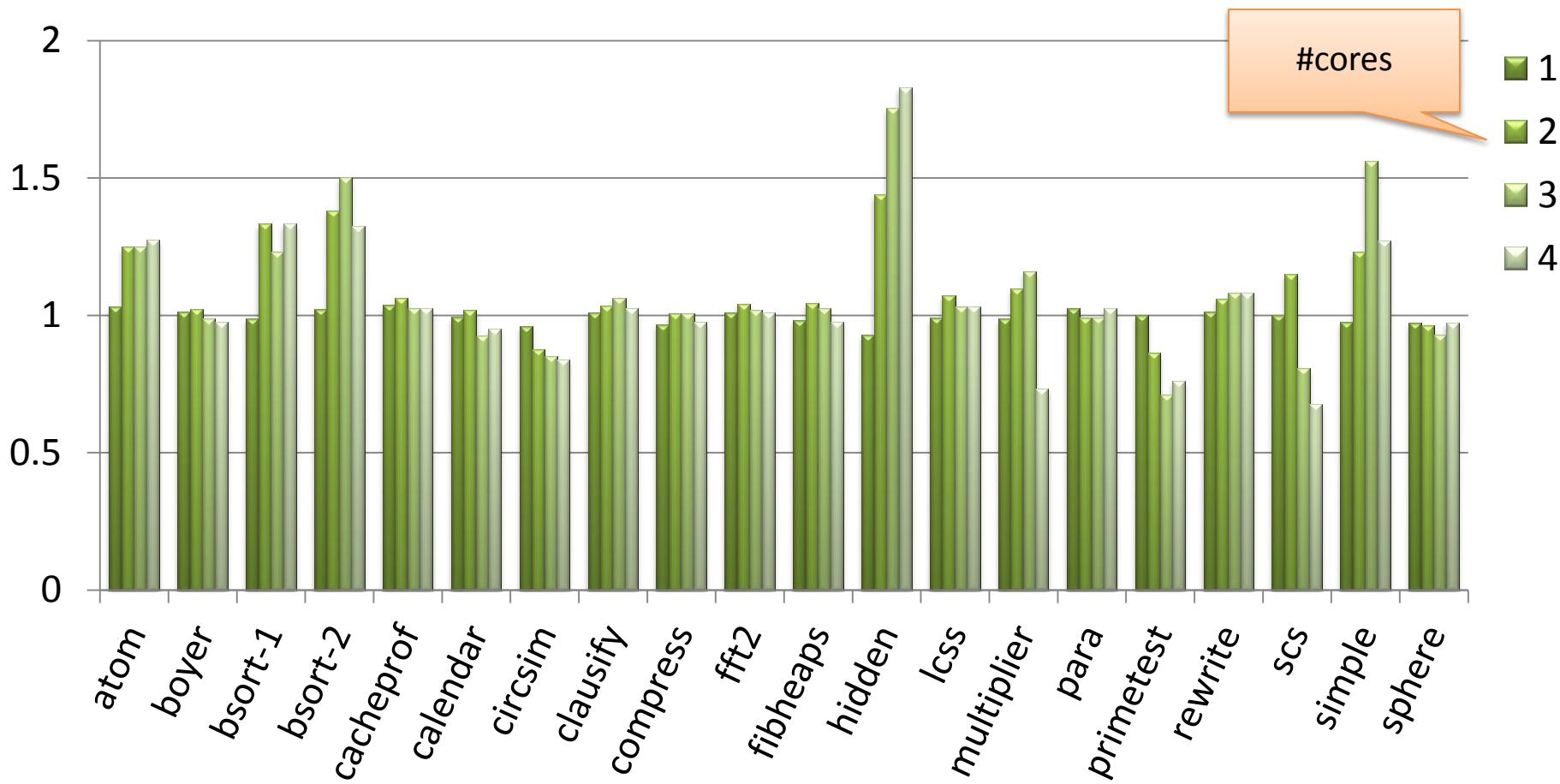
Work stealing implementation



Limit study



Measured performance



Getting good performance

- Amdahl's law
- Granularity of work items
- Overheads of the stealing mechanisms
- Interference between items

Fine-grain parallel tasks

Work stealing queues & ABA

Memory management

Work stealing queues

PopTop() -> Item

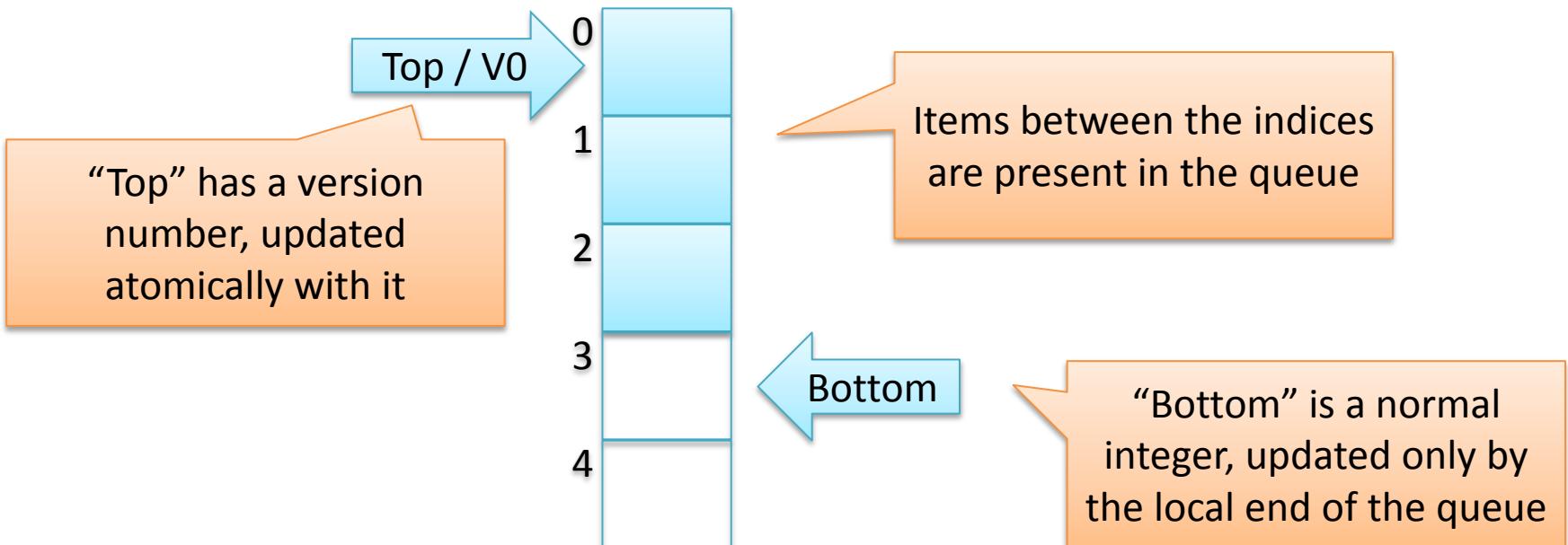
Try to steal an item.
May sometimes return

1. Semantics relaxed for “PopTop”

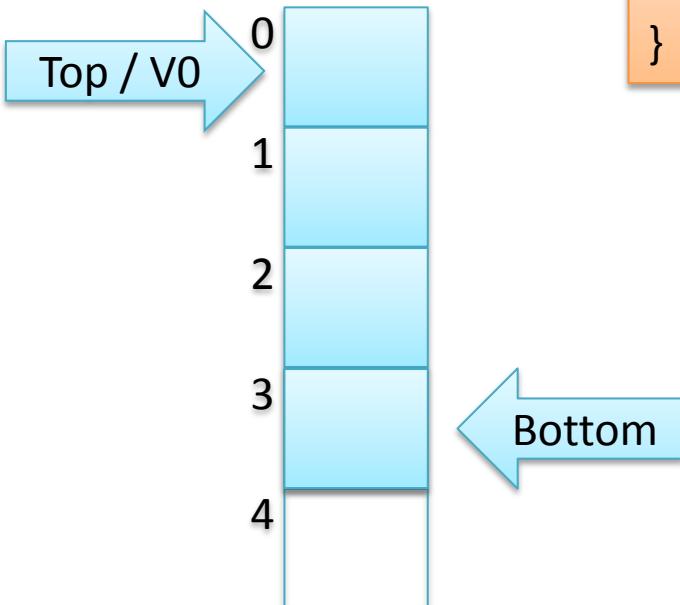
2. Restriction: only one thread ever calls “Push/PopBottom”

3. Implementation costs skewed toward “PopTop” complex

Bounded deque

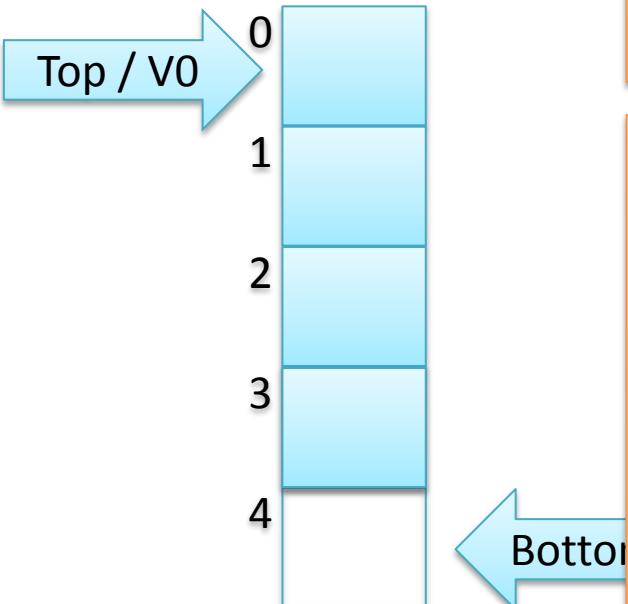


Bounded deque



```
void PushBottom(Item i){  
    tasks[bottom] = i;  
    bottom++;  
}
```

Bounded deque

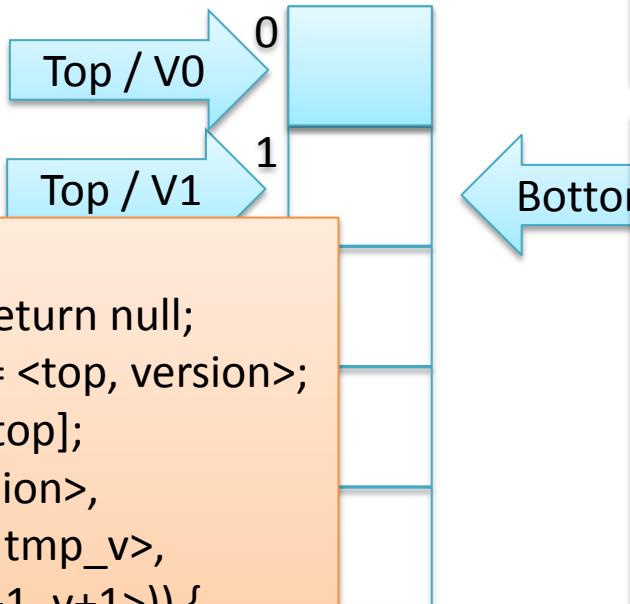


```
void PushBottom(Item i){  
    tasks[bottom] = i;  
    bottom++;  
}
```

```
Item popBottom() {  
    if (bottom == 0) return null;  
    bottom--;  
    result = tasks[bottom];  
    <tmp_top,tmp_v> = <top,version>;  
    if (bottom > tmp_top) return result;  
    ....  
    return null;  
}
```

Bounded deque

```
Item PopTop() {  
    if (bottom <= top) return null;  
    <tmp_top,tmp_v> = <top, version>;  
    result = tasks[tmp_top];  
    if (CAS( &<top,version>,  
            <tmp_top, tmp_v>,  
            <tmp_top+1, v+1>)) {  
        return result;  
    }  
    return null;  
}
```

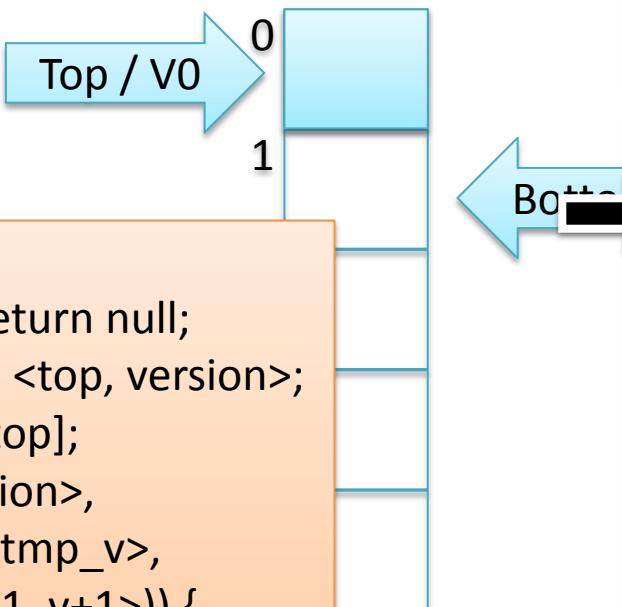


```
void PushBottom(Item i){  
    tasks[bottom] = i;  
    bottom++;  
}
```

```
Item popBottom() {  
    if (bottom==top) {  
        bottom = 0;  
        if (CAS( &<top,version>,  
                <tmp_top,tmp_v>,  
                <0,v+1>)) {  
            return result;  
        }  
    }  
    <top,version>=<0,v+1>
```

Bounded deque

```
Item PopTop() {
    if (bottom <= top) return null;
    <tmp_top,tmp_v> = <top, version>;
    result = tasks[tmp_top];
    if (CAS( &<top,version>,
            <tmp_top, tmp_v>,
            <tmp_top+1, v+1>)) {
        return result;
    }
    return null;
}
```



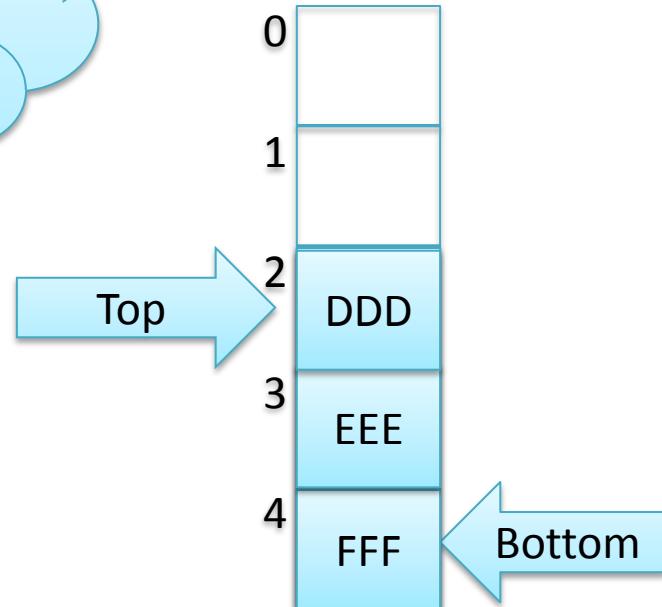
```
void PushBottom(Item i){
    tasks[bottom] = i;
    bottom++;
}
```

```
Item popBottom() {
    if (bottom==top) {
        bottom = 0;
        if (CAS( &<top,version>,
                  <tmp_top,tmp_v>,
                  <0,v+1>)) {
            return result;
        }
    }
    <top,version>=<0,v+1>
```

ABA problems

```
Item PopTop() {  
    if (bottom <= top) return null;  
    tmp_top = top;  
    result = tasks[tmp_top];  
    if (CAS(&top, top, top+1)) {  
        return result;  
    }  
    return null;  
}
```

result = CCC



General techniques

- Local operations designed to avoid CAS
 - Traditionally slower, less so now
- Local operations just use read and write
 - Only one accessor, check for interference
- Use CAS:
 - Resolve conflicts between stealers
 - Resolve local/stealer conflicts
 - Version number to ensure conflicts seen

Fine-grain parallel tasks

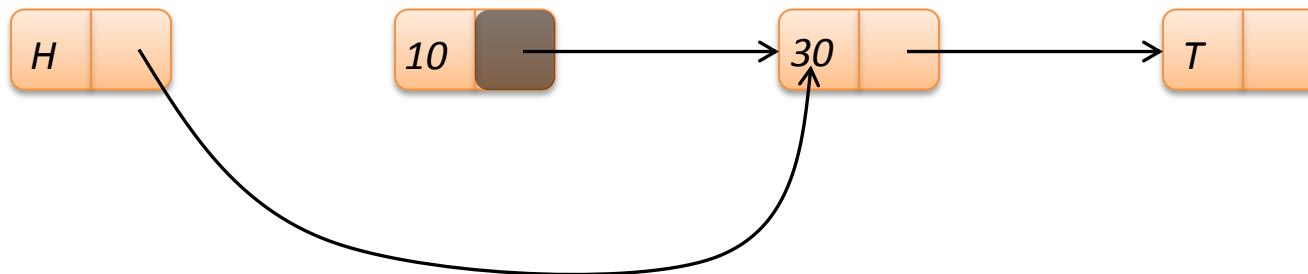
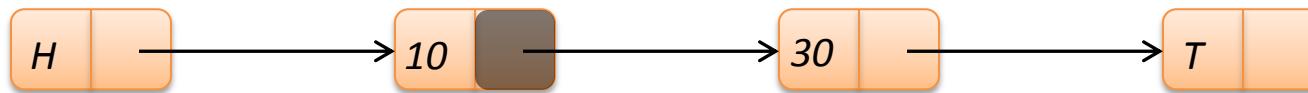
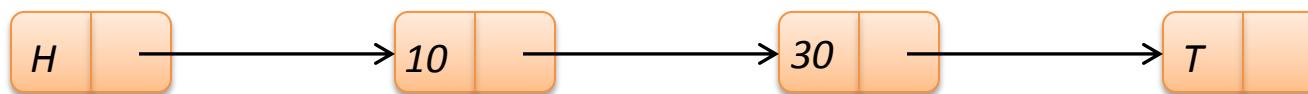
Work stealing queues & ABA

Memory management

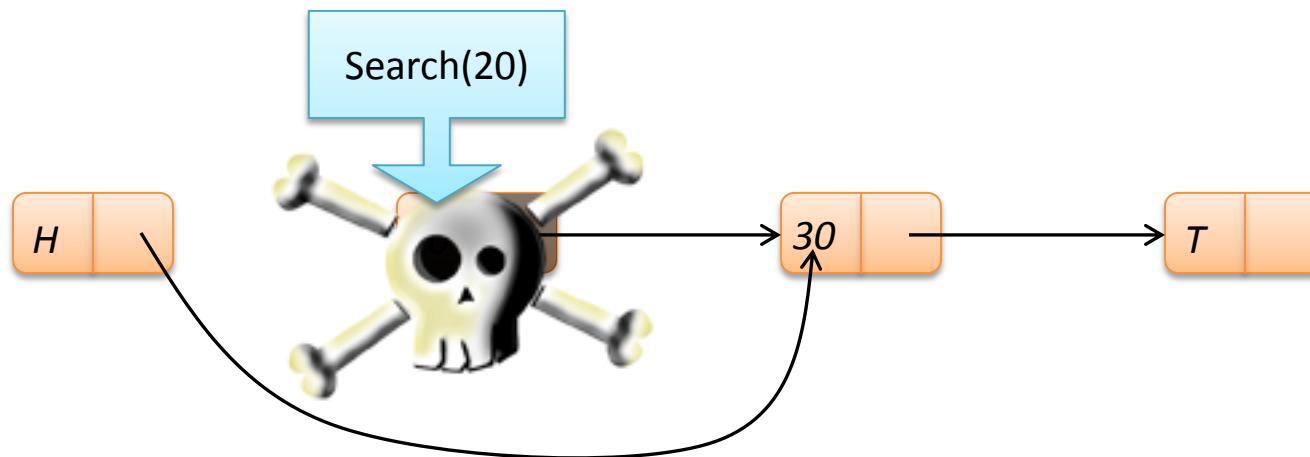
Lock-free data structures in C

- Java:
 - Explicit memory allocation
 - Deallocation by GC
- C/C++:
 - Explicit memory allocation & deallocation
- When is it safe to deallocate a piece of memory?

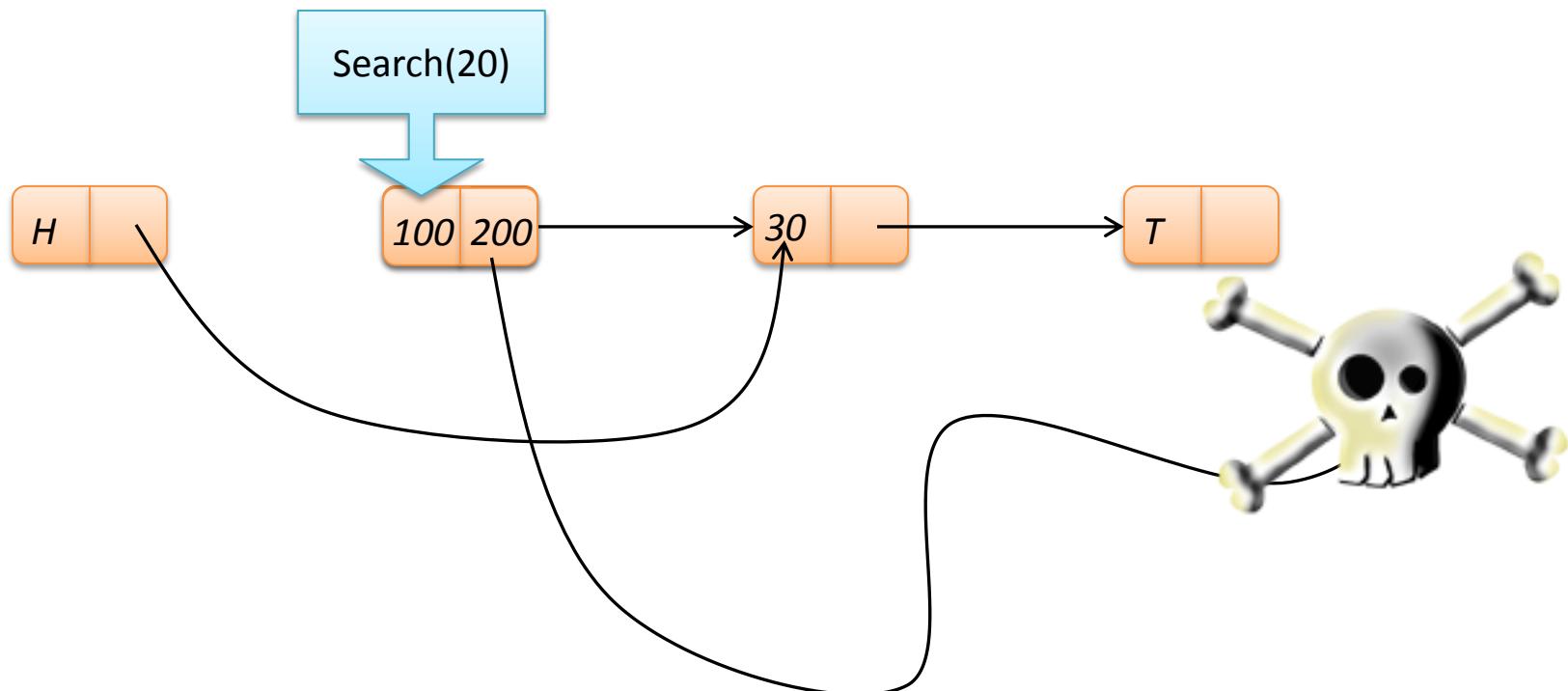
Deletion revisited: Delete(10)



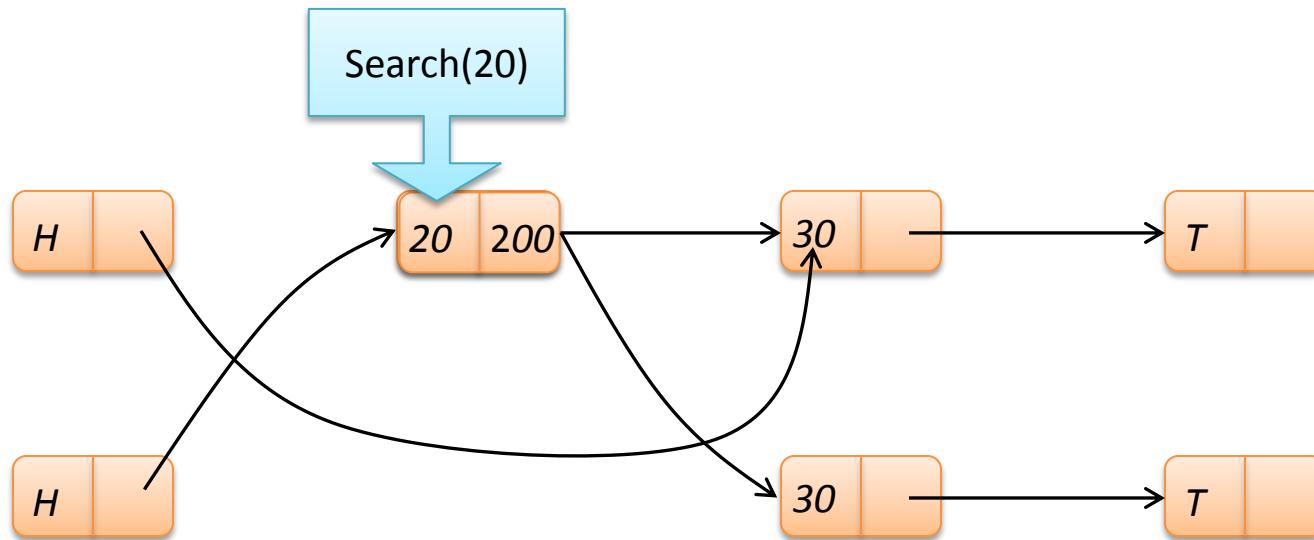
De-allocate to the OS?



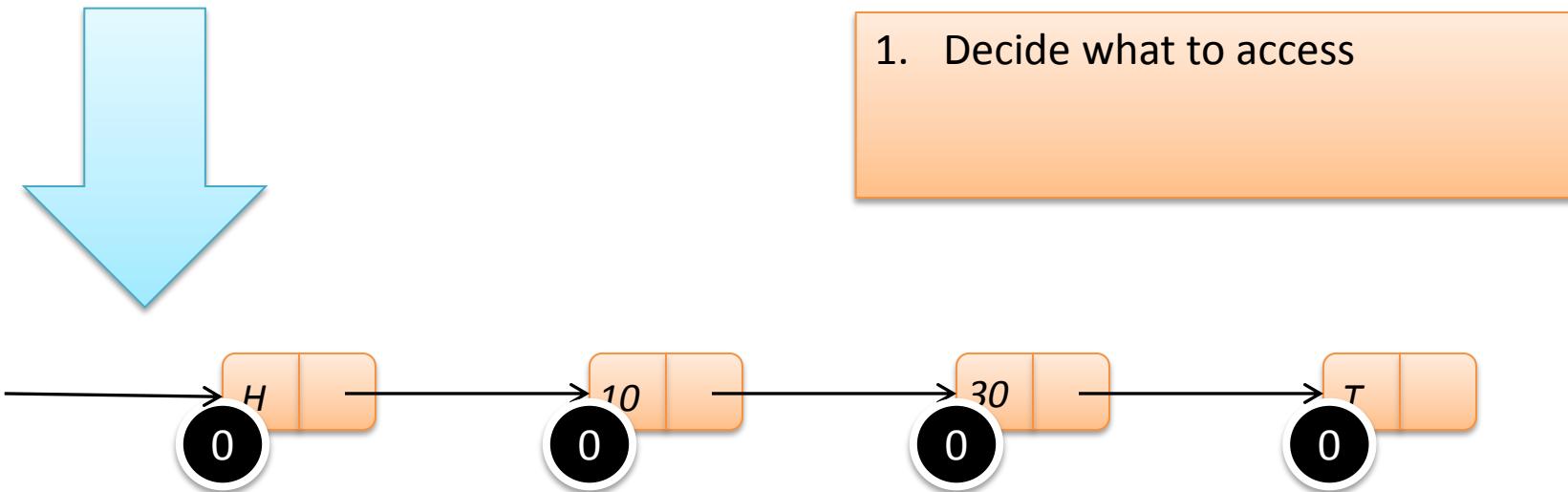
Re-use as something else?



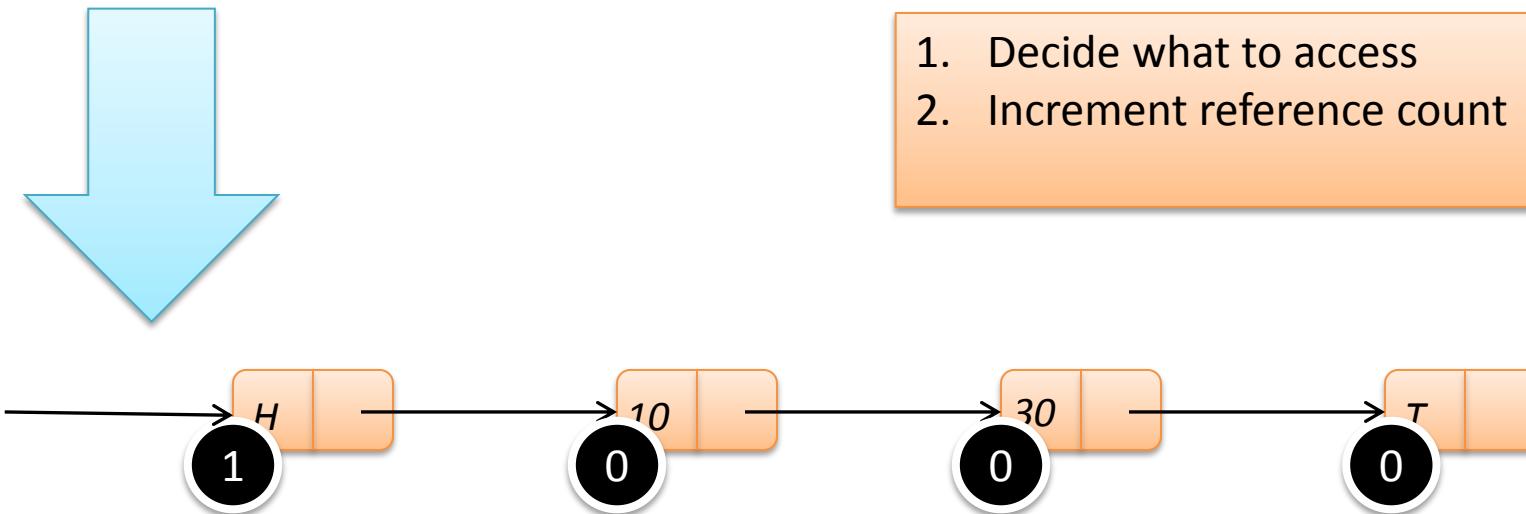
Re-use as a list node?



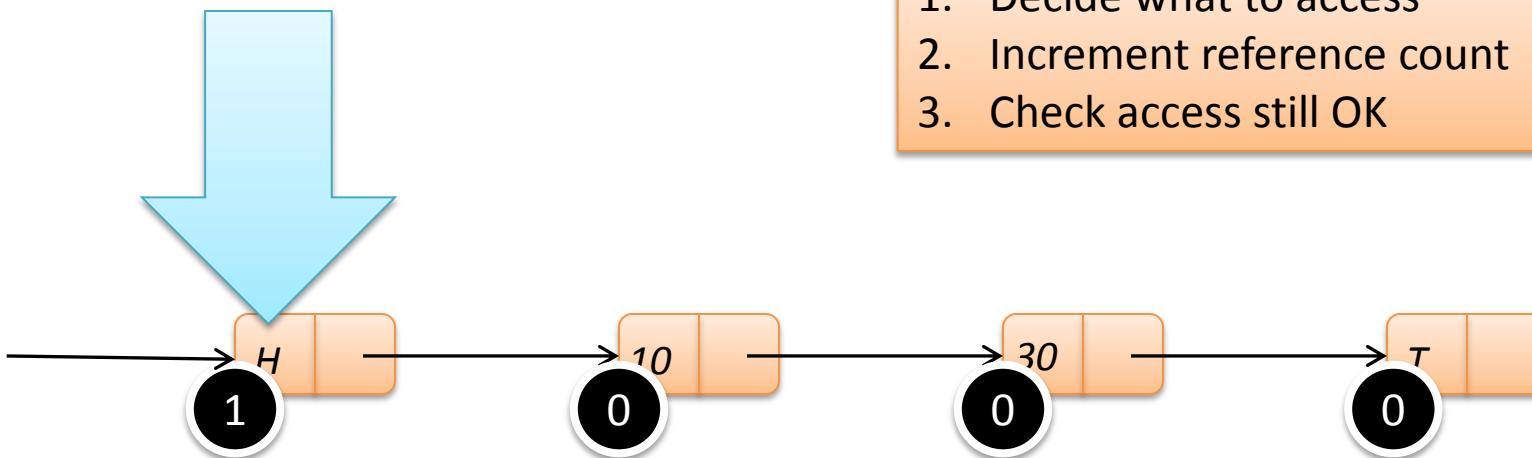
Reference counting



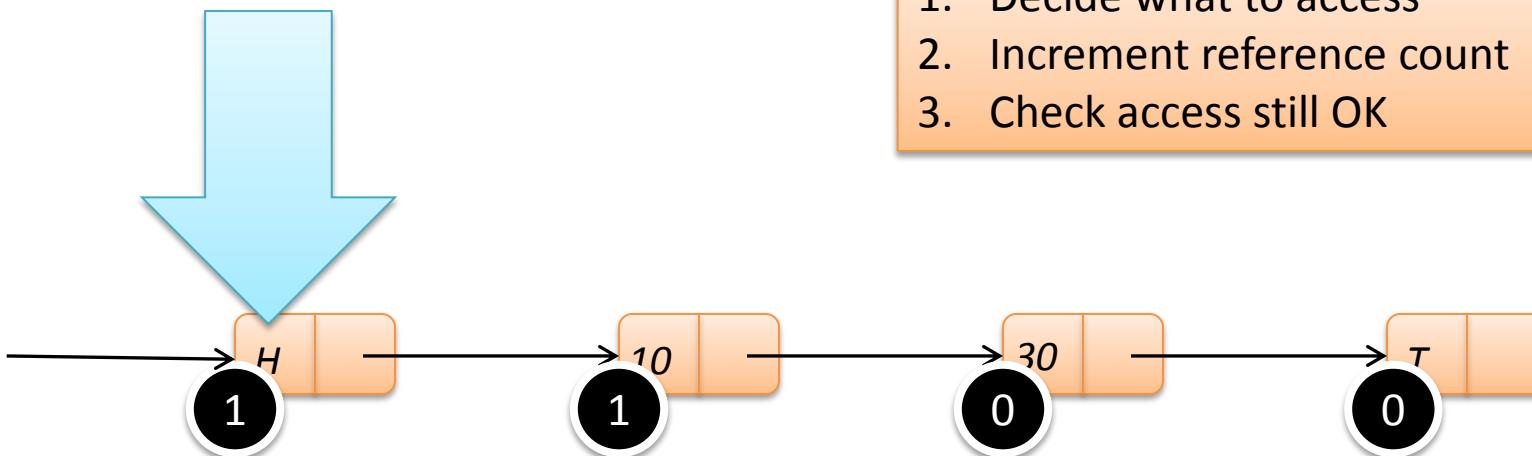
Reference counting



Reference counting

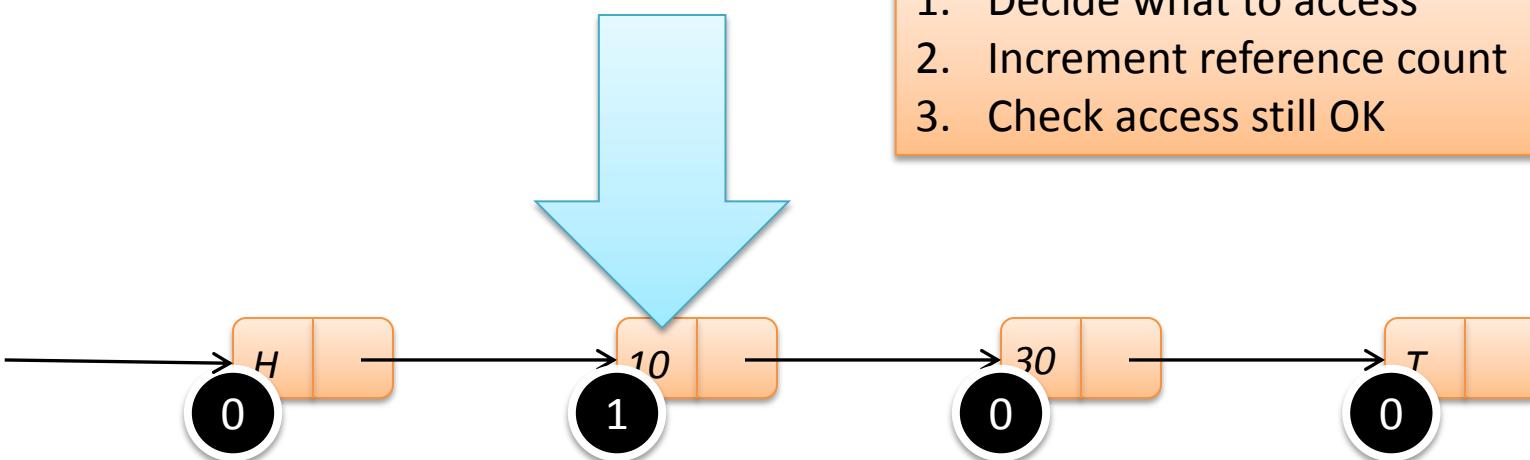


Reference counting

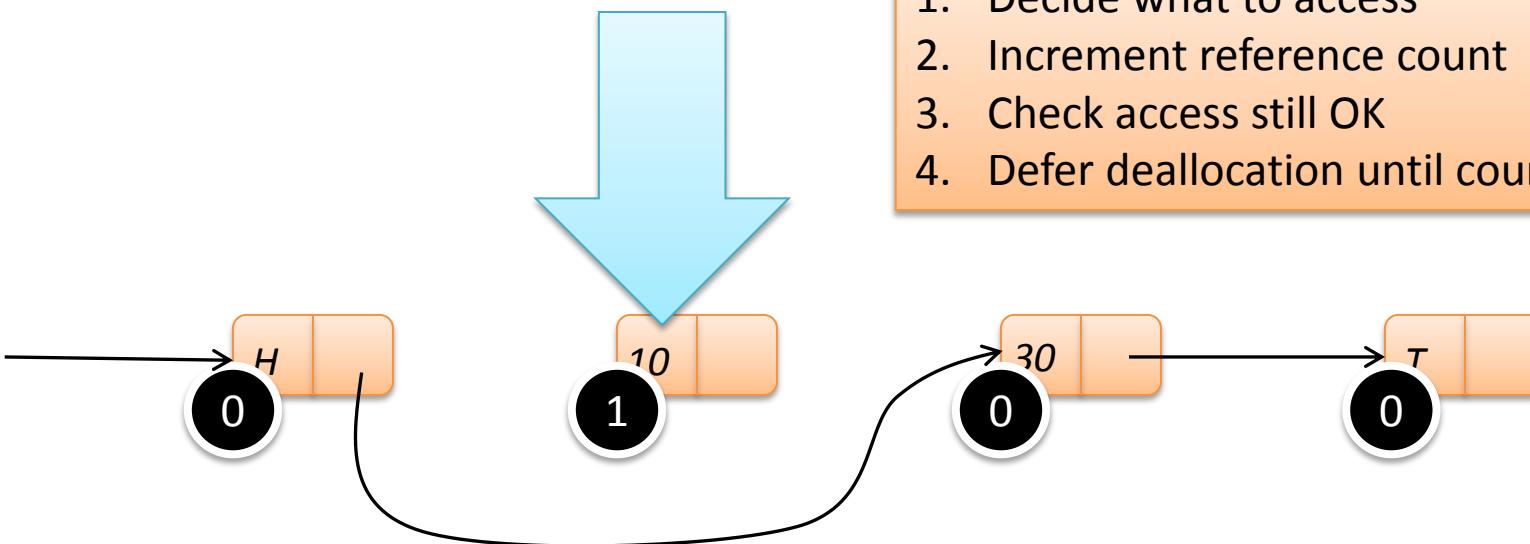


1. Decide what to access
2. Increment reference count
3. Check access still OK

Reference counting



Reference counting



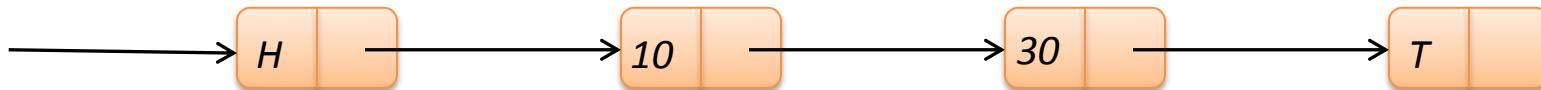
1. Decide what to access
2. Increment reference count
3. Check access still OK
4. Defer deallocation until count 0

Epoch mechanisms

Global epoch: 1000

Thread 1 epoch: -

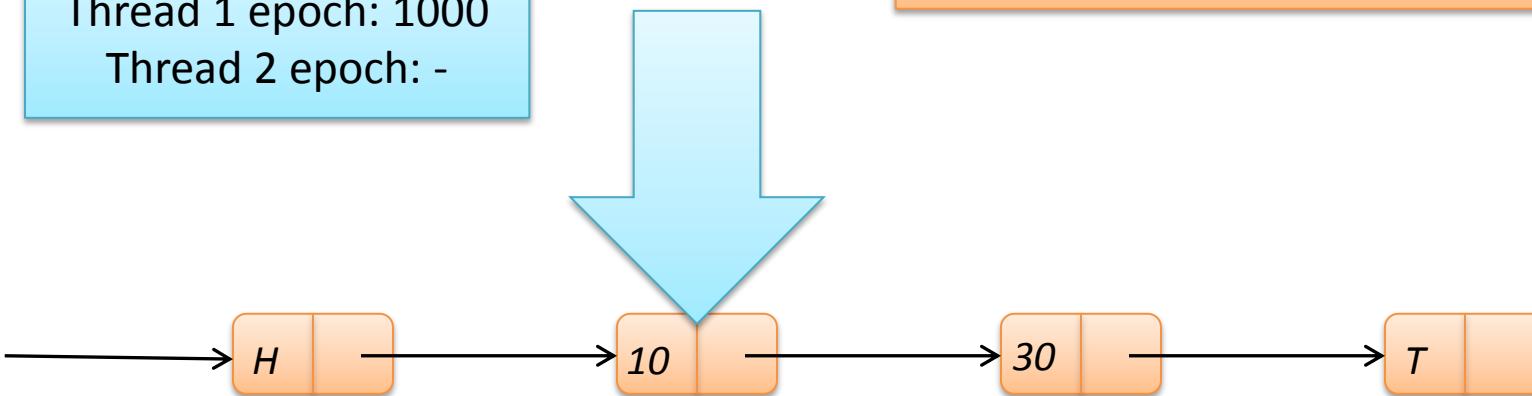
Thread 2 epoch: -



Epoch mechanisms

Global epoch: 1000
Thread 1 epoch: 1000
Thread 2 epoch: -

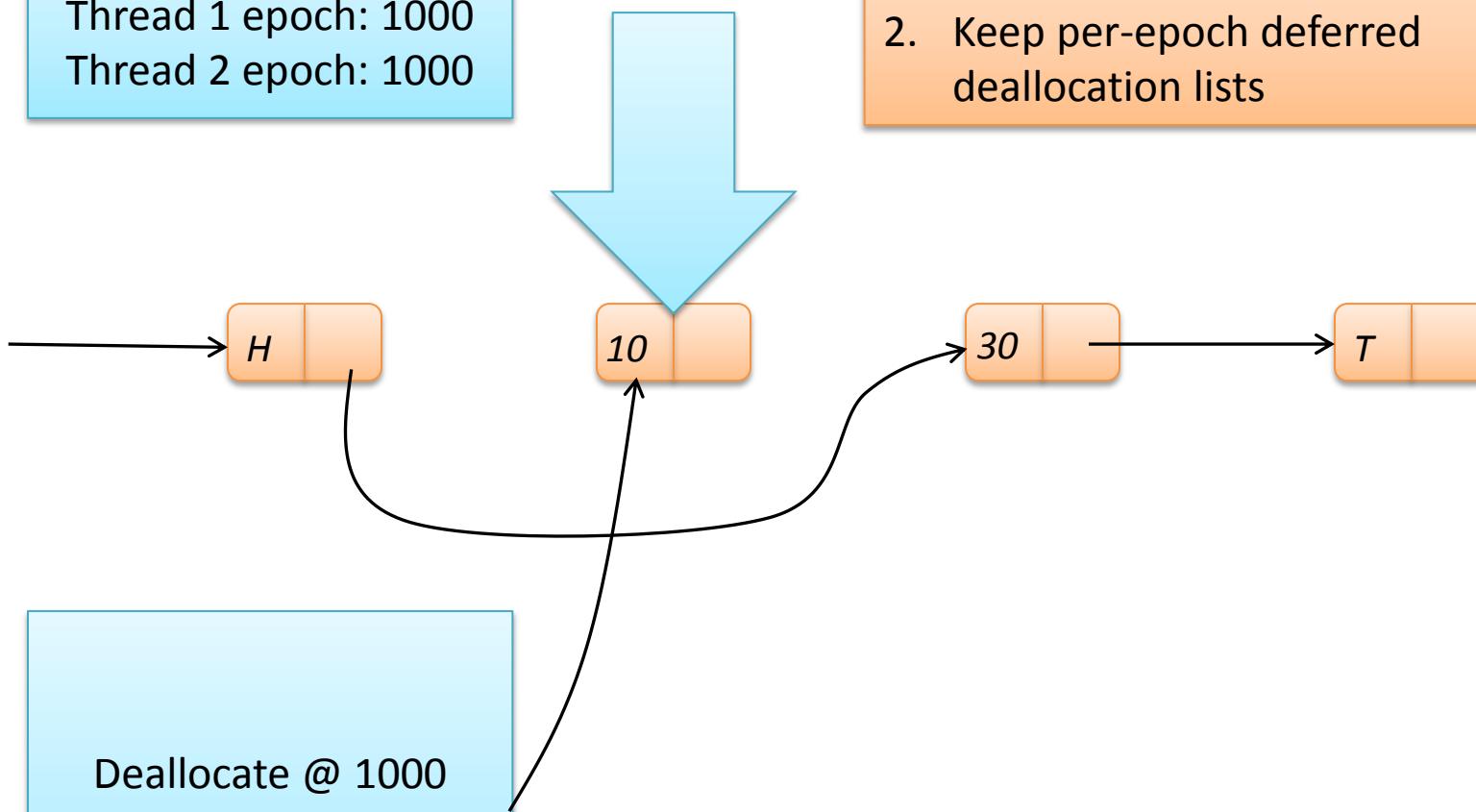
1. Record global epoch at start of operation



Epoch mechanisms

Global epoch: 1000
Thread 1 epoch: 1000
Thread 2 epoch: 1000

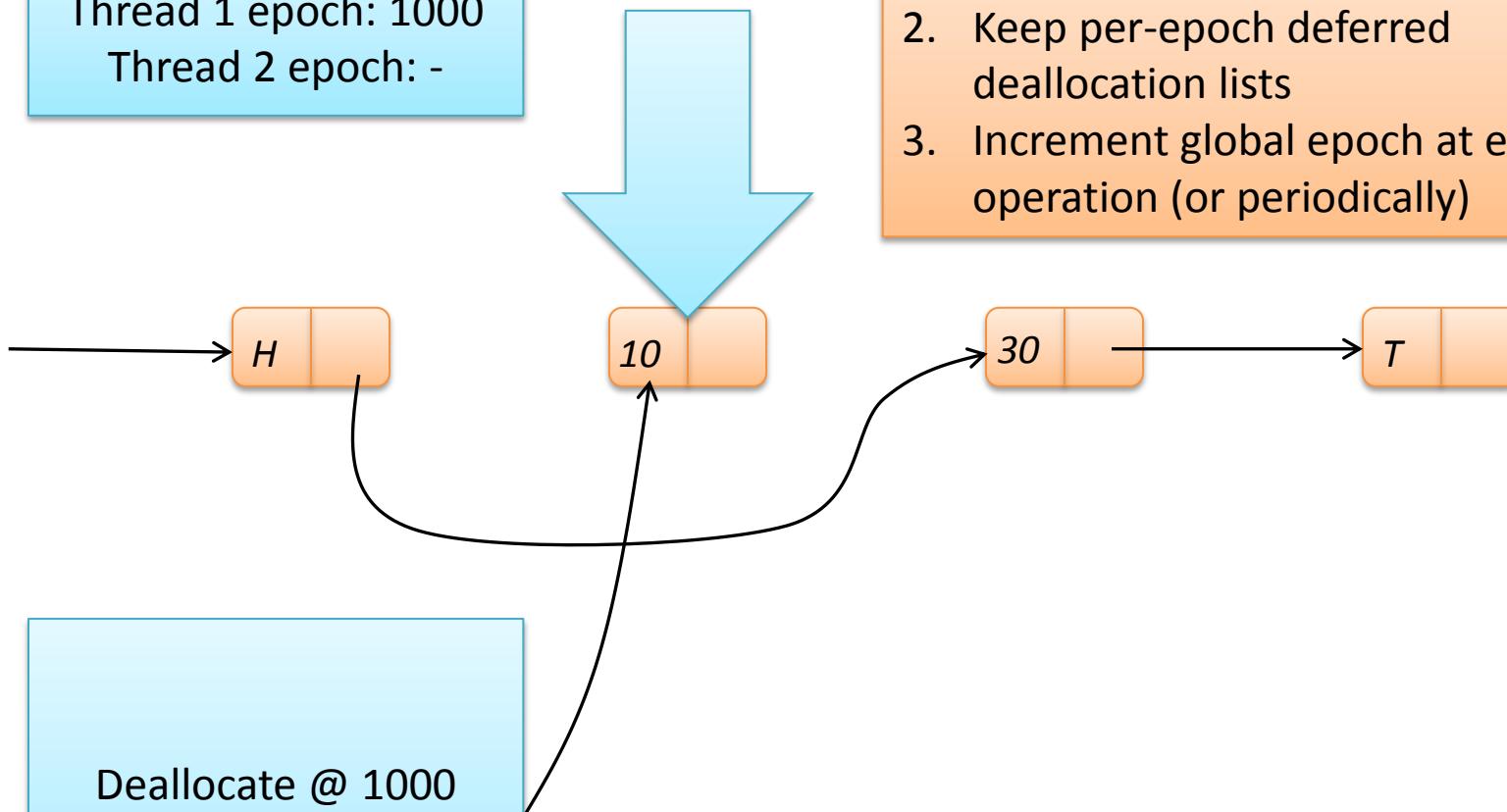
1. Record global epoch at start of operation
2. Keep per-epoch deferred deallocation lists



Epoch mechanisms

Global epoch: 1001
Thread 1 epoch: 1000
Thread 2 epoch: -

1. Record global epoch at start of operation
2. Keep per-epoch deferred deallocation lists
3. Increment global epoch at end of operation (or periodically)



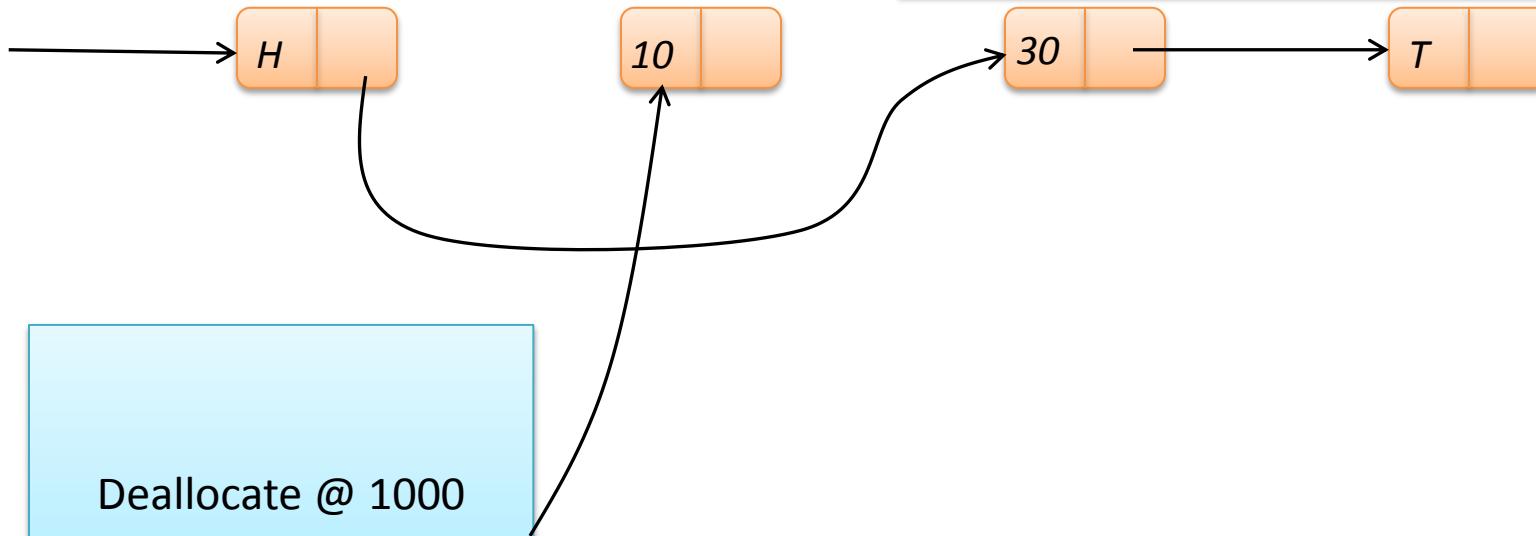
Epoch mechanisms

Global epoch: 1002

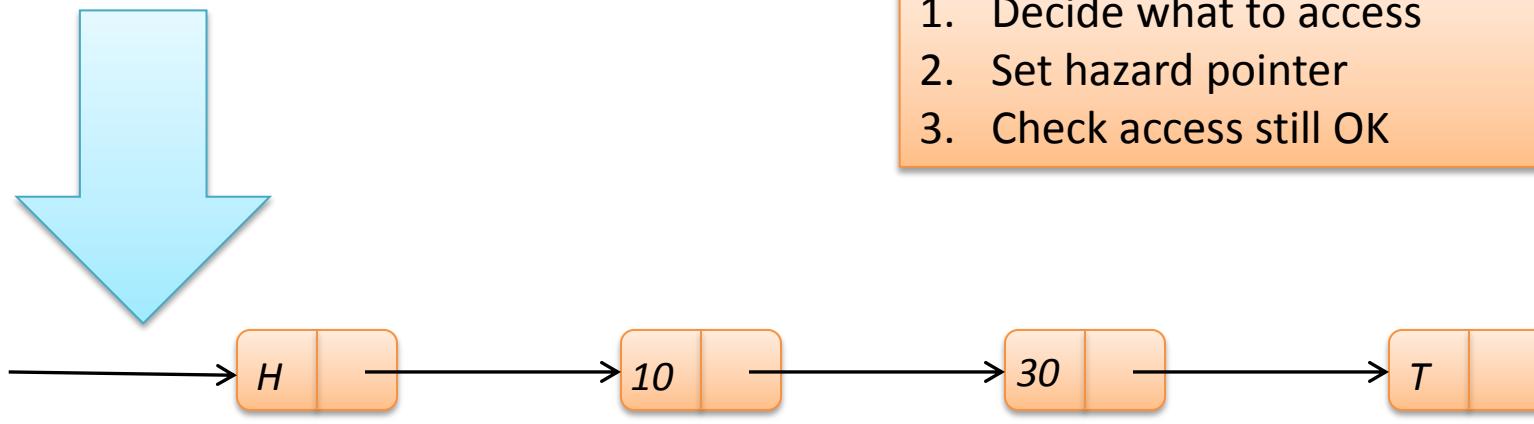
Thread 1 epoch: -

Thread 2 epoch: -

1. Record global epoch at start of operation
2. Keep per-epoch deferred deallocation lists
3. Increment global epoch at end of operation (or periodically)
4. Free when everyone past epoch

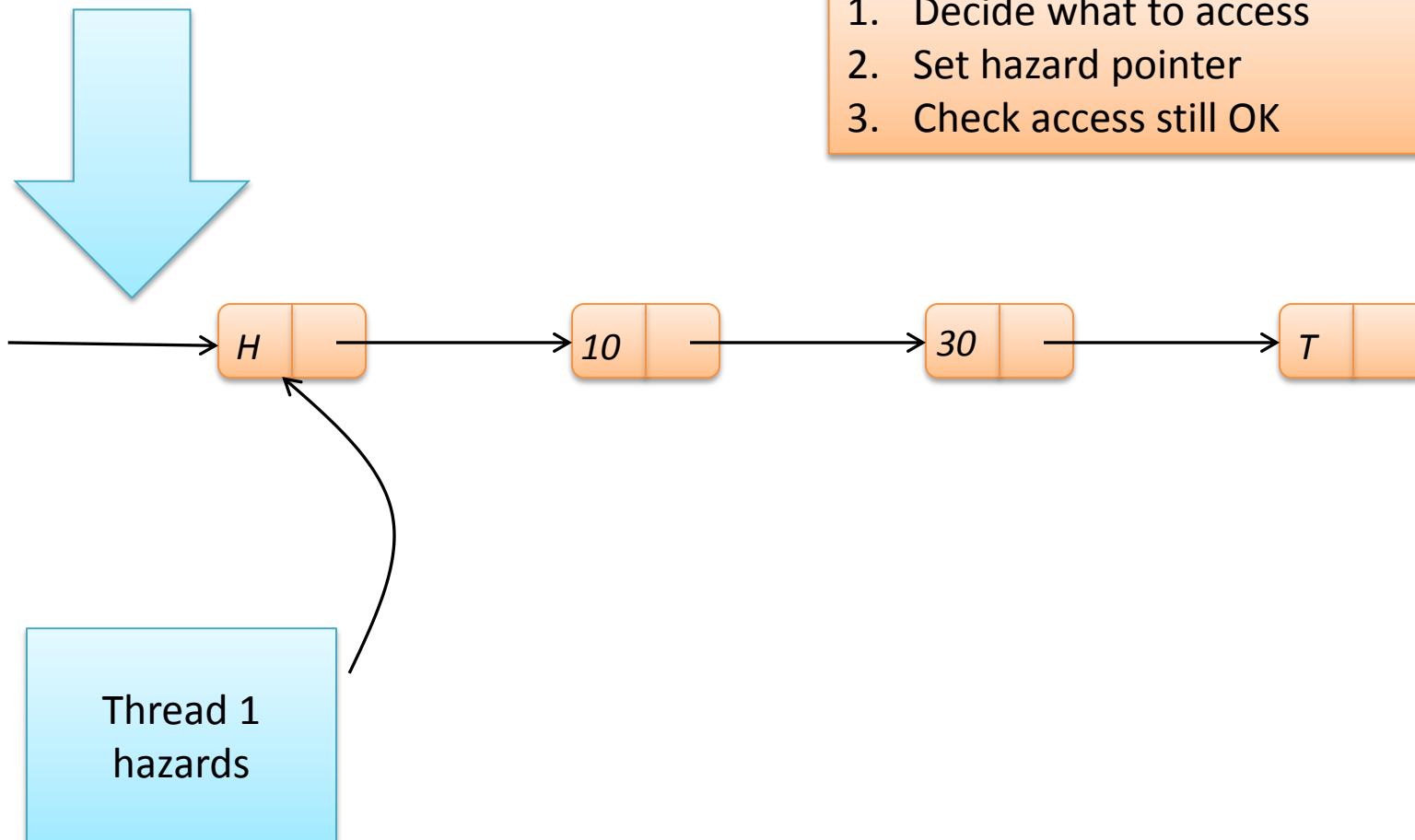


Hazard pointer mechanisms

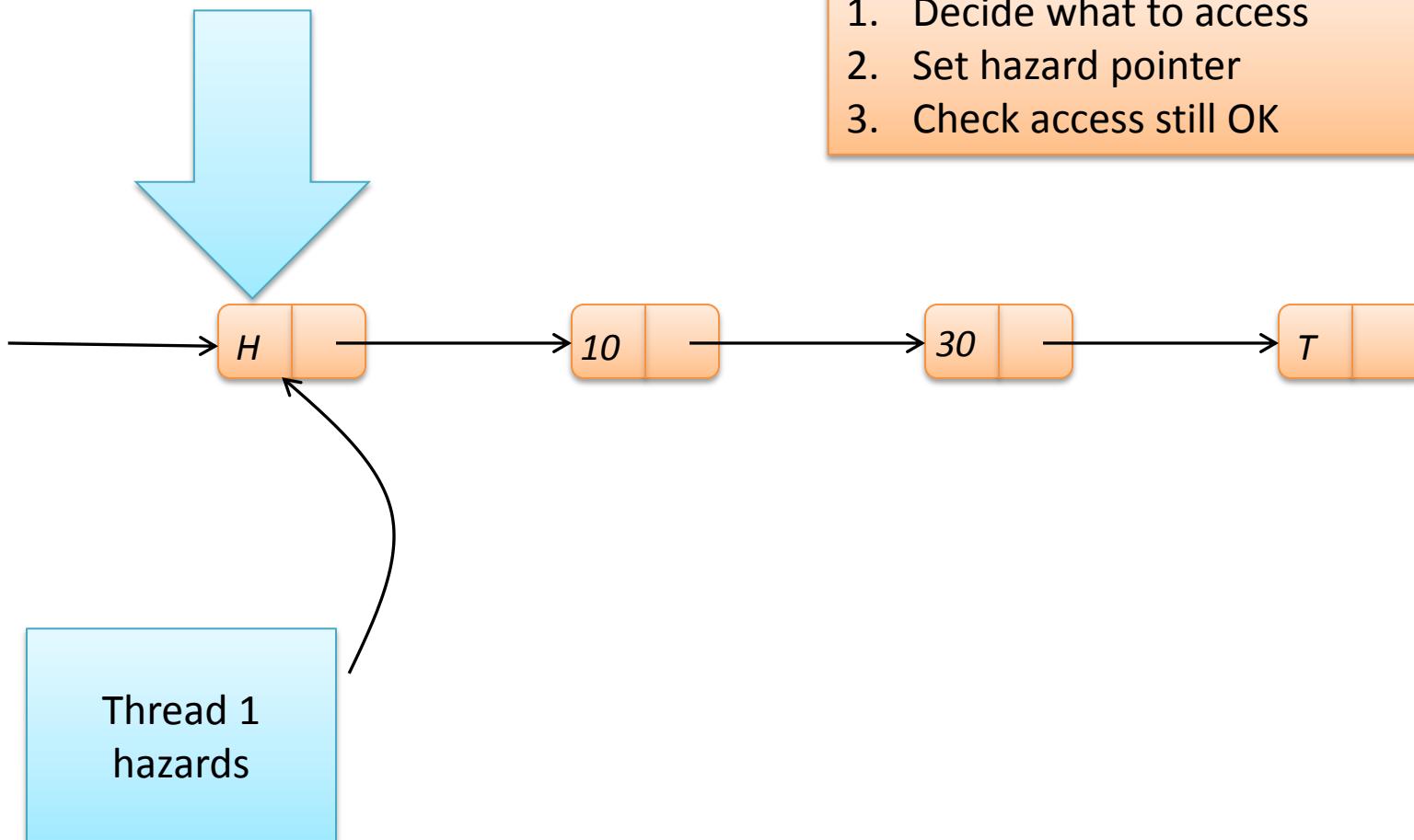


Thread 1
hazards

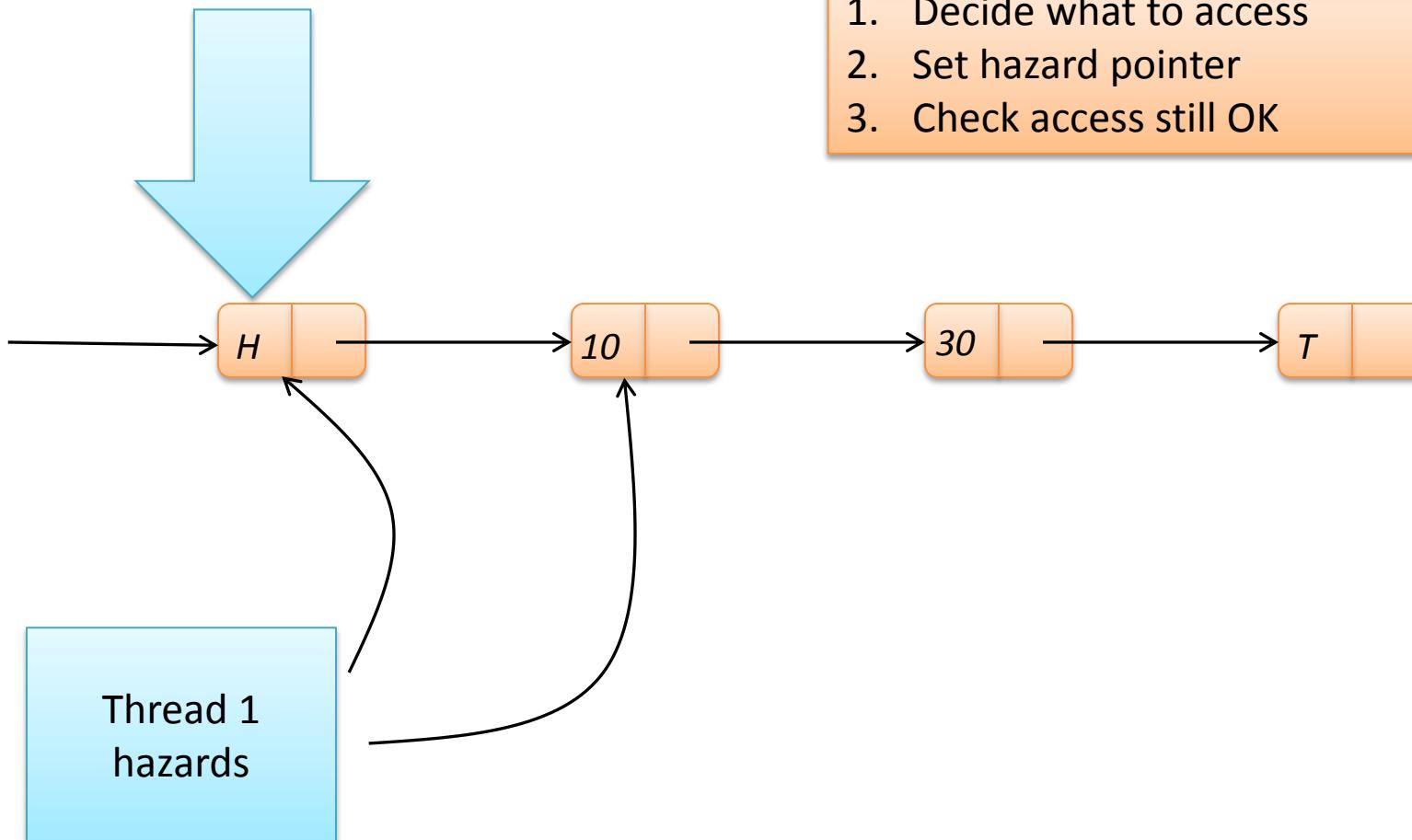
Hazard pointer mechanisms



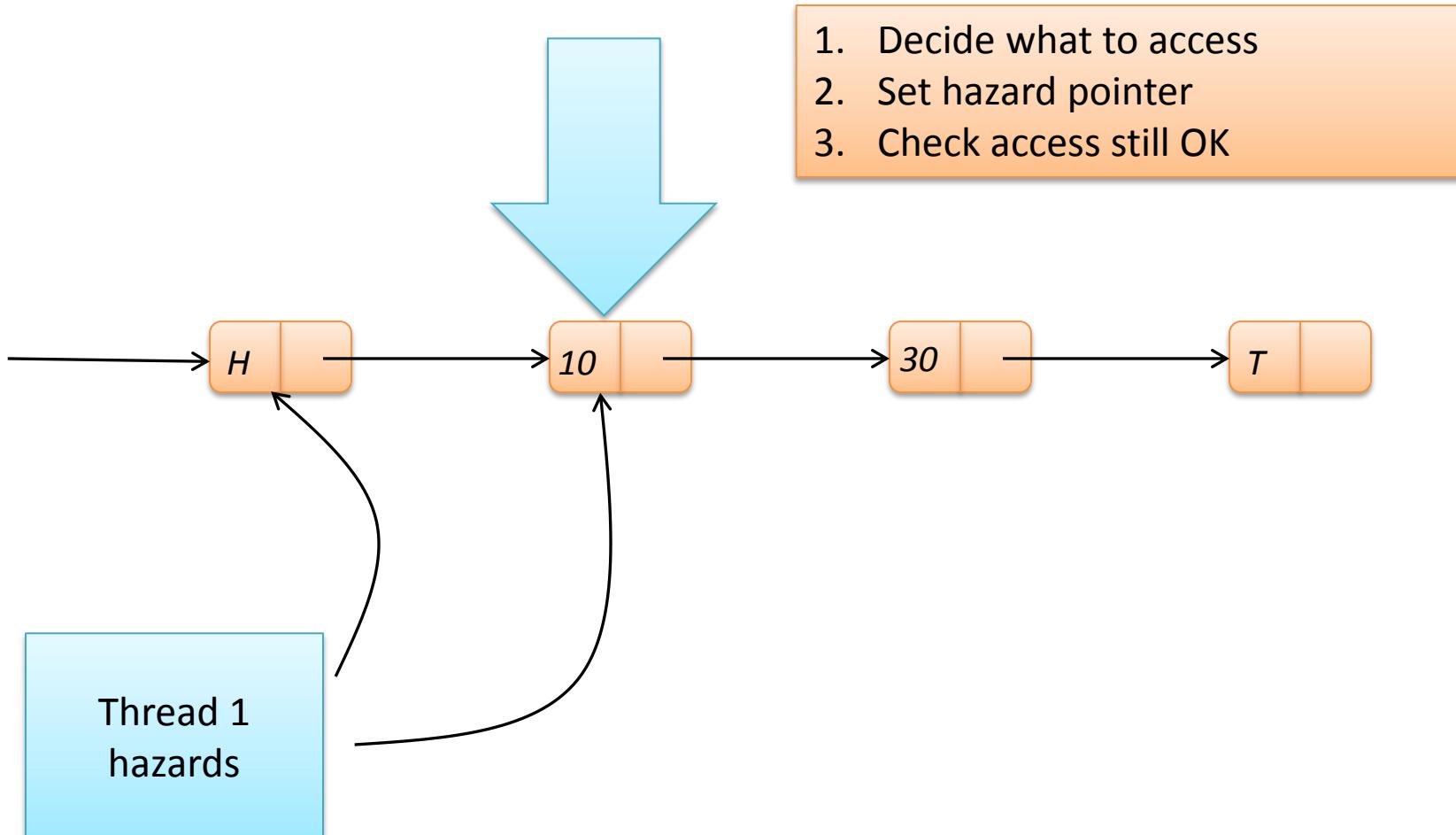
Hazard pointer mechanisms



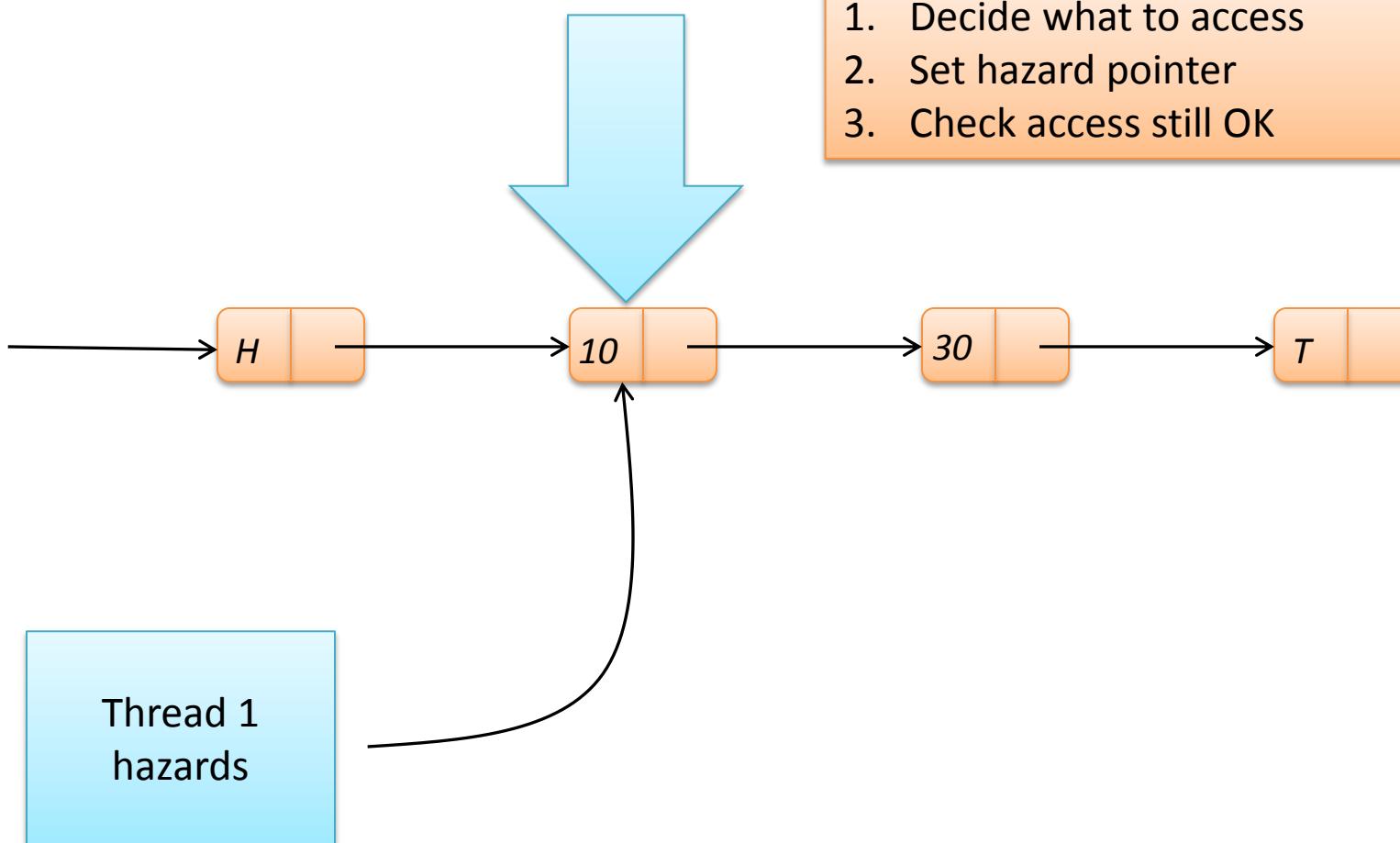
Hazard pointer mechanisms



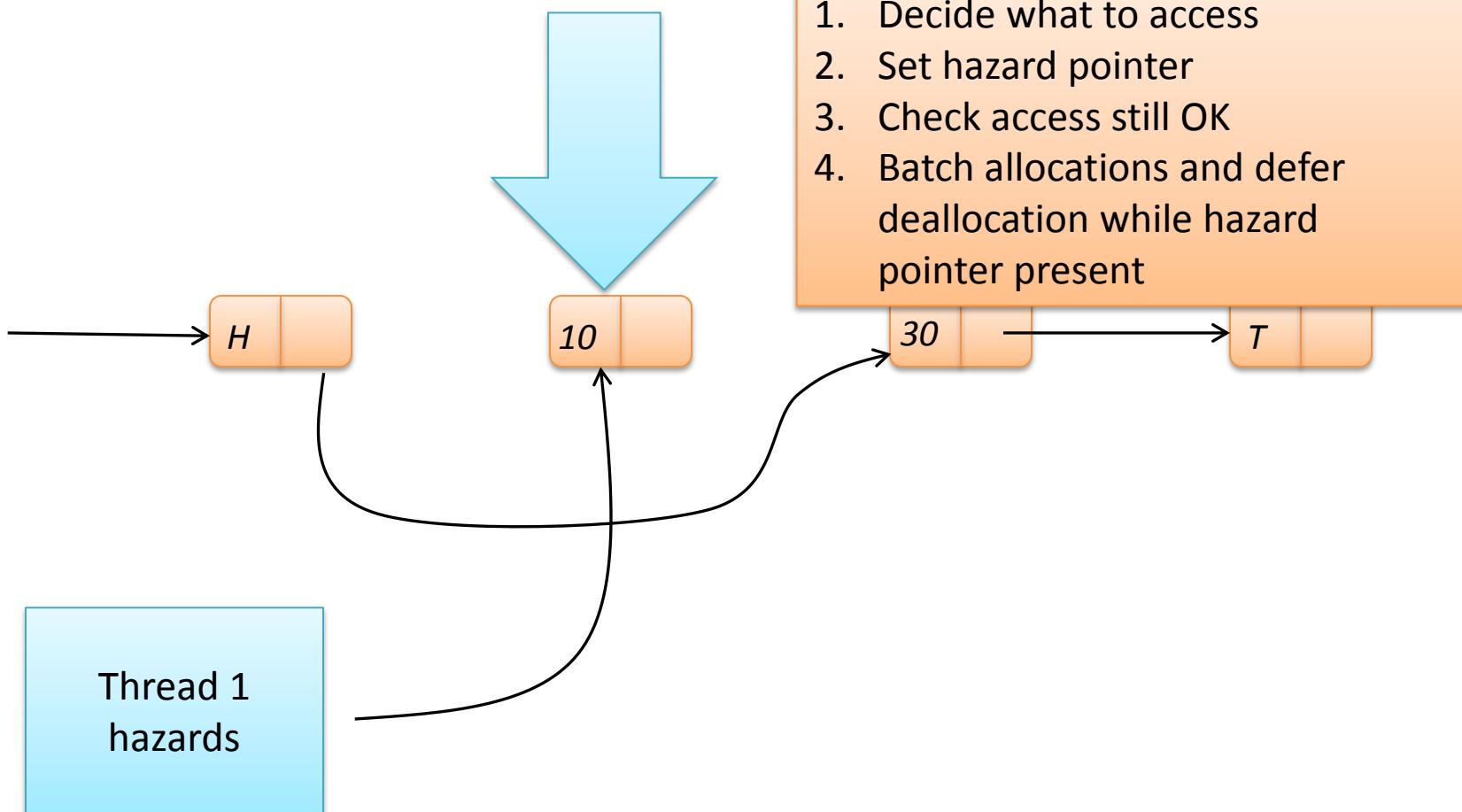
Hazard pointer mechanisms



Hazard pointer mechanisms



Hazard pointer mechanisms



Fine-grain parallel tasks

Work stealing queues & ABA

Memory management

Summary

- Hard to untangle algorithm design from detailed knowledge of the hardware
 - Memory models
 - Costs of operations
 - Scalability

Summary (2)

- General principle:
 - Avoid introducing contention between things that are logically independent
 - Contention for the same locks
 - Contention in the memory system

Summary (3)

- Linearizability as a way of defining what a concurrently accessed data structure should do
- If building directly from CAS then
 - Ensure that a single CAS really does make an atomic update to the logical state
 - Beware of delays & ABA problems