LG7: Recent Developments (RD)

Topics: Future languages, Recent developments, lectured as time permits: System Verilog, BlueSpec, C-to-Gates, Kiwi, Co-Synthesis, Future Directions.

LG7.1 - RD: VTOC.
LG7.2 - RD: System Verilog.
LG7.3 - RD: IP-XACT.
LG7.4 - RD: Bluespec System Verilog.
LG7.5 - RD: C-to-Gates.
LG7.6 - RD: Kiwi Project.
LG7.7 - RD: Co-Synthesis.
LG7.8 - RD: UML (Marte Project).
LG7.9 - RD: Glue Logic Synthesis.
LG7.10 - RD: Transactor Synthesis.

For part II, lecture group 7 is not examinable and dwill only be covered as time permits. (Bluespec will be covered with any luck).