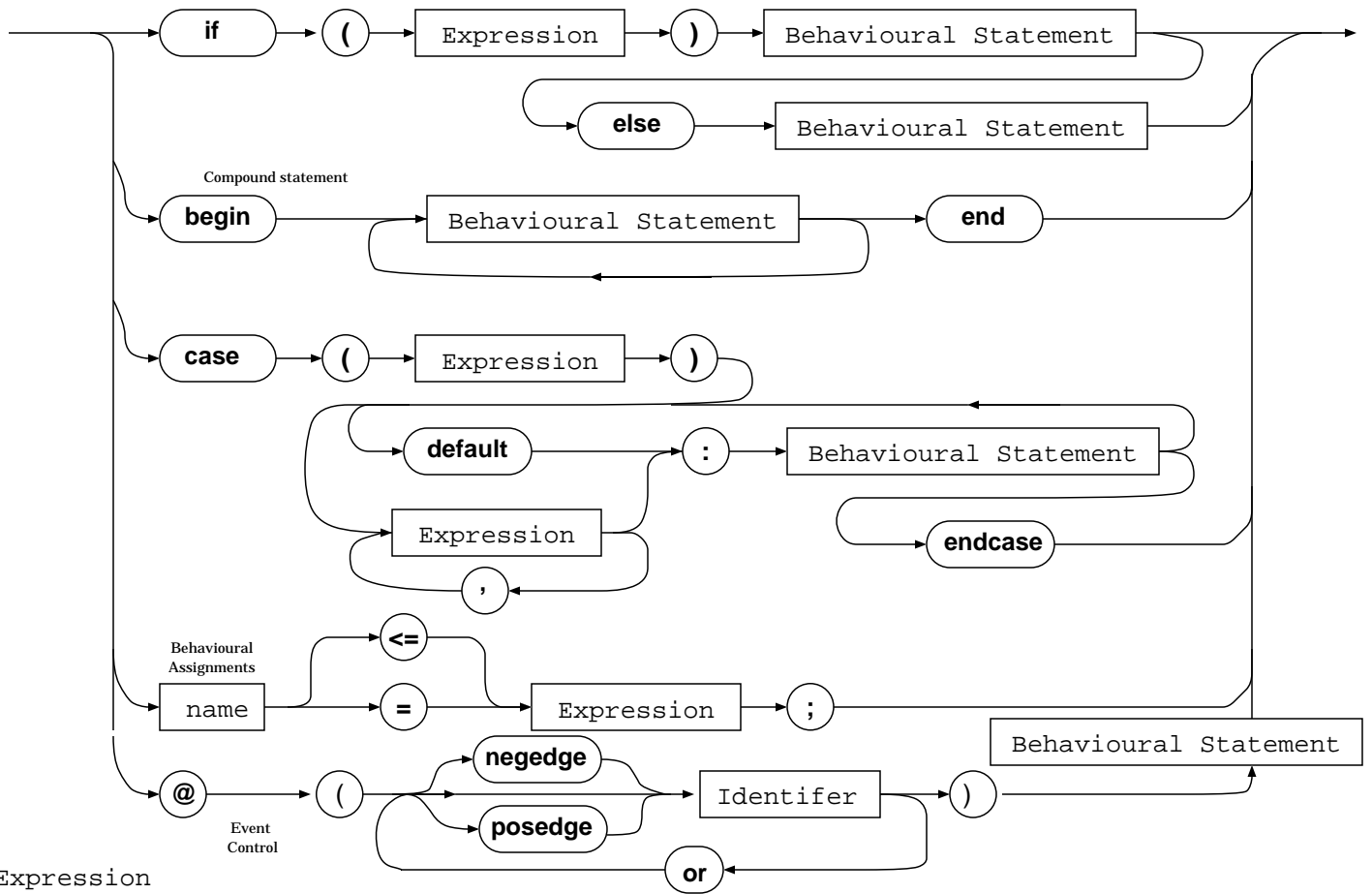


Verilog Syntax Chart Sheet 2 of 2

Behavioural Statement



Expression

