System Level Design Network Meeting: Design for Re-use and Legacy IP

Book the Date! Thursday 14th September 2006
Location: Bristol, Thornbury Golf Club, BS35 3XL

*** FREE to members & invited guests – register at: **www.nmi.org.uk**

NMI network events provide a support forum and opportunity for the professionals to meet, exchange views and debate the issues.

System Level Design (SLD) Network Overview

The need for improved productivity, efficiency and innovation are imperatives for an industry that competes at the global level. System Level Design is promoted as offering a route to deliver more efficient designs, faster and more accurately through improved system level definition, modelling, analysis and implementation methodologies.

The SLD network explores microelectronics design from system/algorithmic definition through to RTL/chip level and complements NMI's Embedded Software and Verification technical networks

Event Theme

Various analyses indicate that today's large system on chip designs re-use somewhere in the region of 80-90% legacy IP. This event will explore SLD methodologies and the particular challenges posed by designing for re-use and incorporating legacy IP.

Agenda

10.00 Registration and Refreshments

10.20 *Welcome & Introduction*
Steve Cozens, Consultant to NMI

10.30 *Electronic System Level (ESL): The New Approach to System Level Design*
Dr David Greaves, Cambridge University

11.00 *Comprehensive System C Cycle Accurate TLM & OSCI update.*
Automated ESL to improve implementation flow using SPIRIT IP-XACT
Nizar Romdhane, ARM (also OSCI & SPIRIT)

11.30 *ESL, a viable approach?*
Jean-Marie Saint-Paul, Mentor Graphics

12.00 *An Automotive perspective on System Level Design and re-use of Legacy IP*
Glenn Farrall, Infineon Technologies

12.30 Buffet Lunch & Networking

13.30 *Building a Complete Electronic System Level (ESL) Design Flow*
Dr Jeremy Bennett, Tenison Design Automation

14.00 *Virtual Platforms: from concept to reality*
Markus Willems, Synopsys

14.30 Panel Session
What are the opportunities and challenges to building an efficient system level design methodology incorporating legacy IP?

Chair: ***Chris Lennard***, ARM

Panel:

Jeremy Bennett	Tenison Design Automation
Robert Cotterell	Altera
Andy Jones	STMicroelectronics
Graham Kirsch	Micron
Colin Tattersall	Beach Solutions

15.30 End of Panel Session & Networking

16.00 Close

To register for the event please register via the NMI website www.nmi.org.uk.
Any queries please contact John Moor, 07739 982327, john.moor@nmi.org.uk

About the NMI…

The National Microelectronics Institute (NMI) is the trade association representing organisations involved in semiconductor design and manufacture in the UK and Ireland.

Its aim is to help build and support a strong semiconductor community by acting as a catalyst and facilitator for commercial and technological development.

A not-for-profit organisation funded by its members, the NMI has a membership that includes fabless semiconductor manufacturers, IDMs, foundries, design services, IP providers, research and academic institutions.

The NMI's work includes:

- Encouraging innovation, communication and collaboration through networking, brokering and sign-posting activities.
- Representing the microelectronics sector to government, policy makers and regulators.
- Supporting skills development, education and training.
- Helping to improve operational efficiencies through benchmarking and best practice initiatives.
- Providing an industry specific information flow.

More information can be found at: www.nmi.org.uk

Event Sponsors:

The NMI would like to thank the following organisations for their support in making this event possible.

ARM IET Mentor Graphics SYNOPSYS