1 Language Features

This document gives an overview of the L3 specification language. The language is imperative, first-order, case-sensitive and strongly typed.

1.1 Commenting

Block comments are delimited by ‘{-’ and ‘-}’. Inline comments start with ‘--’ and extend to the end of the current line. All commented code is simply ignored. Block comments can be nested, which means that it is possible to comment out code that already contains comments.

1.2 Primitive Types

The primitive types of the language are:

```
unit  bool  char  nat  int  bits()  rounding
```

Types can be combined into tuples (with the ‘*’ operator) and maps (with the ‘->’ operator). Finite set, option and list types can be formed with the ‘set’, ‘option’ and ‘list’ postfixes. An equality type is any type that excludes map types. The equality comparison operation ‘==’ can only be used to compare members of equality types. Furthermore, sets can only be constructed over equality types.

The infix ‘::’ is used for type annotation. For example, ‘1::bits(16)’ denotes a 16-bit word with value one. For convenience, this bit-vector annotation can be shortened to ‘1`16’.
Synonyms  The ‘type’ construct can be used to introduce new type synonyms; for example, the following are valid type declarations:

```plaintext
    type word = bits(32)
    type mem = bits(32) -> bits(8)
    type foo = word set * mem * bool
```

Type synonyms can be used interchangeably with their defining type. In the context of the type declarations above, we can introduce a new constant ‘four’ as follows:

```plaintext
    word four = 4::bits(32)
```

The extra type annotation is unnecessary here but it helps demonstrate that ‘bits(32)’ and the synonym ‘word’ are effectively one and the same.

1.3 Literals

The ‘unit’ type has a single element ‘()’. The ‘bool’ type has two elements: ‘true’ and ‘false’. The string type ‘string’ is a pseudonym for ‘char list’. String literals are represented using double quotes and character literals are preceded by a hash; for example, “hello” is a string and ‘#a’ is a character. The type ‘rounding’ has four elements: ‘roundTiesToEven’, ‘roundTowardPositive’, ‘roundTowardNegative’ and ‘roundTowardZero’.

The language supports three numeric types: ‘nat’, ‘int’ and ‘bits(·)’. Number literals can be expressed using binary, decimal or hexadecimal notation; as indicated by the prefixes ‘0b’, ‘0d’ and ‘0x’ respectively. An additional prefix character (‘n’, ‘i’ or ‘w’) can be used to avoid explicit type annotation; for example, ‘0i15’ and ‘0ixF’ both denote the integer value fifteen. A number without any prefixes is treated as a decimal bit-vector, wherein leading zeroes are ignored. Bit-vectors can be represented using a special quotation syntax; for example, “0101” is equivalent to ‘0b101`4’.

Bit-string literals  L3 allows lists of Booleans (bit-strings) to be treated as numeric values, with the head of the list representing the most significant bit. No constraint is placed on the length of bit-strings. This makes bit-strings distinct from bit-vectors, which are fixed width (as constrained by the type). The prefix character ‘s’ may be used to identify bit-string literals. Leading zeroes are significant when bit-string values are presented in binary or hexadecimal formats; for example, the expressions ‘0b00001101::bool list’ and ‘0sx0D’ both represent the list {false, false, false, false, true, true, false, true}. Consequently, the bit-string values ‘0sxD’ and ‘0sx0D’ are not equal. Note that leading zeroes are ignored in decimal bit-string expressions; for example, ‘029::bool list’, ‘0sd29’ and ‘0s11101’ all represent the same bit-string. When the prefix ‘s’ is used on its own, the expression is parsed as a binary value (this means that ‘0s20’ will fail to parse).

---

1 Octal is not supported.
2 The bit-string type represents a countably infinite set of values.
1.4 Global State

Global variables are introduced using the ‘declare’ construct, which adds elements to the global state-space. The following represent valid variable declarations:

- `declare number :: nat`
- `declare { integer :: int, boolean :: bool }` -- two declarations
- `declare { pc :: bits(32) r1 :: bits(64) }` -- commas are optional

At the point of declaration, it is not possible to specify initial values. Users can specify (and later call) their own initialisation procedures if required.

The ‘<-’ operator is used to assign values to mutable variables; for example

```
pc <- 0xF -- an assignment
```

assigns value ‘0xF’ to our declared global variable ‘pc’. The bit-string and bit-vector types also permit assignment to bit-ranges; for example

```
{ pc<3:0> <- 0xF; pc<31> <- true } -- a sequence of assignments
```

will set the bottom four bits and then the most-significant bit of ‘pc’.

1.5 Tuples

Tuples can be formed using the pairing constructor ‘,’ which associates to the right. Bracketing is optional (but recommended); for example, all of the following expressions are semantically identical:

```
"one", true, false
"one", (true, false)
("one", true, false) -- preferred form
("one", (true, false))
```

These expressions have type ‘string * bool * bool’. Note that the expression

```
("one", true), false
```

has type ‘(string * bool) * bool’, which makes it strictly type distinct from (incompatible with) the previous expression.

Note that (in expressions) commas are the loosest binding operator, which means that

```
x, y == 3, 4
```

will parse as

```
(x, ((y == 3), 4)) -- fully elaborated form
```

and not as

```
(x, y) == (3, 4) -- this could be what was intended
```

It is advisable that users only drop brackets when there is little chance of ambiguity.

---

3This is the same as ‘string * (bool * bool)’. 
1.6 Maps

Maps are used to represent total functions. For example, the declaration

```ml
  type map :: nat -> int -- a map type pseudonym
```

introduces a type pseudonym for maps with domain ‘nat’ and codomain ‘int’. Maps and single map entries can be assigned to; for example, given the declaration

```ml
  declare -- three global maps
  {
    m1 :: map
    m2 :: map
    m3 :: nat -> nat
  }
```

the statement

```ml
  { m1 <- m2; m1(1) <- 2 }
```

will set map ‘m1’ to be ‘m2’ and this map is then updated so that ‘m1(1)’ takes value ‘2’.

The language encourages the use of simple, un-curried maps, which means that the type

```ml
  type map :: bool * nat -> int
```

should be used in preference to

```ml
  type nmap :: nat -> int
  type map :: bool -> nmap
```

In particular, the language does not permit the syntax ‘m(true)(2)’ whereas ‘m(true, 2)’ is fine.

The language does not provide a mechanism for specifying anonymous functions or anonymous maps. Furthermore, maps and operations (either primitive or user defined) are considered to be distinct. As such, it is not possible to make assignments of the form ‘m3 <- Log2’.

1.7 Sum Types

Enumerated and sum (tagged union) types can be introduced using the ‘construct’ keyword. For example, the declaration:

```ml
  construct enumerated { Zero One Two Three } -- a new type called "enumerated"
```

introduces a new type ‘enumerated’ that consists of four elements (new constants). Sum types can be declared in a similar manner; for example:

```ml
  construct bool_option
  { BoolNone, BoolSome :: bool } -- an unnecessary variant of "bool option"
```

The elements of this new type are: ‘BoolNone’, ‘BoolSome(false)’ and ‘BoolSome(true)’. Every constructor of the sum type must map from a pre-existing type (or be nullary) and this precludes the definition of new recursive or mutually recursive datatypes.

Enumerated types (where every constructor is nullary) admit casting to/from numeric types. For example, ‘[One]::nat’ is equal to ‘0n1’ and ‘[0n1]::enumerated’ is equal to ‘One’.

---

4Maps must have an equality type as their domain. They can be viewed as a form of array and are typically used to model memories and register banks.

5The function ‘fn x => x + 1’ is an example of an anonymous function in Standard ML.

6The brackets are needed here; for example, ‘BoolSome true’ will not parse.

7The same does not hold for general sum types.
1.8 Records

Record types can be introduced using the ‘record’ construct. The following is an example of a record declaration:

```plaintext
record rec
{
  number :: nat
  integer :: int
  boolean :: bool
}
```

Every value of type ‘rec’ has three components, which may be accessed using dot notation; for example, if we have a global variable

```plaintext
declare rec :: rec
```
then the sub-elements are ‘rec.number’, ‘rec.integer’ and ‘rec.boolean’. The language does not provide a syntax for record literals. As such, records must be built up incrementally; for example, the following is a sequence of assignments to components of the variable ‘rec’:

```plaintext
{ rec.number <- 1; rec.integer <- 2; rec.boolean <- true }
```

It is not possible to express this with a single assignment to ‘rec’.

1.9 Registers

Register types can be introduced using the ‘register’ construct; for example:

```plaintext
register sreg :: bits(32)
{
  31-28:   FLAGS
  7-0, 27-16:   MODE
  15-12:   MASK
}
```

This declares a new type ‘sreg’ that represents the following 32-bit register:

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</table>
```

Note that the bit-field 11..8 of ‘sreg’ does not correspond with a named component. Also note that fields are allowed to be non-contiguous, which is the case for the ‘MODE’ field. Register types are essentially special forms of record types, with the automatic specification of bijections to a bit-vector representation. Each named bit-field can be accessed using the dot notation; for example, if we have

```plaintext
declare status :: sreg
```
then the fields ‘status.FLAGS’ and ‘status.MASK’ have type ‘bits(4)’ and ‘status.MODE’ has type ‘bits(20)’. Unlike records, register types automatic admit casting to/from an underlying bit-vector type. The expression ‘&status’ gives the value of the register (with type ‘bits(32)’); and ‘sreg(0xFFFF0000)’ constructs a register of type ‘sreg’. It is possible to use this notation to update arbitrary bit-fields of a register; for example, the statement:

```plaintext
&status<20:7> <- '11101101100111'
```

will update the bit-field 20..7 of ‘status’ using the 14-bit literal value.
1.10 Polymorphic Types

Users cannot declare their own polymorphic types. Furthermore, all user defined operations and variables must be monomorphic. The language does, however, provide support for three built-in polymorphic datatypes, which may be used under the proviso that these types are always fully type instantiated (free of type variables).

1.10.1 Lists

The language provides limited support for working with lists. Lists can be specified as follows:

\[
\begin{align*}
\text{list \{ 0n0, 2, 4 \}} & \quad \text{-- a list of naturals (length three)} \\
\text{0n0 @ 2 @ 3 @ Nil} & \quad \text{-- expanded version} \\
\text{Cons (0n0, Cons (2, Cons (4, Nil)))} & \quad \text{-- equivalent version}
\end{align*}
\]

These expressions have type ‘\text{nat list}’. Given the first-order nature of L3, only simple list operations are provided (for example: ‘\text{Head}’, ‘\text{Tail}’ and ‘\text{Length}’). Higher-order list operations, such as \text{filter}, \text{map} and \text{fold}, are not available.

1.10.2 Finite Sets

The language provides basic support for finite sets. Sets can be specified as follows:

\[
\begin{align*}
\text{set \{ 0n0, 2, 4 \}} & \quad \text{-- a three element set (of naturals)} \\
\text{0n0 insert 2 insert 4 insert set \{\}} & \quad \text{-- an expanded version} \\
\text{SetOfList (list \{ 0n0, 2, 4 \})} & \quad \text{-- a version built from a list}
\end{align*}
\]

These expressions have type ‘\text{nat set}’. The infix operators ‘\text{in}’ and ‘\text{notin}’ test for set membership; for example, the expressions

\[
\begin{align*}
\text{0n0 in set \{ 0, 2, 4 \}} & \quad \text{-- test for membership} \\
\text{0n1 notin set \{ 0, 2, 4 \}} & \quad \text{-- test for non-membership}
\end{align*}
\]

both evaluate to ‘\text{true}’. Sets can only be defined over equality types. It is not possible, for example, to construct the type ‘\text{(bool -> bool) set}’.

1.10.3 Option Types

The option type is a polymorphic sum type that has two constructors: ‘\text{None: α option}’ and ‘\text{Some: α -> α option}’. Option types are helpful when specifying partial operations.

1.11 Statements and Expressions

This section presents the statements and expressions of the language. Statements can be grouped into blocks using the syntax

\[
\begin{align*}
\{ \\
\text{statement1; -- do this} \\
\text{statement2; -- then do this} \\
\text{\ldots}
\}
\end{align*}
\]
Each block, and sub-statement, has type ‘unit’. The operator ‘;’ is strictly binary: trailing semi-colons not permitted at the end of a sequence of statements. Expressions are distinct from statements, in particular expressions preclude assignments.

1.11.1 Nothing

The statement ‘nothing’ has no effect. Its primary purpose is to facilitate the specification of control flows wherein some paths do something (update the state) and others do nothing.\(^8\)

1.11.2 Conditionals

The language supports if-then-else statements and expressions, and when-do statements. The latter is provided as a convenient shorthand; that is to say, the statement

\[
\text{when } p \text{ do } x
\]

is equivalent to the statement

\[
\text{if } p \text{ then } x \text{ else } \text{nothing}
\]

Note that the language does not support layout syntax (the off-side-rule).

1.11.3 For Loops

The language provides support for simple for-loop statements. On each iteration of a for-loop, a natural number index variable is either incremented or decremented (by one). This process is repeated until the index value reaches a pre-computed upper or lower bound; for example, the loop statements

\[
\text{for } i \text{ in } 1 .. 3 \text{ do proc}(i)
\]

\[
\text{for } i \text{ in } 3 .. 1 \text{ do proc}(i)
\]

are respectively equivalent to

\[
\{ \text{proc}(1); \text{proc}(2); \text{proc}(3) \}
\]

\[
\{ \text{proc}(3); \text{proc}(2); \text{proc}(1) \}
\]

The initial and final loop index values may be arbitrary natural number expressions. As such, it may be the case that the loop direction will only be known once these bound expressions are computed.

In addition to regular for loops, the language also supports foreach loops, which operate over lists. For example, the following code makes three calls to ‘proc’:

\[
\text{foreach } i \text{ in list } \{0n2, 4, 6\} \text{ do proc}(i)
\]

Note that this construct will also work for bit-strings and for normal strings.

There is no support for do while or repeat until loops.

\(^8\)The ‘nothing’ statement is essentially syntactic sugar for ‘()’. 
1.11.4 Exceptions, Unknown and Undefined

The keyword ‘UNKNOWN’ is used to represent an unspecified element, which may be of any given type. The language does not stipulate whether or not this value should be chosen deterministically. If the value were chosen deterministically then the expression ‘UNKNOWN == UNKNOWN’ will always be ‘true’. However, if the choice is non-deterministic then there is no way of knowing if an occurrence of this expression is ‘true’ or ‘false’.

The language supports user defined exceptions, which may be raised but not caught. The ‘exception’ keyword is used to declare new exception categories; for example:

```plaintext
exception ASSERT :: string
```

introduces a new exception ‘ASSERT’ that takes a single string as an argument. This exception can be raised in expressions and statements as follows:

```plaintext
#ASSERT ("an error has occurred")
```

Thus, exceptions can be used to flag the occurrence of a variety of different errors. The L3 interpreter will stop when an exception is raised. Exceptions can be raised within expressions and are not constrained by return type.

The behaviour of various operations is undefined on some inputs; for example the arithmetic expression ‘x div 0n0’ is undefined. For any given undefined expression, the language permits three possible interpretations (implementations):

- A deterministic value can be chosen. For example: ‘x div 0n0’ is equal to ‘0n0’ is a valid interpretation of this undefined expression.

- A non-deterministic value can be chosen. In particular, is not necessary for the expression ‘x div 0n0 == x div 0n0’ to be ‘true’.

- An exception can be raised.

It is expected that specifications will be designed with some preferred treatment of undefined behaviour in mind. In this example one possible way to manage undefined cases is by taking care to explicitly write code of the form ‘if n == 0 then ... else ... x div n .. ’. This will ensure that undefined cases are unreachable.

1.11.5 Procedure Calls

A procedure is any user defined operation with return type ‘unit’. Procedures typically provide functionality by means of updating the global state (through side effects). Procedures can be called (and exceptions can be raised) in statement blocks; for example:

```plaintext
{
  ...
  proc (1, true); -- this is a procedure call
  ...
}
```

The following code is valid but not recommended:
{ …
   #ASSERT "error"; -- raise an exception
   proc (1, true) -- the L3 interpreter will never reach this code
}

In the above, the exception is unconditionally raised, so the remaining code should be regarded as unreachable (redundant).

1.11.6 Local Variables

The L3 language supports two varieties of variables: mutable and immutable. All global variables are mutable. The keyword ‘var’ is used to declare new mutable variables with local scope. Local variables can be introduced anywhere within a statement block and these variables can be assigned to using the ‘<-’ operator. Immutable variables are introduced within a block using the syntax ‘x = expr; …’ and this is treated as a let-statement. An immutable value cannot be updated, however it can fall out of scope by declaring a new immutable variable with the same name. The operators ‘<-’ and ‘=’ are not permitted in expressions.

The specification below uses two mutable variables ‘x’ and ‘y’:

{ var y = z;
  var x = y;
  if p then { x<3:0> <- '1111'; y <- x + 1 }
  else { x<7:4> <- '1111'; y <- 0 }; return (x + y)
}

This could be respecified using immutable variables, for example

{ y = z;
  x = y;
  x, y = if p then (x || '00001111', x || '00001111' + 1)
    else (x || '11110000', 0);
  return (x + y)
}

It is not always easy to eliminate mutable variables. In this case the second specification is more cumbersome because the bit-field update has been replace by an explicit mask (bitwise-or) operation. This operations has been repeated in specify the value of ‘y’ because assignments are not permitted in expressions.

Mutable variables can be declared without an initial assignment; for example, ‘var x’ is equivalent to ‘var x = UNKNOWN’.

1.11.7 Pattern Matching

The language supports match statements and expressions. Pattern matching is supported with respect to: literals (see Section 1.3), constructors,9 (immutable) variables, wildcards (anonymous variables) and bit-patterns.

---

9This includes matching on tuples, as well as ‘None’, ‘Some’, ‘Cons’ and ‘Nil’. Matching on finite sets and record fields is not possible.
Given the type declarations:

```l3
construct enum { One Two Three }
construct data
{
    NoData
    Data1 :: string * bits(8)
    Data2 :: enum * bits(16)
}
```

the following illustrates a match expression:

```l3
match y
{
    case -1, NoData or -2, NoData => 0'4 -- note use of "or"
    case _, Data1 ("hello", '11 x 11') => x + 1 -- use of bit-pattern
    case i, Data2 (Three, '1111 y') => [i] + y<3:0>
    case 1, Data2 (_, 8) => 4
    case _ => 8
}
```

The body (right-hand side of ‘=>’) of the first case line with a matching pattern is selected. The symbol ‘_’ is a wildcard and will match any value. The ‘or’ construct provides a shorthand for writing blocks of clauses with differing patterns but the same body.

The terms ‘11 x 11’ and ‘1111 y’ are called bit-patterns and they will match against a set of bit-vector values. From the type declaration of ‘data’, it is known that the variable ‘x’ must represent a 4-bit value because it occurs in the middle of an 8-bit word (the match requires bits ‘11’ as a prefix and as a suffix). Likewise, ‘y’ must be a 12-bit value. Multiple variables can occur in a bit-patterns, for example, ‘1 x 1 y 0’ is a valid bit-pattern. The type checker will often be able to infer the type of pattern variables and if this fails type annotations can be added.

The ‘pattern’ construct can help avoid situations where explicit annotations are repeatedly required (causing clutter in patterns). If, for example, the variable ‘imm5’ is frequently used to match a 5-bit value then the following declaration can be made:

```l3
pattern imm5 :: bits(5)
```

In subsequent code the default type for pattern variable ‘imm5’ will be ‘bits(5)’. The ‘pattern’ construct only influences type checking. If a particular type hint is no longer appropriate then it can be dropped; for example, one can insert the declaration ‘clear pattern imm5’.

### 1.12 Defining Constants and Operations

Users can construct operational semantics style formal specifications in L3 by defining their own constants and operations. There is no module system or facility for local definitions; all user definitions are made at the top-level with global scope. User constants and operations cannot be overloaded or redefined.

A pure constant is a nullary (zero argument) operation that is state independent; for example, the following code declares a new constant:

```l3
bits(32) UINT_MAX = 0xFFFFFFFF
```
The following code declares operations that are nullary but state dependent:

```plaintext
declare number :: Nat
nat numberPlus1 = return (number + 1)
unit incNumber = number <- numberPlus1
```

Users can also declare new unary operations, which may or may not be state dependent; for example:

```plaintext
nat numberMinusN (n::nat) = return (number + n) -- state dependent
unit decNumber () = number <- numberMinusN (1)
nat mla (m::nat,n::nat,a::nat) = return (m * n + a) -- pure
```

The operation ‘mla’ is unary because it takes a single triple as an argument. To enhance clarity and avoid misunderstandings, users may elect to specify nullary state dependent operations using a single ‘unit’ argument. Given the declarations above, the expression ‘decNumber()’ gives the clear impression that a call is being made (with possible side-effects), whereas the expression ‘incNumber’ does not.

At the point of declaration, operation arguments must be fully type annotated and only monomorphic types are allowed. It is, nevertheless, possible to declare bit-vector operations that are valid with respect to some given set of bit widths, for example, the following are valid declarations:

```plaintext
--- valid for any bit-vector width N ---
bv_mla (m::bits(N), n::bits(N), a::bits(N)) = return (m * n + a)

--- valid for sufficiently wide bit-vectors (N greater than eight) ---
bool bit8 (x::bits(N)) with N > 8 = return (x<8>)
```

An L3 type error will arise if ever an application of the operation ‘bit8’ violates the bit width constraint. Note that vector width variables (such as ‘N::nat’ above) can be used as if they were regular variables in the definitional body of an operation.

### 1.13 Components

When specifying instruction set architectures, the two most significant programmer’s model components are typically the main memory and a collection of general purpose registers. In the MIPS architecture, for example, there are thirty-two general purpose registers, which may be represented using the following map:

```plaintext
declare gpr :: bits(5) -> bits(64)
```

The statement

```plaintext
gpr(d) <- gpr(r)
```

--- 10 An compile time error will arise if the type checker is not able to infer all types: the final definition must be free of type variables.
then expresses moving a value from register ‘r’ to register ‘d’. However, register a memory access is frequently more complicated than this; in particular, registers can be banked and memory can be complex.\textsuperscript{11} As such, it is often necessary to specify collections of operations for accessing (reading and writing) these components. With the MIPS example, register zero is special (it is a constant value) and so one could define the following operations:

\[
\begin{align*}
\text{bits}(64) \text{ read}_\text{gpr} (n::\text{bits}(5)) &= \text{ if } n == 0 \text{ then } 0 \text{ else } \text{gpr} (n) \\
\text{unit write}_\text{gpr} (v::\text{bits}(64), n::\text{bits}(5)) &= \text{ when } n <> 0 \text{ do } \text{gpr} (n) <- v
\end{align*}
\]

Thus, the previous register transfers would now be specified as follows:

\[
\text{write}_\text{gpr} (\text{read}_\text{gpr} (n), d)
\]

This precise style of specification is not entirely satisfying though, since it departs in appearance from the pseudo code found in MIPS reference manuals. The L3 language provides a special mechanism for specifying a pair of operations that read and write state components. With the MIPS example, the following declaration can be made:

\[
\begin{align*}
\text{component GPR (n::\text{bits}(5)) :: \text{bits}(64)} \\
&\{ \\
&\text{value} = \text{if } n == 0 \text{ then } 0 \text{ else } \text{gpr}(n) \\
&\text{assign value} = \text{when } n <> 0 \text{ do } \text{gpr}(n) <- \text{value}
\}
\]

One can then write:

\[
\text{GPR}(d) <- \text{GPR}(r)
\]

which is closer to the syntax used in the MIPS reference manuals. The ‘\text{component}’ construct effectively provides a controlled form of operator overloading, whereby the ‘\text{assign}’ operation\textsuperscript{12} is select when the operator name appears on the left-hand side of an assignment (‘<-’) and the ‘\text{value}’ operation\textsuperscript{13} is applied otherwise. Semantically, these two operations are exactly the same as the manually defined versions ‘\text{write}_\text{gpr}’ and ‘\text{read}_\text{gpr}’. The “write” operation will always have a ‘\text{unit}’ return type. Users are free to specify the “read” and “write” operations in any manner that they deem appropriate; however, the “write” operation cannot be defined recursively, nor can the “read” operation call the “write” operation.

1.14 Recursion

The language permits the definition of recursive operations. It is not possible to define mutually recursive operations.

The L3 language has been designed to act as an authoring front-end for the HOL4 theorem prover; facilitating the task of writing instruction set specifications. In order to successfully import an L3 specification, it is necessary for the HOL4 system to prove that every user defined

\textsuperscript{11}ARM registers are banked according to processor mode. Memory can often be accessed with big- and little-endian byte ordering. Special treatment may be needed for unaligned memory addresses, for example, when reading a 16-bit value from an odd address. There may also be address translation (virtualisation) and memory hierarchies (caching).

\textsuperscript{12}In this example the “write” operations is internally called ‘\text{write}_\text{GPR}’.

\textsuperscript{13}Internally the “read” operation is simply called ‘\text{GPR}’. 
operation is total (terminates on all inputs). If an L3 function is non-terminating then it will be impossible to export that definition to HOL4. If a function is terminating but the standard automation of HOL4 is unable to construct a termination proof, then it may be necessary to provide the prover with a hint in the form of a *measure*.

Consider the following specification:

```
declare m :: bits(8) -> bits(8)

unit load (a::bits(8), l::bits(8) list) =  
  match l
  {
    case Nil => nothing
    case Cons (h, t) => { m(a) <- h; load (a + 1, t) }
  }
```

The ‘load’ operation recurses over the list ‘l’, loading values into the map ‘m’. At the time of writing this documentation, trying to build the exported HOL4 version of this specification gives rise to the following error message:

```
Initial goal:
\exists R. 
  WF R \land 
  \forall state a l h t. 
  (l = h::t) \Rightarrow 
   R ((a + 1w, t), state with m := (a += h) state.m) ((a, l), state)
```

Exception raised at TotalDefn.Define:  
between beginning of frag 0 and end of frag 0:  
at TotalDefn.defnDefine:  
Unable to prove termination!  
Try using "TotalDefn.tDefine <name> <quotation> <tac>".
```

This occurs because HOL4 is unable to automatically prove that ‘load’ always terminates. To get around this problem, the original L3 specification can be modified to contain an annotation as follows:

```
unit load (a::bits(8), l::bits(8) list) measure Length(l) = ...
```

The metadata ‘measure Length(l)’ tells HOL4 to examine the length of the list ‘l’, since this value will decrease upon each recursive call to ‘load’. When the length of the list is zero (the ‘Nil’ case) there are no recursive calls, so termination is guaranteed.

The expression occurring after the ‘measure’ keyword must be of type ‘nat’. After providing a measure, it is still possible that HOL4 will fail to prove termination, either because the supplied measure is incorrect or because the termination proof is non-trivial (requires human guidance and/or custom lemmas). If this occurs then users should either simplify/rewrite their specifications or, if necessary, report the problem.
1.15 Instruction Set Definitions

When working with instruction set models, it is helpful to construct a datatype that represents
the set of machine (low-level) instructions. This datatype constitutes a simple form of abstract
syntax tree (AST) and it can be used to build: instruction decoders (identifying machine-code
instructions and splitting opcodes into sub-fields); instruction encoders (generating opcodes for
a given instruction); next-state functions (applying a semantics function for a given instruction)
and assembly code parsers.

By way of an example, consider the MIPS instruction ANDI: this instruction has the assembly
code syntax

\[
\text{ANDI } rt, rs, \text{ immediate}
\]

and the following 32-bit machine-code encoding:

\[
\begin{array}{ccccccccc}
\text{rt} & \text{rs} & \text{immediate} \\
31 & 26 & 25 & 21 & 20 & 16 & 15 & 0 \\
001100 & 6 & 5 & 5 & 16
\end{array}
\]

The semantics of this instruction can be expressed with the operation:

\[
\text{unit run\_ANDI (rs::bits(5), rt::bits(5), imm::bits(16)) =} \\
\text{GPR(rt) \leftarrow GPR(rs) \&\& \text{ZeroExtend (imm)}}
\]

One could use this operation to directly specify a next-state function, which would be of the
form:

\[
\text{unit Next =} \\
\text{match Fetch \text{ -- fetches a 32-bit opcode from memory}} \\
\{ \\
... \\
\text{case '001100 rs rt imm' \Rightarrow run\_ANDI (rs, rt, imm)} \\
... \\
\}
\]

This style of specification is direct and concise; however, it does lack some of the flexibility that
comes with using an instruction datatype. The L3 language provides support for an slightly
different style of specification. This alternative approach is explained by first examining how
one would specify things manually (explicitly) and then showing how L3 can streamline this
process.

Firstly, one could explicitly define an AST datatype for MIPS instructions; it would be of
the form:

\[
\text{construct instruction} \\
\{ \\
... \\
\text{ANDI :: bits(5) * bits(5) * bits(16)} \\
... \\
\}
\]
This datatype provides a suitable constructor for the **ANDI** instruction. It is now possible to define a decoder, which maps opcodes onto AST entries; for example:

```plaintext
instruction Decode (opc::bits(32)) =
    match opc
    {
        ...
        case '001100 rs rt imm' => ANDI (rs, rt, imm)
        ...
    }
```

This decoder can be used to define our next-state function, that is:

```plaintext
unit Next = Run (Decode (Fetch))
```

where `Run` is defined by

```plaintext
unit Run (i::instruction) =
    match i
    {
        ...
        case ANDI (args) => run_ANDI (args)
        ...
    }
```

When working with full-scale architecture specifications (where the AST type is large), there are some clear overheads associated with implementing this approach (in comparison with the AST free version). It becomes challenging to maintain the AST datatype declaration and to keep it correctly synchronised with the instruction semantics operations (such as `run_ANDI`) and the definition of `Run`. Note, however, that there is great uniformity in the AST datatype declaration and also in the definition of `Run`. As such, L3 is able to automatically define both of these for the user.

To make use of this automation, the declaration of `run_ANDI` is simply modified to:

```plaintext
define ANDI (rs::bits(5), rt::bits(5), imm::bits(16)) =
    GPR(rt) <- GPR(rs) && ZeroExtend (imm)
```

The `define` keyword tells L3 to define a semantics function (as before) and to also create a new `instruction` datatype AST entry; furthermore, the two will be linked with the automatic construction of a `Run` function. The `instruction` type and `Run` function become defined (in scope) following the declaration `define Run`. After this declaration it is no longer possible to use the `define` construct again; however, it does become possible to define functions such as `Decode` and `Next`.

Specifications that are based on using `define` gain the benefits of declaring an instruction AST datatype without the downside of laborious coding overheads. In the example above, the overhead amounts to one extra line of code; this comes from defining a next-state function that calls the dedicated decode function, followed by a call to `Run`.

One of the key advantages of declaring an instruction datatype is that it makes it considerably easier to develop tools that work with assembly code syntax (performing parsing, encoding

---

14 This function is internally called `dfn'ANDI' instead of `run_ANDI'.
and pretty-printing). In this case, it is relatively easy, for example, to map between the MIPS instruction syntax "andi $5, $6, 1024" and the expression 'ANDI (5, 6, 1024)'. Defining an encoder in L3 is relatively straightforward; for example:

```haskell
bits(32) Encode (i::*:instruction) =
  match i
  {
    ...
    case ANDI (rs, rt, imm) => '001100' : rs : rt : imm
    ...
  }
```

Such a function can be used to write a MIPS assembler, which would map the assembly code syntax "andi $5, $6, 1024" onto the machine-code value \(0x30c50400\).

## 2 Tutorial

This section gives a brief overview of how to install and use L3.

### Installation

The sources for L3 can be downloaded from the following webpage:

```url
www.cl.cam.ac.uk/~acjf3/l3
```

The file `l3.tar.bz2` can be unpacked with the command:

```bash
$ tar xvjf l3.tar.bz2
```

This will create a directory of the form `L3-YYYY-MM-DD`, where the year, month and day will correspond with the release date. The current version of L3 depends upon Poly/ML 5.5, which can be obtained from the site `polyml.org`. If Poly/ML is installed then L3 can be built as follows:

```bash
$ cd L3-YYYY-MM-DD
# edit the Makefile
$ make
```

Only minor edits to `Makefile` should be required. Once built, the following should appear when you start L3.\(^{15}\)

```bash
$ l3
<< L3 >>
```

Users should consider running L3 with the command `rlwrap l3`.\(^{16}\)

---

\(^{15}\)This assumes that the directory `L3-YYYY-MM-DD/bin` has been added to the `PATH` environment variable.

\(^{16}\)Details on `rlwrap` utility can be found at the site `freecode.com/projects/rlwrap`. 
**Basic Interaction**  
The command prompt for L3 is essentially the read-eval-print loop (REPL) of Poly/ML. As such, users are free to write arbitrary Standard ML code. The structure **Runtime** enables interaction with L3. The following shows an arithmetic evaluation in Standard ML, as well as in the L3 interpreter (**evalQ**):

```
> 1 + 2;
val it = 3: int
> Runtime.evalQ `0i1 + 2`;
val it = `0i3`: Term.term
```

Various error messages can occur when using the interpreter; for example:

```
> Runtime.evalQ `1 + 2`;
Exception- Fail "Could not infer all types" raised

> Runtime.evalQ `0i2 div 0`;
Exception- Except ("Div", SOME ("div" (int, [`0i2`, `0i0`]))) raised
```

The first error occurs because unannotated number literals are regarded as bit-vector values and here the vector width cannot be inferred.

L3 declarations can be made using the function **loadQ**. The following sequence shows the declaration and manipulation of a global variable:

```
> Runtime.loadQ `declare n :: nat`;
<VAR> n
val it = (): unit

> Runtime.evalQ `n`;
val it = `UNKNOWN::nat`: Term.term

> Runtime.evalQ `{ n <- 3; n }`;
val it = `0n3`: Term.term
```

The global state can be examined using the function **show**; for example:

```
> Runtime.show();
val it = [("n", `0n3`)]: Eval.term_stack
```

The state can be “wiped” clean using the function **reset** and all declarations can be wiped using **resetAll**; for example:

```
> Runtime.reset();
val it = (): unit
> Runtime.show();
val it = [("n", `UNKNOWN::nat`)]: Eval.term_stack

> Runtime.resetAll();
val it = (): unit
> Runtime.show();
val it = []: Eval.term_stack
```

The function ‘**LoadQ**’ is similar to ‘**loadQ**’ but it performs a ‘**resetAll**’ before making declarations.
**Constants**  The structure `Consts` provides various functions for working with L3 constants. Many of the operation in the `Consts` structure will be of little interest to end-users, however it can be used to display some helpful information.

The function `Consts.lookupConst` can be used to lookup an operation; for example:

```plaintext
> Consts.lookupConst "Log2";
val it = Primitive [(bits(a) -> bits(a), -), (nat -> nat, -)]: const
```

Since `Log2` is a primitive operation, only the valid types (ordered by overload priority) are show. With user operations, the internal representation (definition) of the operation will be shown; for example:

```plaintext
> Runtime.loadQ `unit demo () = ()`;
> Consts.lookupConst "demo";
val it = Definition (Abs ["(_", (unit, -))], unit -> unit, `()`), NONE, 1): Consts.const
```

The functions `Consts.listDefinitions` and `Consts.listExceptions` provide details of all user operations and exceptions respectively. The function `Consts.stats` gives a summary of all the current declarations:

```plaintext
> Consts.stats();
Primitives .............. 77
Destructors............... 0
Constructors............. 0
Accessors ............... 0
Exceptions............... 1
User operations.......... 1
Instruction definitions.. 0
```

Here the constructors for primitive types (such as `Cons`) are counted under “primitives”.

**Types**  The `Types` structure can be used to display information relating to L3 types. In particular, this structure provides the functions `Types.lookupConst` and `Types.listConsts`, which are demonstrated below:

```plaintext
> Runtime.loadQ `construct enum {One Two Three}`;
> Types.listConsts();
val it = 
[("enum",
  {eq = true, ast = false, def = Constructors (Enum <dict(3)>), num = 0})]:
  (string * typeconst) list

> Types.lookupConst "int";
val it = SOME {eq = true, ast = false, def = BaseType, num = -1}:
  typeconst option
```
Loading and Exporting Specifications  When writing an L3 specification, it is expected that user declarations will be contained in an L3 source file. A sample instruction set specification (tiny.spec) can be found in the L3 distribution subdirectory examples. This specification is based on Thacker’s pedagogical Tiny 3 architecture; it can be loaded as follows:

```plaintext
> Runtime.LoadF "tiny.spec";
Loading... tiny.spec
<TYP> regT
<TYP> wordT
...
<FUN> test_prog
Done.
```

The string argument to the functions `Runtime.loadF` and `Runtime.LoadF` can be in the form of a comma separated list of files, which will be loaded in order. That is, the call

```plaintext
Runtime.loadF "a.spec, b.spec"
```

will load `a.spec` followed by `b.spec`.

The function `HolExport.export` will export the current L3 specification in the form of an HOL4 script; for example:

```plaintext
> HolExport.export "tiny";
raise'exception ....................... ok.
function ............................. ok.
...
 m'extend ............................... skip.
m'unextend ............................. skip.
Created file: tinyScript.sml
Created file: tinyLib.sml
Created file: tinyLib.sig
Done.
```

The function `HolExport.spec` provides a convenient shortcut for performing `LoadF` and `export` using just a single call; for example, `HolExport.spec ("tiny.spec", "tiny")` will perform the load and export presented above.

L3 is capable of exporting two styles of HOL specification: an explicitly monadic version (based on `state_transformerTheory`); and a version that directly manipulates state using let-expressions (which is the default style). The function `HolExport.monadicExport` can be used to switch between these two styles.

Generated HOL4 specifications are not designed to be manually edited (or human readable). To build an L3 specification script, the HOL library `Import` is needed. This library is currently located in the directory `$HOL_DIR/examples/l3-machine-code/common` and so adding this path to the `INCLUDES` variable of a local Holmakefile is recommended. The examples/l3-machine-code directory contains various examples of using L3 generated specifications.

Testing Specifications  Having written an L3 specification, it is fairly easy to write code (in Standard ML) that runs test vectors. Code for running the `tiny.spec` example can be found in the file `run-tiny.spec`. In this test harness, the Tiny state is initialised, and a small test program is loaded, simply by evaluating the expression `initialize (test_prog)`.

It is also possible to evaluate (run) models that have been exported to HOL4. The file `run-tiny.sml` shows how this can be accomplished for the Tiny example.
A Primitive Types and Operations

The primitive types are as follows:

```
unit bool char nat int bits(·) rounding
```

There are two infix type constructors: ‘*’ for pairs, and ‘-›’ for maps. There are also postfix type constructors ‘set’, ‘option’ and ‘list’; for example, sets of strings are represented by the type ‘string set’. The following sections describe these primitive types, together with some details of associated operations. The following constants and operations are available:

nullary : () Nil None false roundTiesToEven roundTowardNegative roundTowardPositive roundTowardZero true
prefix : ! - ~ Abs Cardinality Cons Difference Drop Element FP32_Abs FP32_Add FP32_Equal FP32_IsNaN FP32_LessThan FP32_Mul FP32_Neg FP32_Sub FP64_Abs FP64_Add FP64_Equal FP64_IsNaN FP64_LessThan FP64_Mul FP64_Neg FP64_Sub FromBinString FromDecString FromHexString Fst Head IndexOf InitMap Intersection IsAlpha IsAlphaNum IsDigit IsHexDigit IsLower IsMember IsSome IsSubset IsUpper Length Log2 Max Min Msb QuotRem Remove RemoveDuplicates RemoveExcept Reverse SetOfList SignExtend SignedMax SignedMin Size Snd Some Tail ToLower ToUpper Union Update ValOf ZeroExtend not
infix : != &~ * ** + , - : < <+ <= <+> <<= >+ >= >++ ?? @ ^ || >>
other : :: <> ·<·>: [·:] fields splitl splitr tokens

Many arithmetic operations are overloaded. Unless otherwise stated, the order of selection is: ‘bits(·)’, ‘nat’, ‘int’ and then ‘bool list’.

A.1 Unit

The type ‘unit’ represents the singleton set containing the value ‘(·)’. The primary use of this type is as the return type for procedures.

A.2 Bool

The type ‘bool’ represents the two element set \( \mathbb{B} = \{ \text{true}, \text{false} \} \). The following standard Boolean operations are available:

```
negation not, ! bool \to bool
conjunction and bool \times bool \to bool
disjunction or bool \times bool \to bool
equality == \alpha \times \alpha \to bool
non-equality <:!,=\alpha \times \alpha \to bool
```

The infix operators ‘and’ and ‘or’ are interpreted with short-circuit evaluation; for example, in the expression ‘false and f(·)’ the value of ‘f(·)’ is not computed, as the overall expression will always evaluate to false. This short-circuiting influences the side-effects that occur during expression evaluation.
There are two prefix symbols for logical negation (these are semantically identical) with ‘!’ binding slightly tighter than ‘not’. This distinction is becomes significant when working with equalities; for example: ‘not a == b’ is taken to be ¬(a = b), whereas ‘!a == b’ is (¬a) = b.

Equality is only defined with respect to equality types, represented here by the type variable α. An equality type is any type built without the use of the map operator ‘->’.

A.3 Nat

The type ‘nat’ represents the natural numbers \( \mathbb{N} = \{0, 1, 2, \ldots\} \). The following operations are available:

<table>
<thead>
<tr>
<th>Operation</th>
<th>Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>logarithm (base two)</td>
<td>Log2 nat → nat</td>
</tr>
<tr>
<td>minimum value</td>
<td>Min nat × nat → nat</td>
</tr>
<tr>
<td>maximum value</td>
<td>Max nat × nat → nat</td>
</tr>
<tr>
<td>less than</td>
<td>&lt; nat × nat → bool</td>
</tr>
<tr>
<td>greater than</td>
<td>&gt; nat × nat → bool</td>
</tr>
<tr>
<td>less than or equal to</td>
<td>&lt;= nat × nat → bool</td>
</tr>
<tr>
<td>greater than or equal to</td>
<td>&gt;= nat × nat → bool</td>
</tr>
<tr>
<td>addition</td>
<td>+ nat × nat → nat</td>
</tr>
<tr>
<td>subtraction</td>
<td>- nat × nat → nat</td>
</tr>
<tr>
<td>multiplication</td>
<td>* nat × nat → nat</td>
</tr>
<tr>
<td>exponentiation</td>
<td>** nat × nat → nat</td>
</tr>
<tr>
<td>division</td>
<td>div nat × nat → nat</td>
</tr>
<tr>
<td>modulus</td>
<td>mod nat × nat → nat</td>
</tr>
</tbody>
</table>

The ‘div’ and ‘mod’ operations satisfy the following property:

\[ \forall n. 0 < n \implies \forall k. k = (k \div n) \times n + (k \mod n) \land (k \mod n) < n. \]

The expressions ‘Log2(0)’, ‘x div 0’ and ‘x mod 0’ are all undefined.\(^1\)

A.4 Int

The type ‘int’ represents the integers \( \mathbb{Z} = \mathbb{N} \cup \{-1, -2, -3, \ldots\} \). The following operations are available:

\(^1\)The interpreter will raise an exception when attempting to evaluate them.
unary minus \(-\) \(\text{int} \to \text{int}\)
absolute value \(\text{Abs}\) \(\text{int} \to \text{int}\)
minimum value \(\text{Min}\) \(\text{int} \times \text{int} \to \text{int}\)
maximum value \(\text{Max}\) \(\text{int} \times \text{int} \to \text{int}\)
less than \(<\) \(\text{int} \times \text{int} \to \text{bool}\)
greater than \(>\) \(\text{int} \times \text{int} \to \text{bool}\)
less than or equal to \(<=\) \(\text{int} \times \text{int} \to \text{bool}\)
greater than or equal to \(>=\) \(\text{int} \times \text{int} \to \text{bool}\)
addition \(+\) \(\text{int} \times \text{int} \to \text{int}\)
subtraction \(-\) \(\text{int} \times \text{int} \to \text{int}\)
multiplication \(*\) \(\text{int} \times \text{int} \to \text{int}\)
exponentiation \(\text{**}\) \(\text{int} \times \text{nat} \to \text{int}\)
quotient and remainder \(\text{QuotRem}\) \(\text{int} \times \text{int} \to \text{int} \times \text{int}\)
quotient \(\text{quot}\) \(\text{int} \times \text{int} \to \text{int}\)
remainder \(\text{rem}\) \(\text{int} \times \text{int} \to \text{int}\)
division \(\text{div}\) \(\text{int} \times \text{int} \to \text{int}\)
modulus \(\text{mod}\) \(\text{int} \times \text{int} \to \text{int}\)

The operation ‘\(\text{Abs}\)’ gives an positive integer value; for example, ‘\(\text{Abs}(-2)\)’ = ‘2’. The integer operations ‘\(\text{quot}\)’ and ‘\(\text{div}\)’ are related to natural number division as follows:

\[
i \text{ quot} j = \begin{cases} 
z(n(i) \text{ div } n(j)) & 0 < j \land 0 \leq i \\
- z(n(-i) \text{ div } n(j)) & 0 < j \land i < 0 \\
- z(n(i) \text{ div } n(-j)) & j < 0 \land 0 \leq i \\
z(n(-i) \text{ div } n(-j)) & j < 0 \land i < 0 \\
\text{undefined} & j = 0 
\end{cases}
\]

and

\[
i \text{ div} j = \begin{cases} 
z(n(i) \text{ div } n(j)) & 0 < j \land 0 \leq i \\
- z(n(-i) \text{ div } n(j)) & 0 < j \land i < 0 \land n(-i) \text{ mod } n(j) = 0 \\
- z(n(i) \text{ div } n(-j)) & j < 0 \land 0 \leq i \land n(i) \text{ mod } n(-j) = 0 \\
z(n(-i) \text{ div } n(-j)) & j < 0 \land i < 0 \\
\text{undefined} & j = 0 
\end{cases}
\]

where \(z : \mathbb{N} \to \mathbb{Z}\) and \(n : \mathbb{Z} \to \mathbb{N}\) are value preserving maps. The ‘\(\text{rem}\)’ and ‘\(\text{mod}\)’ operations satisfy the equations:

\[
i \text{ rem} j = \begin{cases} 
i - (i \text{ quot} j) \times j & j \neq 0 \\
\text{undefined} & j = 0 
\end{cases}
\]

\[
i \text{ mod} j = \begin{cases} 
i - (i \text{ div} j) \times j & j \neq 0 \\
\text{undefined} & j = 0 
\end{cases}
\]

A.5 Bit-vectors

The type ‘\(\text{bits}(n)\)’ represents fixed-width \(n\)-bit) words, also known as bit-vectors; for example, \(\text{bits}(32)\) is the type of 32-bit machine words.\(^{18}\) The bits type can be viewed as representing

\(^{18}\)These are called \textit{double-words (dword)} in the x86 architecture and simply \textit{words} in the ARM architecture.
the set \( \mathbb{Z}_n = \{0, 1, \ldots, 2^n - 1\} \), for some \( 0 < n \). At the type system level no distinction is made between signed and unsigned words; however, some operations (such as orderings) have signed and unsigned variants.

The following bit-vector operations are available:

1's complement \(~\) \hspace{1cm} \text{bits}(n) \rightarrow \text{bits}(n)
2's complement \(-\) \hspace{1cm} \text{bits}(n) \rightarrow \text{bits}(n)
absolute value \text{Abs} \hspace{1cm} \text{bits}(n) \rightarrow \text{bits}(n)
logarithm (base two) \text{Log2} \hspace{1cm} \text{bits}(n) \rightarrow \text{bits}(n)
zero-extend \text{ZeroExtend} \hspace{1cm} \text{bits}(m) \rightarrow \text{bits}(n)
sign-extend \text{SignExtend} \hspace{1cm} \text{bits}(m) \rightarrow \text{bits}(n)
reverse bits \text{Reverse} \hspace{1cm} \text{bits}(n) \rightarrow \text{bits}(n)
minimum value \( (u) \) \text{Min} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
maximum value \( (u) \) \text{Max} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
minimum value \( (s) \) \text{SignedMin} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
maximum value \( (s) \) \text{SignedMax} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
width/size \text{Size} \hspace{1cm} \text{bits}(n) \rightarrow \text{nat}
most-significant (sign) bit \text{Msb} \hspace{1cm} \text{bits}(n) \rightarrow \text{bool}
bit value \text{\cdot<->} \hspace{1cm} \text{bits}(m) \times \text{nat} \rightarrow \text{bool}
bit field \text{\cdot<::>} \hspace{1cm} \text{bits}(m) \times \text{nat} \times \text{nat} \rightarrow \text{bits}(n)
concatenation \text{::} \hspace{1cm} \text{bits}(m) \times \text{bits}(n) \rightarrow \text{bits}(p)
duplication \text{^} \hspace{1cm} \text{bits}(m) \times \text{nat} \rightarrow \text{bits}(n)
less than \( (s) \) \text{<} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bool}
greater than \( (s) \) \text{>} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bool}
less than or equal to \( (s) \) \text{<=} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bool}
greater than or equal to \( (s) \) \text{>=} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bool}
less than \( (u) \) \text{<} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bool}
greater than \( (u) \) \text{>} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bool}
less than or equal to \( (u) \) \text{<=} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bool}
greater than or equal to \( (u) \) \text{>=} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bool}
addition \text{+} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
subtraction \text{-} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
multiplication \text{*} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
quotient \( (s) \) \text{quot} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
remained \( (s) \) \text{rem} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
division \( (u) \) \text{div} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
modulus \( (u) \) \text{mod} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
bitwise conjunction \&\& \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
bitwise disjunction \text{11} \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)
bitwise exclusive-or ?? \hspace{1cm} \text{bits}(n) \times \text{bits}(n) \rightarrow \text{bits}(n)

Unsigned operations are labelled with \( (u) \) and signed operations are labelled with \( (s) \).

The shift operations support shifting by either a bit-vector value or by a natural number value. The overloading is resolved such that the ‘nat’ variant is selected over the ‘bits(\cdot)’ version. The shift operations are:
A number of operations are *undefined* depending on the types of their domain and range. This is detailed in the following table:

<table>
<thead>
<tr>
<th>Operation</th>
<th>Undefined when</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZeroExtend((x \cdot n) \cdot m)</td>
<td>(m &lt; n)</td>
</tr>
<tr>
<td>SignExtend((x \cdot n) \cdot m)</td>
<td>(m &lt; n)</td>
</tr>
<tr>
<td>((x \cdot m) &lt; p)</td>
<td>(m \leq p)</td>
</tr>
<tr>
<td>((x \cdot m) &lt; h : l &gt; n)</td>
<td>(n \neq h + 1 - l \lor h &lt; l \lor m \leq h)</td>
</tr>
<tr>
<td>((x \cdot m) : (y \cdot n) \cdot p)</td>
<td>(p \neq m + n)</td>
</tr>
<tr>
<td>((x \cdot m) ^ n \cdot p)</td>
<td>(p \neq m \cdot n) (which covers (n = 0))</td>
</tr>
</tbody>
</table>

The type checker will fail when these conditions are obviously violated. Alternatively, the interpreter may raise a runtime error when these conditions are not met.

As before the expressions ‘\(\log_2(0)\)’, ‘\(x \div 0\)’, \(\text{rem} 0\)’, ‘\(x \mod 0\)’ and ‘\(x \div 0\)’ are all *undefined*.

### A.6 Bit-strings

Various bit-string operation are defined over the type ‘\(\text{bool list}\)’. These are listed below:

<table>
<thead>
<tr>
<th>Operation</th>
<th>(\text{Undefined when})</th>
</tr>
</thead>
<tbody>
<tr>
<td>bit value</td>
<td>(\cdot \langle - \rangle) (\text{bool list} \times \text{nat} \rightarrow \text{bool})</td>
</tr>
<tr>
<td>bit field</td>
<td>(\cdot \langle - : - \rangle) (\text{bool list} \times \text{nat} \times \text{nat} \rightarrow \text{bool list})</td>
</tr>
<tr>
<td>concatenation</td>
<td>(\cdot :\rangle) (\text{bool list} \times \text{bool list} \rightarrow \text{bool list})</td>
</tr>
<tr>
<td>duplication</td>
<td>(\cdot ^\langle \rangle) (\text{bool list} \times \text{nat} \rightarrow \text{bool list})</td>
</tr>
<tr>
<td>addition</td>
<td>(\cdot +\rangle) (\text{bool list} \times \text{bool list} \rightarrow \text{bool list})</td>
</tr>
<tr>
<td>bitwise conjunction</td>
<td>(\cdot &amp;&amp;\rangle) (\text{bool list} \times \text{bool list} \rightarrow \text{bool list})</td>
</tr>
<tr>
<td>bitwise disjunction</td>
<td>(\cdot</td>
</tr>
<tr>
<td>bitwise exclusive-or</td>
<td>(\cdot ??\rangle) (\text{bool list} \times \text{bool list} \rightarrow \text{bool list})</td>
</tr>
<tr>
<td>left-shift</td>
<td>(\cdot &lt;&lt;\rangle) (\text{bool list} \times \text{nat} \rightarrow \text{bool list})</td>
</tr>
<tr>
<td>right-shift</td>
<td>(\cdot &gt;&gt;&gt;\rangle) (\text{bool list} \times \text{nat} \rightarrow \text{bool list})</td>
</tr>
<tr>
<td>rotate-right</td>
<td>(\cdot #&gt;&gt;\rangle) (\text{bool list} \times \text{nat} \rightarrow \text{bool list})</td>
</tr>
</tbody>
</table>

The bit value operation gives the truth value of a bit-string at a given bit position, for example:

\['0s1101<0> = 'true', \ '0s1101<1> = 'false', \ '0s1101<2> = 'true',\n'0s1101<3> = 'true', \ '0s1101<4> = 'false', \ '0s1101<5> = 'false'.\]
The least-significant bit (position zero) occurs to the right of the bit-string. The bit value operation gives ‘false’ when the bit position is greater than the size of the bit-string.

The bit field operation extracts a range of bits from a bit-string. For example:

‘0s1101<3:1>’ = ‘0s110’ and ‘0s1101<5:2>’ = ‘0s0011’.

In the bit-string expression $x<h:l>$ the size of the result is always $h + 1 - l$ and the result is undefined when $h < l$.

Bit-string addition does not require the arguments to be of the same size and it gives a non-truncated result. For example,

‘0s101 + 0s1’ = ‘0s110’ and ‘0s101 + 0s11’ = ‘0s1000’.

The operation $\ll$ performs a left-shift; for example, ‘0s1101 $\ll$ 2’ = ‘0s110100’. The operation ‘:’ is bit-string concatenation and ‘^’ is duplication; for example:

‘0s101 : 0s11’ = ‘0s10111’ and ‘0s101 ^ 3’ = ‘0s101101101’.

Regular list operations work as one would expect over bit-strings; for example, ‘Head (0s101)’ is a valid expression with value ‘true’.

A.7 Characters and Strings

The following built-in functions are available for characters and strings:

- **is alphabetic**: IsAlpha :: char → bool
- **is alpha-numeric**: IsAlphaNum :: char → bool
- **is decimal digit**: IsDigit :: char → bool
- **is hexadecimal digit**: IsHexDigit :: char → bool
- **is lowercase**: IsLower :: char → bool
- **is uppercase**: IsUpper :: char → bool
- **parse binary**: FromBinString :: string → num option
- **parse decimal**: FromDecString :: string → num option
- **parse hexadecimal**: FromHexString :: string → num option
- **convert to lowercase**: ToLower :: string → string
- **convert to uppercase**:ToUpper :: string → string

The following string operations are useful for parsing:

- **split from the left**: splitl :: (char → bool) × string → string × string
- **split from the right**: splittr :: (char → bool) × string → string × string
- **split into fields**: fields :: (char → bool) × string → string list
- **split into tokens**: tokens :: (char → bool) × string → string list

These operations are based on the Standard ML functions Substring.splitl, Substring.splitr, String.fields and String.tokens. Character predicates are declared using special syntax; for example, the expression:

\[ \text{splitl space or hexdigit and not in set \{#"a", #"f"\} ("12bcab") } \]

gives the result ‘("ab", "12bc")’.
A.8 Floating-point

The L3 language does not provide specialised types for representing floating-point numbers. However, 32-bit (single precision) and 64-bit (double precision) bit-vector values can be treated as floating-point numbers; the following IEEE compliant encoding formats are assumed:

```
31 30 23 22 0
  s  e  m
1  8  23
63 62 52 51 0
  s  e  m
 1 11 52
```

Here ‘s’ is the sign bit (set to ‘1’ for negative numbers), ‘e’ is a biased exponent value and ‘m’ is the mantissa (significant). The following constants and operations are available (where ‘fp’ stands for bits(32) or bits(64), depending on the name of the operation):

- **Rounding modes**: roundTiesToEven, ieee_rounding, roundTowardPositive, roundTowardNegative, roundTowardZero
- **Flags (record)**: DivideByZero, InvalidOp, ieee_flags→bool, Overflow, Precision, Underflow
- **Comparison values**: FP_LT, FP_EQ, FP_GT, FP_UN, ieee_compare
- **Convert format**: FP32_ToFP64, FP64_ToFP32, ieee_rounding × fp64→fp32
- **From integer**: FP{32|64}_FromInt, ieee_rounding × int→fp
- **To integer**: FP{32|64}_ToInt, ieee_rounding × fp→int_option
- **From string**: FP{32|64}, string→fp
- **Negative zero**: FP{32|64}_NegZero, fp
- **Positive zero**: FP{32|64}_PosZero, fp
- **Negative infinity**: FP{32|64}_NegInf, fp
- **Positive infinity**: FP{32|64}_PosInf, fp
- **A quiet NaN**: FP{32|64}_qNan, fp
- **A signalling NaN**: FP{32|64}_sNan, fp
- **Is integral number**: FP{32|64}_IsIntegral, fp→bool
- **Is finite number**: FP{32|64}_IsFinite, fp→bool
- **Is NaN**: FP{32|64}_IsNan, fp→bool
- **Is signalling NaN**: FP{32|64}_IsSignallingNan, fp→bool
- **Is normal number**: FP{32|64}_IsNormal, fp→bool
- **Is sub-normal number**: FP{32|64}_IsSubnormal, fp→bool
- **Comparison**: FP{32|64}_Compare, fp × fp→ieee_compare
- **Equality**: FP{32|64}_Equal, fp × fp→bool
- **Less than**: FP{32|64}_LessThan, fp × fp→bool
- **Less than or equal**: FP{32|64}_LessEqual, fp × fp→bool
- **Greater than**: FP{32|64}_GreaterThan, fp × fp→bool
- **Greater than or equal**: FP{32|64}_GreaterEqual, fp × fp→bool
The L3 interpreter supports evaluation for 32-bit and 64-bit floating-point expressions but only when running on an x86 machine that supports SSE2 extensions (Pentium 4 and later). The fused multiply-add/sub operations can only be interpreted on machines supporting the FMA extension, which came in with the 4th generation (Haswell) Intel processors. These restrictions also apply to any Standard ML code that is generated by L3.

### A.9 Miscellaneous operations

The following operations are available:

<table>
<thead>
<tr>
<th>Operation</th>
<th>Syntax</th>
<th>Signature</th>
</tr>
</thead>
<tbody>
<tr>
<td>Absolute</td>
<td>FP{32</td>
<td>64}_Abs</td>
</tr>
<tr>
<td>Negation</td>
<td>FP{32</td>
<td>64}_Neg</td>
</tr>
<tr>
<td>Square root</td>
<td>FP{32</td>
<td>64}_Sqrt</td>
</tr>
<tr>
<td>(with flags)</td>
<td>FP{32</td>
<td>64}<em>Sqrt</em></td>
</tr>
<tr>
<td>Addition</td>
<td>FP{32</td>
<td>64}_Add</td>
</tr>
<tr>
<td>(with flags)</td>
<td>FP{32</td>
<td>64}<em>Add</em></td>
</tr>
<tr>
<td></td>
<td></td>
<td>ieee_flags × fp</td>
</tr>
<tr>
<td>Subtraction</td>
<td>FP{32</td>
<td>64}_Sub</td>
</tr>
<tr>
<td>(with flags)</td>
<td>FP{32</td>
<td>64}<em>Sub</em></td>
</tr>
<tr>
<td></td>
<td></td>
<td>ieee_flags × fp</td>
</tr>
<tr>
<td>Multiplication</td>
<td>FP{32</td>
<td>64}_Mul</td>
</tr>
<tr>
<td>(with flags)</td>
<td>FP{32</td>
<td>64}<em>Mul</em></td>
</tr>
<tr>
<td></td>
<td></td>
<td>ieee_flags × fp</td>
</tr>
<tr>
<td>Fused multiply-add</td>
<td>FP{32</td>
<td>64}_MulAdd</td>
</tr>
<tr>
<td>(with flags)</td>
<td>FP{32</td>
<td>64}<em>MulAdd</em></td>
</tr>
<tr>
<td></td>
<td></td>
<td>ieee_flags × fp</td>
</tr>
<tr>
<td>Fused multiply-sub</td>
<td>FP{32</td>
<td>64}_MulSub</td>
</tr>
<tr>
<td>(with flags)</td>
<td>FP{32</td>
<td>64}<em>MulSub</em></td>
</tr>
<tr>
<td></td>
<td></td>
<td>ieee_flags × fp</td>
</tr>
<tr>
<td>Division</td>
<td>FP{32</td>
<td>64}_Div</td>
</tr>
<tr>
<td>(with flags)</td>
<td>FP{32</td>
<td>64}<em>Div</em></td>
</tr>
<tr>
<td></td>
<td></td>
<td>ieee_flags × fp</td>
</tr>
</tbody>
</table>

The L3 interpreter supports evaluation for 32-bit and 64-bit floating-point expressions but only when running on an x86 machine that supports SSE2 extensions (Pentium 4 and later). The fused multiply-add/sub operations can only be interpreted on machines supporting the FMA extension, which came in with the 4th generation (Haswell) Intel processors. These restrictions also apply to any Standard ML code that is generated by L3.
Sets are only defined over equality types, see Appendix A.2. The casting map is defined over cast-able types (as represented by the polymorphic type variables \(\alpha\) and \(\beta\)): these are the primitive types (with the exception of ‘unit’) and user defined enumerated types. Assume ‘Enum’ and ‘Enum2’ are declared with:

\[
\text{construct Enum} \{ \text{One, Two, Three} \} \\
\text{construct Enum2} \{ \text{Four, Five, Six, Seven} \}
\]

The cast operations are described in Tables 1 to 7.
### Table 1: Cast to 'bool'

<table>
<thead>
<tr>
<th>From</th>
<th>Operation</th>
<th>Examples</th>
</tr>
</thead>
<tbody>
<tr>
<td>bool</td>
<td>identity</td>
<td>’[true]::bool’ = ‘true’</td>
</tr>
<tr>
<td>string</td>
<td>parse bool</td>
<td>’[&quot;true&quot;]::bool’ = ‘true’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>’[&quot;false&quot;]::bool’ = ‘false’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>otherwise undefined</td>
</tr>
<tr>
<td>nat</td>
<td>is not zero</td>
<td>’[0n3]::bool’ = ‘true’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>’[0n0]::bool’ = ‘false’</td>
</tr>
<tr>
<td>int</td>
<td>is not zero</td>
<td>’[-0i1]::bool’ = “true”</td>
</tr>
<tr>
<td></td>
<td></td>
<td>’[0i0]::bool’ = ‘false’</td>
</tr>
<tr>
<td>bool list</td>
<td>is not zero</td>
<td>’[0s11]::bool’ = ‘true’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>’[0s0]::bool’ = ‘false’</td>
</tr>
<tr>
<td>bits(...)</td>
<td>is not zero</td>
<td>’[3`3]::bool’ = ‘true’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>’[0`3]::bool’ = ‘false’</td>
</tr>
<tr>
<td>Enum</td>
<td>is not the first element</td>
<td>’[One]::bool’ = ‘false’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>’[Two]::bool’ = ‘true’</td>
</tr>
</tbody>
</table>

### Table 2: Cast to 'string'

<table>
<thead>
<tr>
<th>From</th>
<th>Operation</th>
<th>Examples</th>
</tr>
</thead>
<tbody>
<tr>
<td>bool</td>
<td>name</td>
<td>’[true]::string’ = “true”</td>
</tr>
<tr>
<td>string</td>
<td>identity</td>
<td>’[&quot;hello&quot;]::string’ = “hello”</td>
</tr>
<tr>
<td>nat</td>
<td>decimal</td>
<td>’[0n3]::string’ = “3”</td>
</tr>
<tr>
<td>int</td>
<td>signed decimal</td>
<td>’[-0i1]::string’ = “-1”</td>
</tr>
<tr>
<td>bool list</td>
<td>binary</td>
<td>’[0s11]::string’ = “11”</td>
</tr>
<tr>
<td>bits(...)</td>
<td>unsigned hexadecimal</td>
<td>’[139`8]::string’ = “8B”</td>
</tr>
<tr>
<td>Enum</td>
<td>name</td>
<td>’[One]::string’ = “One”</td>
</tr>
</tbody>
</table>
Table 3: Cast to ‘nat’

<table>
<thead>
<tr>
<th>From</th>
<th>Operation</th>
<th>Examples</th>
</tr>
</thead>
<tbody>
<tr>
<td>bool</td>
<td>bit value</td>
<td>‘[false]::nat’ = ‘0n0’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[true]::nat’ = ‘0n1’</td>
</tr>
<tr>
<td>string</td>
<td>parse decimal</td>
<td>‘[“123”]::nat’ = ‘0n123’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[“”]::nat’ is undefined</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[“-1”]::nat’ is undefined</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[“hello”]::nat’ is undefined</td>
</tr>
<tr>
<td>nat</td>
<td>identity</td>
<td>‘[0n123]::nat’ = ‘0n123’</td>
</tr>
<tr>
<td>int</td>
<td>value</td>
<td>‘[0i123]::nat’ = ‘0n123’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[-0i1]::nat’ is undefined</td>
</tr>
<tr>
<td>bool list</td>
<td>value</td>
<td>‘[0s11]::nat’ = ‘0n3’</td>
</tr>
<tr>
<td>bits(·)</td>
<td>unsigned value</td>
<td>‘[139 8]::nat’ = ‘0n139’</td>
</tr>
<tr>
<td>Enum</td>
<td>enumerate (count from zero)</td>
<td>‘[One]::nat’ = ‘0n0’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[Two]::nat’ = ‘0n1’</td>
</tr>
</tbody>
</table>

Table 4: Cast to ‘int’

<table>
<thead>
<tr>
<th>From</th>
<th>Operation</th>
<th>Examples</th>
</tr>
</thead>
<tbody>
<tr>
<td>bool</td>
<td>bit value</td>
<td>‘[false]::int’ = ‘0i0’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[true]::int’ = ‘0i1’</td>
</tr>
<tr>
<td>string</td>
<td>parse decimal</td>
<td>‘[“123”]::int’ = ‘0i123’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[“-123”]::int’ = ‘-0i123’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[“-123”]::int’ = ‘-0i123’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[“”]::int’ is undefined</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[“hello”]::int’ is undefined</td>
</tr>
<tr>
<td>nat</td>
<td>value</td>
<td>‘[0n123]::int’ = ‘0i123’</td>
</tr>
<tr>
<td>int</td>
<td>identity</td>
<td>‘[0i123]::int’ = ‘0i123’</td>
</tr>
<tr>
<td>bool list</td>
<td>value</td>
<td>‘[0s11]::int’ = ‘0i3’</td>
</tr>
<tr>
<td>bits(·)</td>
<td>signed value</td>
<td>‘[123 8]::int’ = ‘0i123’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[130 8]::int’ = ‘-0i126’</td>
</tr>
<tr>
<td>Enum</td>
<td>enumerate</td>
<td>‘[One]::int’ = ‘0i0’</td>
</tr>
</tbody>
</table>
### Table 5: Cast to 'bit-string'

<table>
<thead>
<tr>
<th>From</th>
<th>Operation</th>
<th>Examples</th>
</tr>
</thead>
<tbody>
<tr>
<td>bool</td>
<td>bit value</td>
<td>‘false)::bool list’ = ‘0s0’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘true)::bool list’ = ‘0s1’</td>
</tr>
<tr>
<td>string</td>
<td>parse binary</td>
<td>‘&quot;101&quot;)::bool list’ = ‘0s101’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘&quot;123&quot;)::bool list’ is undefined</td>
</tr>
<tr>
<td>nat</td>
<td>value</td>
<td>‘0n123)::bool list’ = ‘0s1111011’</td>
</tr>
<tr>
<td>int</td>
<td>value</td>
<td>‘0i123)::bool list’ = ‘0s1111011’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘-0i1)::bool list’ is undefined</td>
</tr>
<tr>
<td>bool list</td>
<td>identity</td>
<td>‘0s11)::bool list’ = ‘0s1’</td>
</tr>
<tr>
<td>bits(·)</td>
<td>unsigned value</td>
<td>‘123·8)::bool list’ = ‘0s011111011’</td>
</tr>
<tr>
<td>Enum</td>
<td>enumerate</td>
<td>‘[One]::bool list’ = ‘0s0’</td>
</tr>
</tbody>
</table>

### Table 6: Cast to 'bits(n)'

<table>
<thead>
<tr>
<th>From</th>
<th>Operation</th>
<th>Examples</th>
</tr>
</thead>
<tbody>
<tr>
<td>bool</td>
<td>bit value</td>
<td>‘false]<code>8’ = ‘0x0</code>8’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘true]<code>8’ = ‘0x1</code>8’</td>
</tr>
<tr>
<td>string</td>
<td>parse hexadecimal</td>
<td>‘&quot;8B&quot;]<code>8’ = ‘0x8B</code>8’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘&quot;A&quot;]<code>8’ = ‘0xF6</code>8’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘&quot;100&quot;]`8’ is undefined (too big)</td>
</tr>
<tr>
<td>nat</td>
<td>unsigned value (mod 2^n)</td>
<td>‘0n123]<code>8’ = ‘0x7B</code>8’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘0n256]<code>8’ = ‘0x0</code>8’</td>
</tr>
<tr>
<td>int</td>
<td>signed value (mod 2^n)</td>
<td>‘0i123]<code>8’ = ‘0x7B</code>8’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘0i256]<code>8’ = ‘0x0</code>8’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘-0i1]<code>8’ = ‘0xFF</code>8’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘-0i133]<code>8’ = ‘0x7B</code>8’</td>
</tr>
<tr>
<td>bool list</td>
<td>unsigned value (mod 2^n)</td>
<td>‘0s1111]<code>8’ = ‘0xF</code>8’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘0s1000000001]<code>8’ = ‘0x1</code>8’</td>
</tr>
<tr>
<td>bits(·)</td>
<td>identity / zero extend (p ≤ n) truncate (n &lt; p)</td>
<td>‘0x81]`10’ = ‘0x81·10’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘0xFF0·12]`8’ = ‘0xF0·8’</td>
</tr>
<tr>
<td>Enum</td>
<td>enumerate</td>
<td>‘[One]`8’ = ‘0s0’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[Three]`1’ is undefined (too big)</td>
</tr>
</tbody>
</table>
Table 7: Cast to ‘Enum’

<table>
<thead>
<tr>
<th>From</th>
<th>Operation</th>
<th>Examples</th>
</tr>
</thead>
<tbody>
<tr>
<td>bool</td>
<td>type error</td>
<td></td>
</tr>
<tr>
<td>string</td>
<td>parse</td>
<td>‘[&quot;One&quot;]::Enum’ = ‘One’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[&quot;hello&quot;]::Enum’ is undefined</td>
</tr>
<tr>
<td>nat</td>
<td>select</td>
<td>‘[0n1]::Enum’ = ‘Two’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[0n3]::Enum’ is undefined</td>
</tr>
<tr>
<td>int</td>
<td>select</td>
<td>‘[0i1]::Enum’ = ‘Two’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[0i3]::Enum’ is undefined</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[-0i1]::Enum’ is undefined</td>
</tr>
<tr>
<td>bool list</td>
<td>select</td>
<td>‘[0s1]`8’ = ‘Two’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[0s11]::Enum’ is undefined</td>
</tr>
<tr>
<td>bits(·)</td>
<td>select</td>
<td>‘[1`8]::Enum’ = ‘Two’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[3`8]::Enum’ is undefined</td>
</tr>
<tr>
<td>Enum</td>
<td>identity</td>
<td>‘[One]::Enum’ = ‘One’</td>
</tr>
<tr>
<td>Enum2</td>
<td>select</td>
<td>‘[Four]::Enum’ = ‘One’</td>
</tr>
<tr>
<td></td>
<td></td>
<td>‘[Seven]::Enum’ is undefined</td>
</tr>
</tbody>
</table>


B Syntax

Reserved words

The names of all primitive types, operators and constants are reserved, see Appendix A. Other reserved words are:

assign case clear component construct declare define do else exception for foreach if list match nothing pattern patterns record register return set then type var when RAO! RAZ! UNK! UNKNOWN

Comments

Inline comments start with ‘--’. Block comments are delimited by ‘{-’ and ‘-}’, and can be nested. All commented code is ignored (not parsed).

Number literals

In the following, no white-space is permitted.

\(\langle \text{bin} \rangle ::= '0' | '1'\)

\(\langle \text{dec} \rangle ::= \langle \text{bin} \rangle | '2' | '3' | '4' | '5' | '6' | '7' | '8' | '9'\)

\(\langle \text{hex} \rangle ::= \langle \text{dec} \rangle | 'a' | 'b' | 'c' | 'd' | 'e' | 'f' | 'A' | 'B' | 'C' | 'D' | 'E' | 'F'\)

\(\langle \text{binary} \rangle ::= \langle \text{bin} \rangle | \langle \text{binary} \rangle \langle \text{binary} \rangle | \langle \text{binary} \rangle '_\_\langle \text{binary} \rangle\)

\(\langle \text{decimal} \rangle ::= \langle \text{dec} \rangle | \langle \text{decimal} \rangle \langle \text{decimal} \rangle | \langle \text{decimal} \rangle '_\_\langle \text{decimal} \rangle\)

\(\langle \text{hexadecimal} \rangle ::= \langle \text{hex} \rangle | \langle \text{hex} \rangle \langle \text{hexadecimal} \rangle | \langle \text{hex} \rangle '_\_\langle \text{hexadecimal} \rangle\)

\(\langle \text{number} \rangle ::= \langle \text{decimal} \rangle | '0b' \langle \text{binary} \rangle | '0d' \langle \text{decimal} \rangle | '0x' \langle \text{hexadecimal} \rangle | '0n' \langle \text{decimal} \rangle | '0nb' \langle \text{binary} \rangle | '0nd' \langle \text{decimal} \rangle | '0nx' \langle \text{hexadecimal} \rangle\)

\(\langle \text{numeric} \rangle ::= \langle \text{number} \rangle | '0i' \langle \text{decimal} \rangle | '0ib' \langle \text{binary} \rangle | '0id' \langle \text{decimal} \rangle | '0ix' \langle \text{hexadecimal} \rangle | '0s' \langle \text{decimal} \rangle | '0sb' \langle \text{binary} \rangle | '0sd' \langle \text{decimal} \rangle | '0sx' \langle \text{hexadecimal} \rangle | '0w' \langle \text{decimal} \rangle | '0wb' \langle \text{binary} \rangle | '0wd' \langle \text{decimal} \rangle | '0wx' \langle \text{hexadecimal} \rangle\)

Identifiers

\(\langle \text{alpha} \rangle ::= 'a' .. 'z' | 'A' .. 'Z'\)

\(\langle \text{alphanum} \rangle ::= \langle \text{alpha} \rangle | \langle \text{dec} \rangle | '_\_\_\_\_\_\_\_\_\_\_\_\_'\)

\(\langle \text{alphanums} \rangle ::= \langle \text{alphanum} \rangle | \langle \text{alphanum} \rangle \langle \text{alphanums} \rangle\)

\(\langle \text{name} \rangle ::= \langle \text{alpha} \rangle | \langle \text{alpha} \rangle \langle \text{alphanums} \rangle\)
Types

In the following, white-space is permitted.

\[
\langle type \rangle \ ::= \langle tuple-type \rangle \mid \langle tuple-type \rangle \rightarrow \langle tuple-type \rangle
\]

\[
\langle tuple-type \rangle \ ::= \langle base-type \rangle \mid \langle base-type \rangle^{*} \langle tuple-type \rangle
\]

\[
\langle base-type \rangle \ ::= \text{`unit' | `bool' | `string' | `nat' | `int'} \\
\quad | \text{`bits'} (\langle bits-type \rangle) \text{'} \\
\quad | \langle base-type \rangle \text{`list'} \\
\quad | \langle base-type \rangle \text{`option'} \\
\quad | \langle base-type \rangle \text{`set'} \\
\quad | \langle name \rangle \\
\quad | \text{`C'} \langle type \rangle \text{'}
\]

\[
\langle bits-type \rangle \ ::= \langle name \rangle \mid \langle number \rangle
\]

\[
\langle annotation \rangle \ ::= \text{`.::'} \langle type \rangle \mid `.\.' \langle bits-type \rangle
\]

\[
\langle a-name \rangle \ ::= \langle name \rangle \langle annotation \rangle
\]

\[
\langle opt-a-name \rangle \ ::= \langle name \rangle \mid \langle a-name \rangle
\]

Specifications

\[
\langle specification \rangle \ ::= \langle declaration \rangle \mid \langle declaration \rangle \langle specification \rangle
\]

\[
\langle declaration \rangle \ ::= \langle definition \rangle \\
\quad \mid \text{`type'} \langle name \rangle \text{`='} \langle type \rangle \\
\quad \mid \text{`construct'} \langle name \rangle \text{'} {\lbrace} \langle constructs \rangle \text{'} \\
\quad \mid \text{`record'} \langle name \rangle \text{'} {\lbrace} \langle arguments \rangle \text{'} \\
\quad \mid \text{`register'} \langle a-name \rangle \text{'} {\lbrace} \langle fields \rangle \text{'} \\
\quad \mid \text{`declare'} \langle globals \rangle \\
\quad \mid \text{`exception'} \langle opt-a-name \rangle \\
\quad \mid \text{`pattern'} \langle hints \rangle \\
\quad \mid \text{`clear'} \text{`pattern'} \langle name-list \rangle \\
\quad \mid \text{`clear'} \text{`patterns'}
\]

\[
\langle constructs \rangle \ ::= \langle opt-a-name \rangle \\
\quad \mid \langle opt-a-name \rangle \langle constructs \rangle \\
\quad \mid \langle opt-a-name \rangle \text{'} , \langle constructs \rangle
\]

\[
\langle globals \rangle \ ::= \langle a-name \rangle \mid \text{'} {\lbrace} \langle arguments \rangle \text{'} 	ext{'}
\]

\[
\langle arguments \rangle \ ::= \langle a-name \rangle \\
\quad \mid \langle a-name \rangle \langle arguments \rangle \\
\quad \mid \langle a-name \rangle \text{'} , \langle arguments \rangle
\]

\[
\langle hints \rangle \ ::= \langle hint \rangle \mid \text{'} {\lbrace} \langle hint-sequence \rangle \text{'} 	ext{'}
\]

\[
\langle hint-sequence \rangle \ ::= \langle hint \rangle \mid \langle hint \rangle \langle hint-sequence \rangle \mid \langle hint \rangle \text{'} , \langle hint-sequence \rangle
\]
\langle \text{hint} \rangle ::= \langle \text{names} \rangle \langle \text{annotation} \rangle

\langle \text{names} \rangle ::= \langle \text{name} \rangle | \langle \text{name} \rangle ', \langle \text{names} \rangle | \langle \text{name} \rangle \langle \text{names} \rangle

\langle \text{name-list} \rangle ::= \langle \text{name} \rangle | \langle \text{name} \rangle ', \langle \text{name-list} \rangle

\textbf{Definitions}

\langle \text{definition} \rangle ::= \text{''define} \langle \text{define} \rangle
  \mid \text{''component} \langle \text{component} \rangle
  \mid \langle \text{function} \rangle

\langle \text{define} \rangle ::= \langle \text{ast} \rangle \langle \text{define-sig} \rangle \text{''=} \langle \text{block} \rangle

\langle \text{ast} \rangle ::= \langle \text{name} \rangle | \langle \text{name} \rangle ', \langle \text{ast} \rangle

\langle \text{component} \rangle ::= \langle \text{name} \rangle \langle \text{component-sig} \rangle \{ \text{''value''=} \langle \text{block} \rangle \text{''assign''=} \langle \text{name} \rangle \text{''=} \langle \text{block} \rangle \}

\langle \text{function} \rangle ::= \langle \text{type} \rangle \langle \text{name} \rangle \langle \text{function-sig} \rangle \text{''=} \langle \text{block} \rangle

\langle \text{define-sig} \rangle ::= \langle \text{single-constraint} \rangle
  \mid \text{''} \langle \text{single-constraint} \rangle
  \mid \text{''} \langle \text{single-constraint} \rangle \langle \text{single-constraint} \rangle
  \mid \text{''} \langle \text{single-constraint} \rangle

\langle \text{function-sig} \rangle ::= \langle \text{constraints} \rangle
  \mid \text{''} \langle \text{constraints} \rangle
  \mid \text{''} \langle \text{constraints} \rangle \langle \text{constraints} \rangle
  \mid \text{''} \langle \text{constraints} \rangle

\langle \text{component-sig} \rangle ::= \langle \text{type-constraint} \rangle
  \mid \text{''} \langle \text{type-constraint} \rangle
  \mid \text{''} \langle \text{type-constraint} \rangle \langle \text{type-constraint} \rangle
  \mid \text{''} \langle \text{type-constraint} \rangle

\langle \text{type-constraint} \rangle ::= \text{''} \langle \text{type} \rangle
  \mid \text{''} \langle \text{type} \rangle \langle \text{constraints} \rangle

\langle \text{args} \rangle ::= \langle \text{a-name} \rangle | \langle \text{a-name} \rangle ', \langle \text{args} \rangle

\textbf{Blocks and Statements}

\langle \text{block} \rangle ::= \langle \text{statement} \rangle
  \mid \text{''} \langle \text{sequence} \rangle \text{''} ; \langle \text{return} \rangle \text{''}
  \mid \langle \text{return} \rangle

\langle \text{statement} \rangle ::= \text{''} \langle \text{sequence} \rangle \text{''} | \langle \text{command} \rangle

\langle \text{return} \rangle ::= \text{''return''} \langle \text{expression} \rangle | \langle \text{expression} \rangle

\langle \text{sequence} \rangle ::= \langle \text{command} \rangle | \langle \text{command} \rangle \text{''} ; \langle \text{sequence} \rangle
(command) ::= "#" (name) | "#" (name) (expression-literal) | (name) (expression-literal) | 'if' (expression) 'then' (block) 'else' (block) | 'when' (expression) 'do' (statement) | 'match' (expression) '{' (cases) '}' | 'for' (name) 'in' (expression) '...' (expression) 'do' (statement) | 'foreach' (name) 'in' (expression) 'do' (statement) | 'nothing' | 'var' (name) | 'var' (name) '=' (expression) | (reference) '<=' (expression) | (tuple) '=' (expression)

(tuple) ::= (tuple-literal) | (tuple-literal) ',' (tuple)

(tuple-literal) ::= (tuple-lit) | (tuple-lit) (annotation)

(tuple-lit) ::= (name) | '.' | '(' (tuple) ')

(cases) ::= (case) | (case) (cases)

(case) ::= 'case' (patterns) '=>' (block)

(patterns) ::= (pattern) | (pattern) 'or' (patterns)

Expressions

(expression) ::= (expr) | (expr) (binary-op) (expression)

(expr) ::= (clause) | (clause) (annotation)

(binary-op) ::= ',', '|', 'or', 'and', '==', '!=', '<', '<=', '>', '>=', '<>', '+=', '|=', 'in', 'notin', 'insert', '@', '.', '++', '??', '*', 'quot', 'rem', 'div', 'mod','&&', '<', '>', '>>', '>>>', '<<<', '>>>', '#<<', '^', '

(clause) ::= (unary-op) (expression) | (reference) | (expression-literal) | "#" (name) | "#" (name) (expression-literal) | 'if' (expression) 'then' (expression) 'else' (expression) | 'match' (expression) '{' (expression-cases) '}' | 'set' '{' (expression) '}' | 'list' '{' (expression) '}'

(unary-op) ::= 'not' | '-' | '+' | '-'

(expression-literal) ::= '""' | '"' (expression) '"' | '{' (expression) '}'
The document contains a grammar definition for pattern expressions. The grammar rules are as follows:

- **Expressions**:
  - `<expression-cases>` ::= `<expression-case> | <expression-case> <expression-cases>`
  - `<expression-case>` ::= `'case' <patterns> '=>' <expression>

- **References**:
  - `<reference>` ::= `&` <entity> | `&` <entity> <index> | <dotted> <index> | <dotted> ‘.’ `&` <entity> | <dotted> ‘.’ <dotted> <index>

- **Dotted**:
  - `<dotted>` ::= <entity> | <entity> ‘.’ <dotted>

- **Entities**:
  - `<entity>` ::= <name> | <name> ‘(` expression ‘)’

- **Indexes**:
  - `<index>` ::= ‘<’ <expression> ‘>’

- **Patterns**:
  - `<pattern>` ::= <pat> | <pat> ‘,’ <pattern> | <pat> ‘&’ <pattern>
  - `<pat>` ::= <pattern-literal> | <pattern-literal> <annotation>
  - `<pattern-literal>` ::= <name> ‘(’ <pattern> ‘)’ | <name> ‘(’ <pattern> ‘)’<pattern>
  - `<pattern-lit>` ::= <name> | <number> | ‘_’ | ‘true’ | ‘false’ | ‘UNKNOWN’

- **Bit patterns**:
  - `<bit-pattern>` ::= ‘‘`‘ <bit-pat>` ‘‘
  - `<bit-pat>` ::= <bit-pattern-lit> | <bit-pattern-lit> <bit-pat> | <bit-pattern-lit> ‘.’ <bit-pat>
  - `<bit-pattern-lit>` ::= <pattern-lit> | <pattern-lit> ‘‘`‘ <bits-type> | ‘C’ <binary> ‘)’

Note: The document continues with similar definitions and rules for further components of the grammar.
Constraints

⟨constraints⟩ ::= 'with' ⟨constraint-list⟩

⟨single-constraint⟩ ::= 'with' ⟨name⟩ 'in' ⟨number-list⟩

⟨constraint-list⟩ ::= ⟨constraint⟩ | ⟨constraint⟩ 'and' ⟨constraint-list⟩

⟨constraint⟩ ::= ⟨name⟩ 'in' ⟨number-set⟩ | ⟨name⟩ ⟨ordering⟩ ⟨number⟩

⟨ordering⟩ ::= '<' | '>' | '<=' | '>='

⟨number-list⟩ ::= ⟨number⟩ | ⟨number⟩ ',' ⟨number-list⟩

⟨number-set⟩ ::= ⟨range⟩ | ⟨range⟩ ',' ⟨number-set⟩

⟨range⟩ ::= ⟨number⟩ | ⟨number⟩ '-' ⟨number⟩ | ⟨number⟩ '...' 

Register specifications

⟨fields⟩ ::= ⟨field⟩ | ⟨field⟩ ⟨fields⟩ | ⟨field⟩ ',' ⟨fields⟩

⟨field⟩ ::= ⟨bit-ranges⟩ ':' ⟨field-name⟩

⟨bit-ranges⟩ ::= ⟨bit-range⟩ | ⟨bit-range⟩ ',' ⟨bit-ranges⟩

⟨bit-range⟩ ::= ⟨number⟩ | ⟨number⟩ '-' ⟨number⟩

⟨field-name⟩ ::= 'UNK!' | 'RAZ!' | 'RAO!' | ⟨name⟩