## 2007 Paper 11 Question 1

## Digital Electronics

(a) State De Morgan's theorems.
(b) Simplify the function

$$
f=\bar{a} \bar{b} c \bar{d}+\bar{a} b \bar{c} d+a \bar{b} \bar{c}+a \bar{b} \bar{d}
$$

with don't care states $\bar{a} \bar{b} \bar{c} d$ and $\bar{a} \bar{b} c \bar{d}$ to give expressions in the following forms:
(i) sum of products;
(ii) product of sums.
(c) Simplify the function

$$
f=(\bar{a}+\bar{b}+\bar{c}) \cdot(b+d)
$$

to give an expression in the sum of products form.
(d) Implement with 2-level logic the function in part (c) using only
(i) NOR gates;
(ii) NAND gates.

Assume that complemented input variables are available.

