**1996 Paper 7 Question 4**

**ECAD Topics**

Write brief notes on *each* of the following:

(*a*)  structural hardware description language                    [4 marks]

(*b*)  behavioural hardware description language                   [4 marks]

(*c*)  event-driven simulation                                     [4 marks]

(*d*)  circuit simulation                                          [4 marks]

(*e*)  fault simulation                                            [4 marks]