## 1994 Paper 5 Question 1

## Introduction to Computer Architecture

The MIPS R2000 is a 32-bit architecture and has three basic instruction formats:

I type	op	rs	rt	immediate			
	6	5	5		16		
J type	op	target address					
	6			26			
R type	op	rs	rt	rd	re	funct	
	6	5	5	5	5	6	

Discuss the different ways in which addresses may be constructed for

(a)	data reference		[8  marks]
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(b) effecting transfer of control

[12 marks]