1994 Paper 12 Question 9

Specification and Verification of Hardware

Briefly discuss each of the following topics:

(a)	the representation of schematic diagrams in predicate calculus	[5 marks]
(b)	the use of primitive recursion in hardware specification	[5 marks]
(c)	modelling combinational and sequential circuits	[5 marks]
(d)	temporal abstraction	[5 marks]