The MIPS Instruction Set

Summarise five of the following six subjects concerning the MIPS R2000 in no more than 150 words each (diagrams are allowed and do not count to the word limit):

(a) instruction formats
(b) fetch–execute cycle
(c) delayed load
(d) delayed branch
(e) byte sex or endianness
(f) C function call interface under MIPS Ultrix, DEC’s version of UNIX

[4 marks each]