BYTES and WORDS

```
add r1, pc, #(table-8-.)
mov r2, #0x63
str r2, [r1]
add r1, r1, #1
str r2, [r1]
```

table % 8192 ; 8K of zero bytes

<table>
<thead>
<tr>
<th></th>
<th>0</th>
<th>1</th>
<th>2</th>
<th>3</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>63</td>
<td>00</td>
<td>00</td>
<td>00</td>
</tr>
</tbody>
</table>

When we access words in memory the ARM expects us to provide an address where the bottom two bits are zero, this corresponds to an aligned memory access.