

II. Matrix Multiplication

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Outline

Introduction

Serial Matrix Multiplication

Digression: Multithreading

Multithreaded Matrix Multiplication



Matrix Multiplication

Remember: If $A = (a_{ij})$ and $B = (b_{ij})$ are square $n \times n$ matrices, then the matrix product $C = A \cdot B$ is defined by

$$c_{ij} = \sum_{k=1}^n a_{ik} \cdot b_{kj} \quad \forall i, j = 1, 2, \dots, n.$$



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SQUARE-MATRIX-MULTIPLY(A, B)

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1   $n = A.rows$ 
2  let  $C$  be a new  $n \times n$  matrix
3  for  $i = 1$  to  $n$ 
4      for  $j = 1$  to  $n$ 
5           $c_{ij} = 0$ 
6          for  $k = 1$  to  $n$ 
7               $c_{ij} = c_{ij} + a_{ik} \cdot b_{kj}$ 
8  return  $C$ 
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SQUARE-MATRIX-MULTIPLY(A, B) takes time $\Theta(n^3)$.



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This definition suggests that $n^2 \cdot n = n^3$ arithmetic operations are necessary.

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Divide & Conquer: First Approach

Assumption: n is always an exact power of 2.



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$$A = \begin{pmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{pmatrix}, \quad B = \begin{pmatrix} B_{11} & B_{12} \\ B_{21} & B_{22} \end{pmatrix}, \quad C = \begin{pmatrix} C_{11} & C_{12} \\ C_{21} & C_{22} \end{pmatrix}.$$



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Hence the equation $C = A \cdot B$ becomes:

$$\begin{pmatrix} C_{11} & C_{12} \\ C_{21} & C_{22} \end{pmatrix} = \begin{pmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{pmatrix} \cdot \begin{pmatrix} B_{11} & B_{12} \\ B_{21} & B_{22} \end{pmatrix}$$



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Hence the equation $C = A \cdot B$ becomes:

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This corresponds to the four equations:

$$C_{11} = A_{11} \cdot B_{11} + A_{12} \cdot B_{21}$$

$$C_{12} = A_{11} \cdot B_{12} + A_{12} \cdot B_{22}$$

$$C_{21} = A_{21} \cdot B_{11} + A_{22} \cdot B_{21}$$

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Hence the equation $C = A \cdot B$ becomes:

$$\begin{pmatrix} C_{11} & C_{12} \\ C_{21} & C_{22} \end{pmatrix} = \begin{pmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{pmatrix} \cdot \begin{pmatrix} B_{11} & B_{12} \\ B_{21} & B_{22} \end{pmatrix}$$

This corresponds to the four equations:

$$C_{11} = A_{11} \cdot B_{11} + A_{12} \cdot B_{21}$$

$$C_{12} = A_{11} \cdot B_{12} + A_{12} \cdot B_{22}$$

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Each equation specifies two multiplications of $n/2 \times n/2$ matrices and the addition of their products.



Divide & Conquer: First Approach (Pseudocode)

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SQUARE-MATRIX-MULTIPLY-RECURSIVE(A, B)

```
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2  let  $C$  be a new  $n \times n$  matrix
3  if  $n == 1$ 
4       $c_{11} = a_{11} \cdot b_{11}$ 
5  else partition  $A, B$ , and  $C$  as in equations (4.9)
6       $C_{11} = \text{SQUARE-MATRIX-MULTIPLY-RECURSIVE}(A_{11}, B_{11})$ 
           +  $\text{SQUARE-MATRIX-MULTIPLY-RECURSIVE}(A_{12}, B_{21})$ 
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10 return  $C$ 
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$$C_{11} = A_{11} \cdot B_{11} + A_{12} \cdot B_{21}$$

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Line 5: Handle submatrices implicitly through index calculations instead of creating them.

$$C_{11} = A_{11} \cdot B_{11} + A_{12} \cdot B_{21}$$

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Let $T(n)$ be the runtime of this procedure.



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Let $T(n)$ be the runtime of this procedure. Then:

$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1, \\ & \text{if } n > 1. \end{cases}$$



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8 Multiplications



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```

Let $T(n)$ be the runtime of this procedure. Then:

$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1, \\ 8 \cdot T(n/2) & \text{if } n > 1. \end{cases}$$

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4 Additions and Partitioning



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```

Let $T(n)$ be the runtime of this procedure. Then:

$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1, \\ 8 \cdot T(n/2) + \Theta(n^2) & \text{if } n > 1. \end{cases}$$

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```

Let $T(n)$ be the runtime of this procedure. Then:

$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1, \\ 8 \cdot T(n/2) + \Theta(n^2) & \text{if } n > 1. \end{cases}$$

Solution: $T(n) =$



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$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1, \\ 8 \cdot T(n/2) + \Theta(n^2) & \text{if } n > 1. \end{cases}$$

Solution: $T(n) = \Theta(8^{\log_2 n})$



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Let $T(n)$ be the runtime of this procedure. Then:

$$T(n) = \begin{cases} \Theta(1) & \text{if } n = 1, \\ 8 \cdot T(n/2) + \Theta(n^2) & \text{if } n > 1. \end{cases}$$

Solution: $T(n) = \Theta(8^{\log_2 n}) = \Theta(n^3)$

No improvement over the naive algorithm!



Divide & Conquer: First Approach (Pseudocode)

```
SQUARE-MATRIX-MULTIPLY-RECURSIVE( $A, B$ )
1   $n = A.rows$ 
2  let  $C$  be a new  $n \times n$  matrix
3  if  $n == 1$ 
4       $c_{11} = a_{11} \cdot b_{11}$ 
5  else partition  $A, B$ , and  $C$  as in equations (4.9)
6       $C_{11} = \text{SQUARE-MATRIX-MULTIPLY-RECURSIVE}(A_{11}, B_{11})$ 
           +  $\text{SQUARE-MATRIX-MULTIPLY-RECURSIVE}(A_{12}, B_{21})$ 
7       $C_{12} = \text{SQUARE-MATRIX-MULTIPLY-RECURSIVE}(A_{11}, B_{12})$ 
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10 return  $C$ 
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Goal: Reduce the number of multiplications



Divide & Conquer: Second Approach

Idea: Make the recursion tree less bushy by performing only **7** recursive multiplications of $n/2 \times n/2$ matrices.



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Strassen's Algorithm (1969)

1. Partition each of the matrices into four $n/2 \times n/2$ submatrices
2. Create 10 matrices S_1, S_2, \dots, S_{10} . Each is $n/2 \times n/2$ and is the **sum or difference** of two matrices created in the previous step.
3. Recursively compute **7 matrix products** P_1, P_2, \dots, P_7 , each $n/2 \times n/2$
4. Compute $n/2 \times n/2$ submatrices of C by **adding and subtracting** various combinations of the P_j .



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Time for steps 1,2,4: $\Theta(n^2)$, hence $T(n) = 7 \cdot T(n/2) + \Theta(n^2) \Rightarrow T(n) = \Theta(n^{\log 7})$.



Solving the Recursion

$$T(n) = 7 \cdot T(n/2) + c \cdot n^2$$



Details of Strassen's Algorithm

The 10 Submatrices and 7 Products

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- $O(n^{2.517})$, Romani (1982)
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- $O(n^{2.3728642})$, V. Williams (2011)
- $O(n^{2.3728639})$, Le Gall (2014)
- ...



Outline

Introduction

Serial Matrix Multiplication

Digression: Multithreading

Multithreaded Matrix Multiplication



Memory Models

Distributed Memory

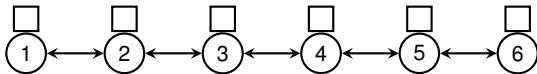
- Each processor has its private memory
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Memory Models

Distributed Memory

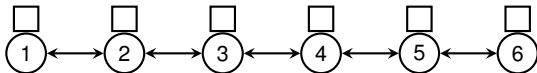
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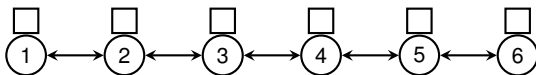
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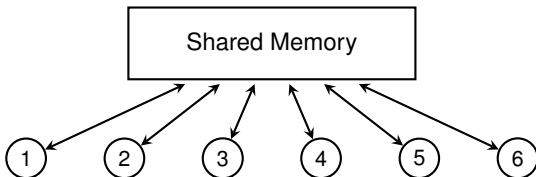
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Dynamic Multithreading

- Programming shared-memory parallel computer difficult




Dynamic Multithreading

- Programming shared-memory parallel computer difficult
- Use [concurrency platform](#) which coordinates all resources



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Scheduling jobs, communication protocols, load balancing etc.



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Only logical parallelism, but not actual!
Need a **scheduler** to map threads to processors.

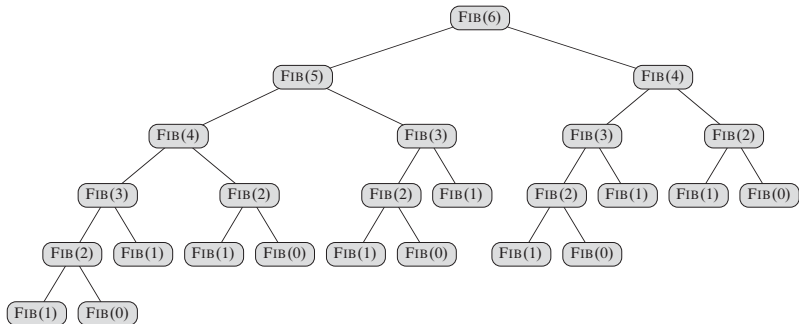


Computing Fibonacci Numbers Recursively (Fig. 27.1)

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1:   if n<=1 return n
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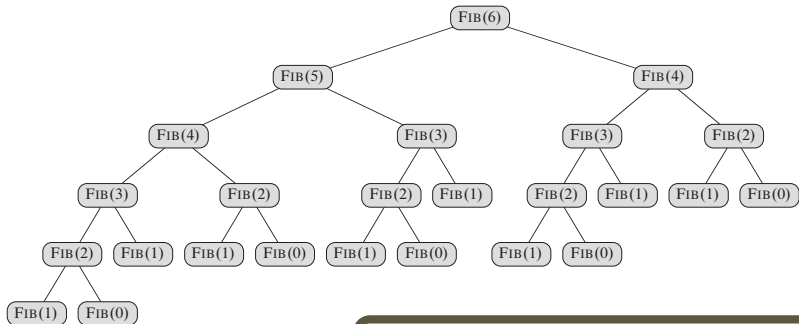
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Very inefficient – exponential time!

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Computing Fibonacci Numbers in Parallel (Fig. 27.2)

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0: P-FIB(n)
1:   if n<=1 return n
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5:         return x+y
```



Computing Fibonacci Numbers in Parallel (Fig. 27.2)

- Without **spawn** and **sync** same pseudocode as before
- **spawn** does not imply parallel execution (depends on scheduler)

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Computing Fibonacci Numbers in Parallel (Fig. 27.2)

Computation Dag $G = (V, E)$

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Computing Fibonacci Numbers in Parallel (Fig. 27.2)

Computation Dag $G = (V, E)$

- V set of threads (instructions/strands **without parallel control**)

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Computing Fibonacci Numbers in Parallel (Fig. 27.2)

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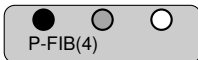


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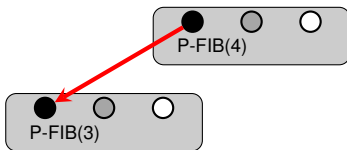
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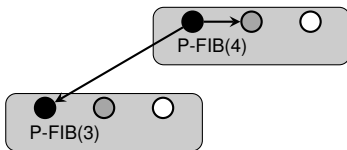
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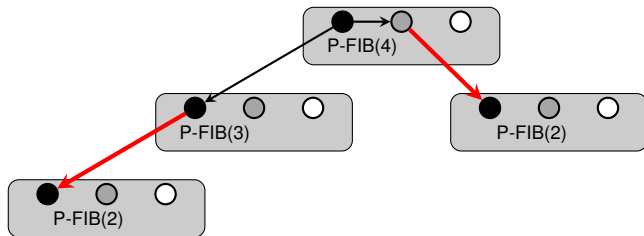
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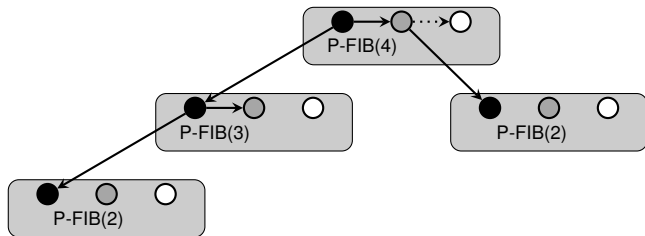
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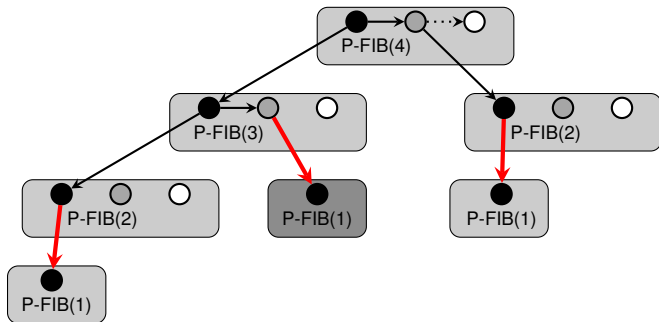
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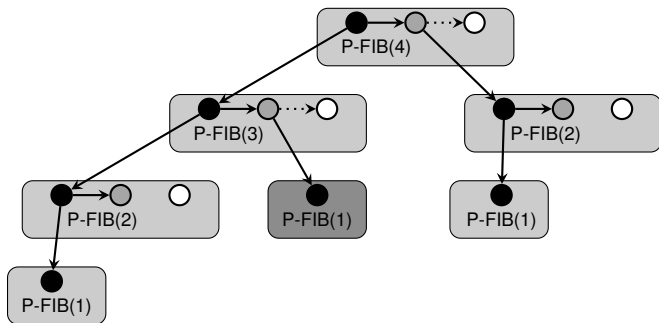
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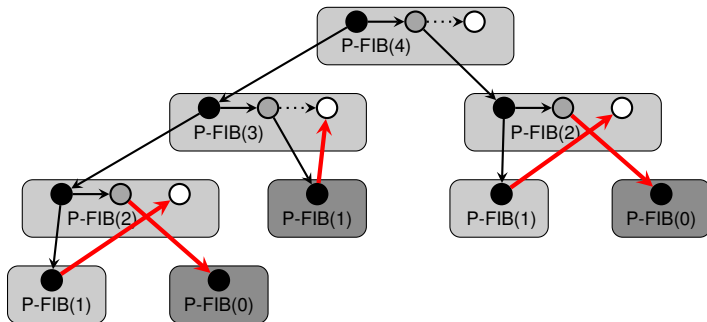
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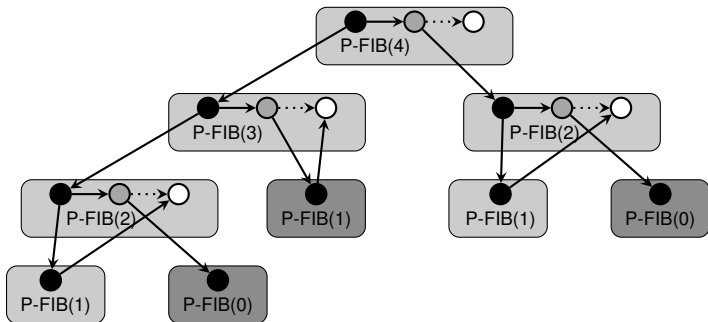
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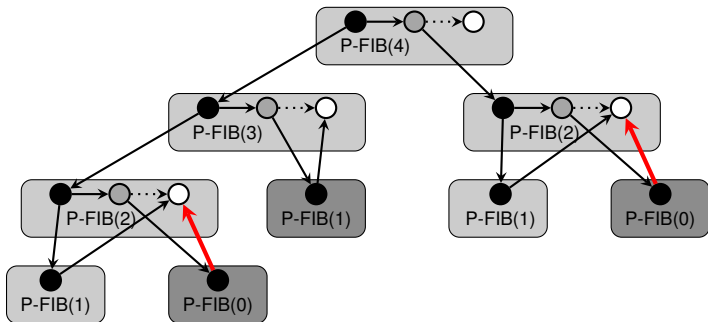
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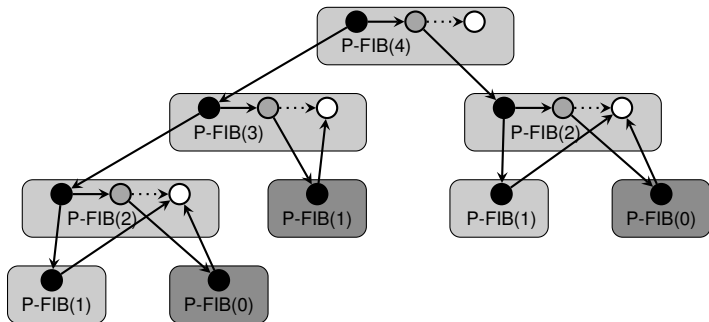
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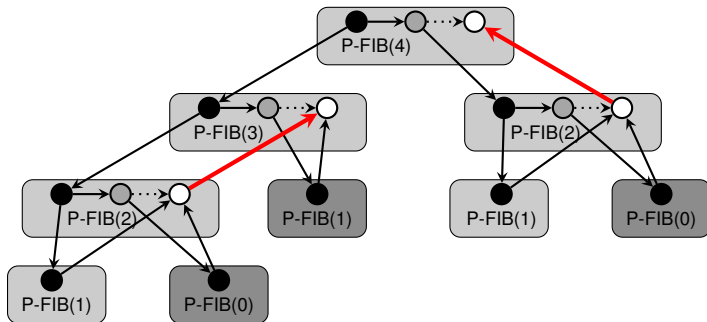
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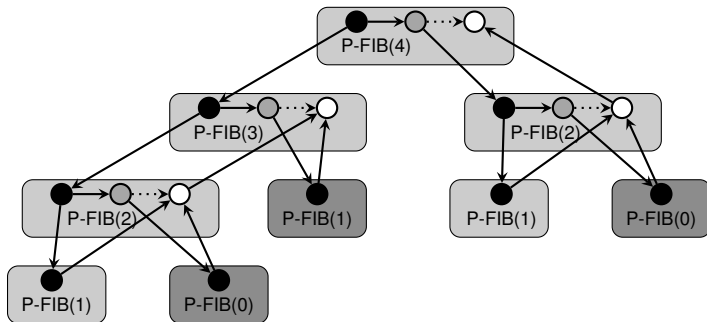
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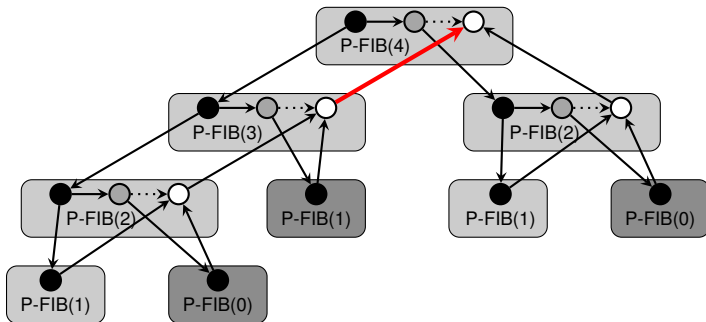
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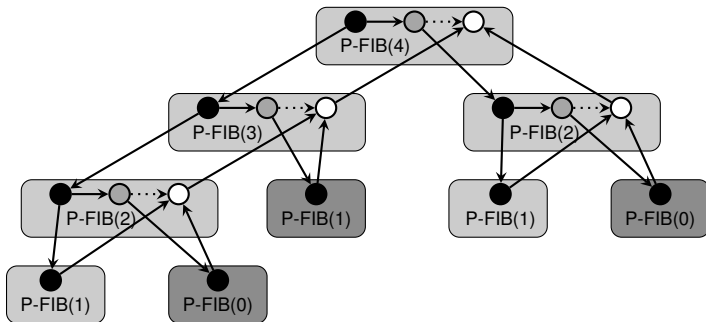
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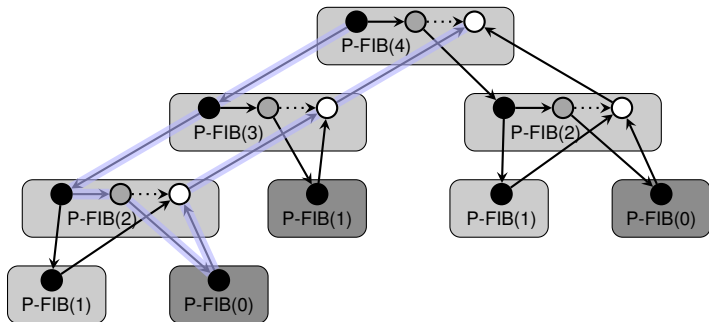
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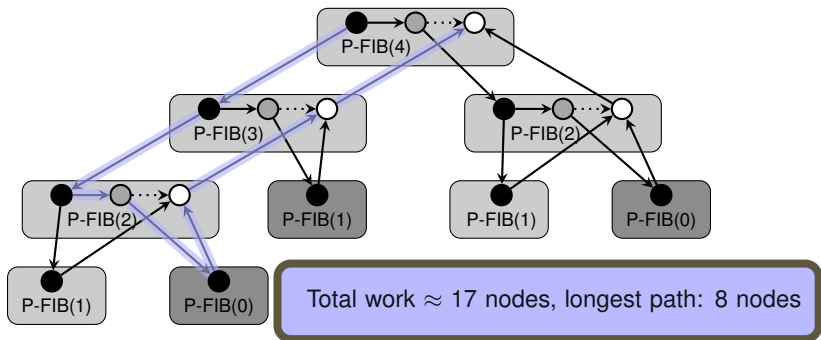
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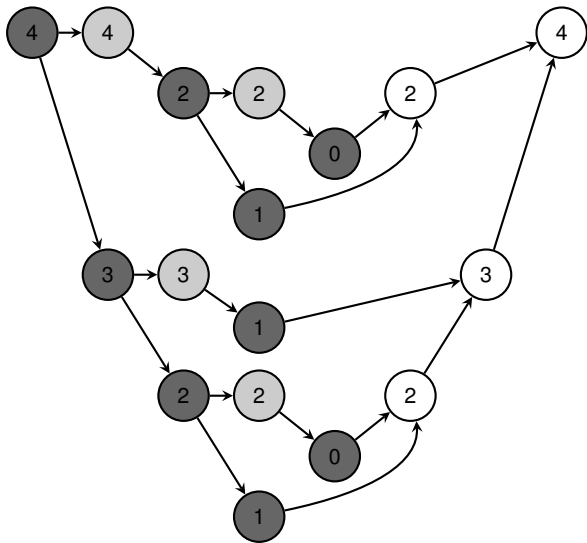
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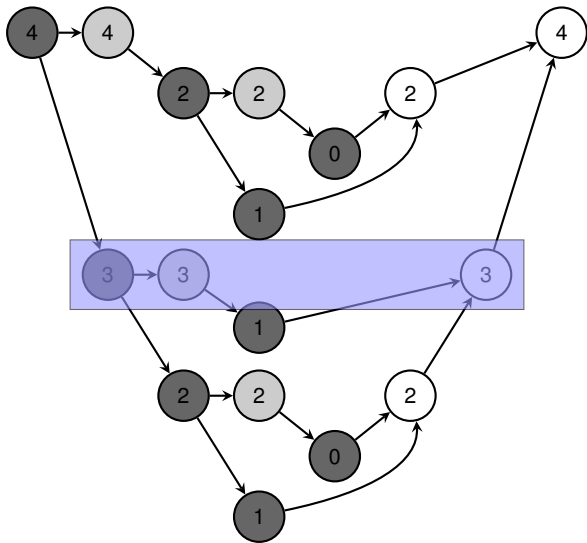
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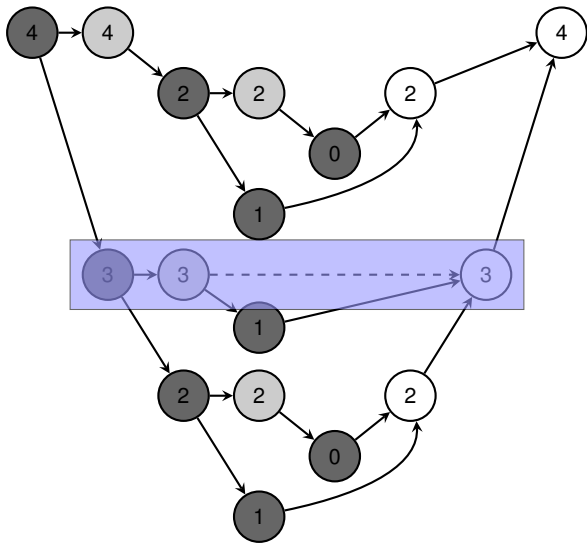
Computing Fibonacci Numbers in Parallel (DAG Perspective)



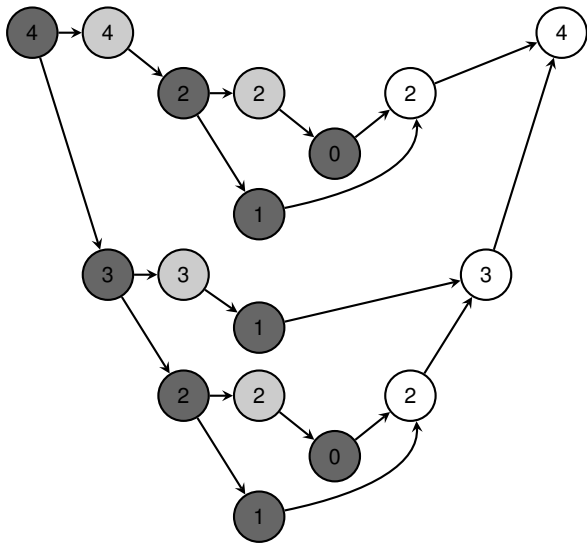
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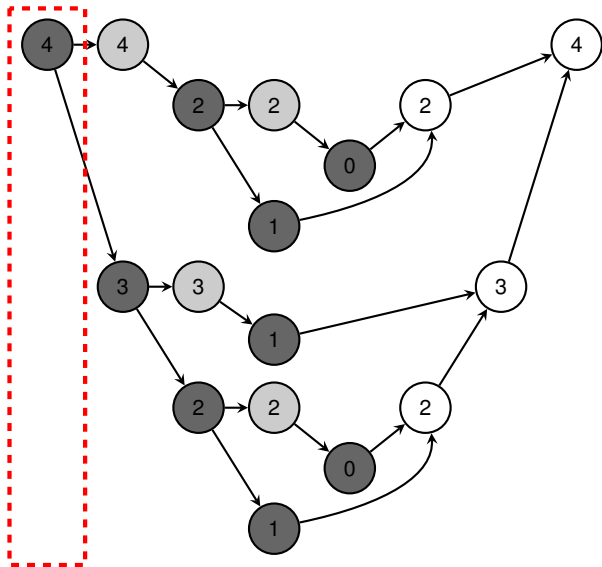
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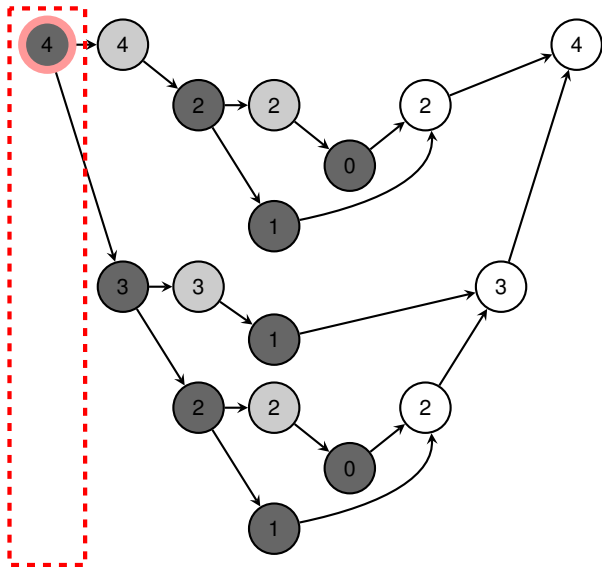
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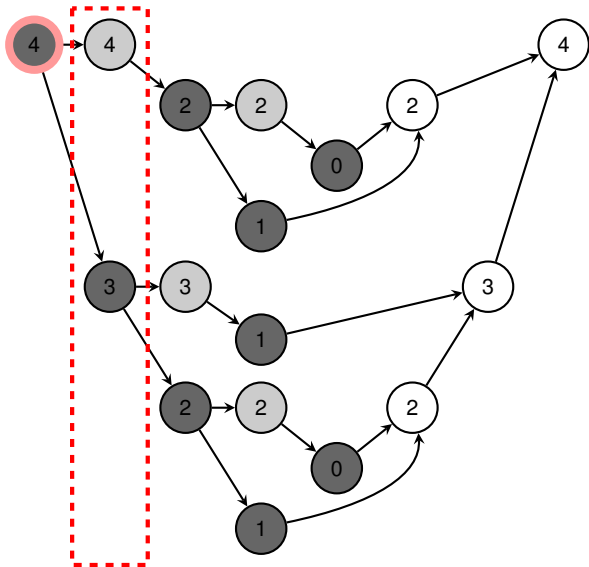
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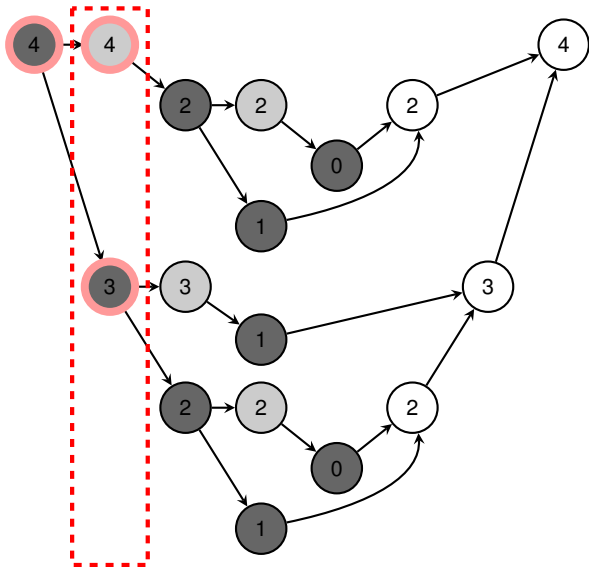
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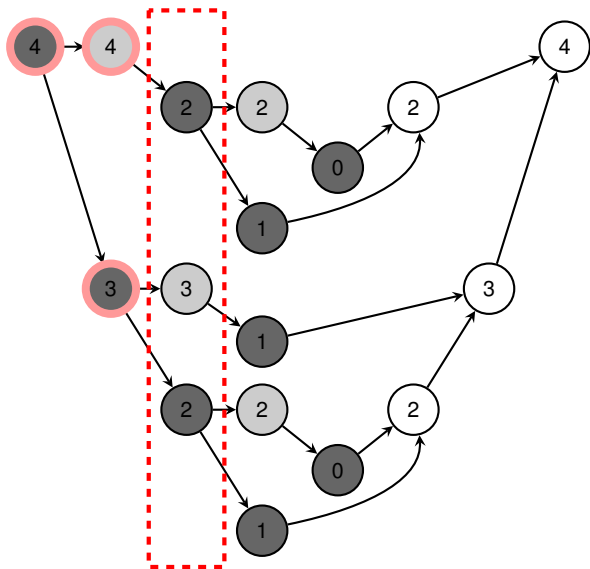
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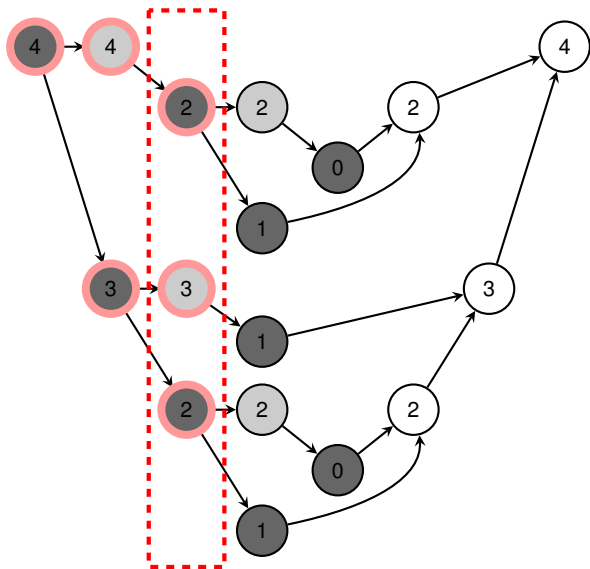
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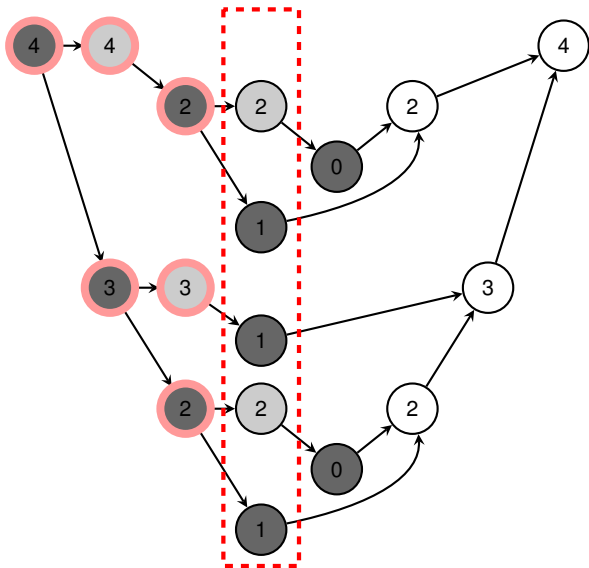
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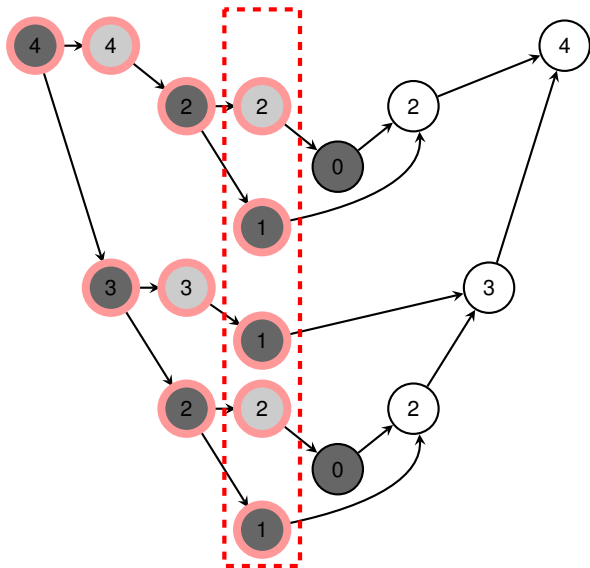
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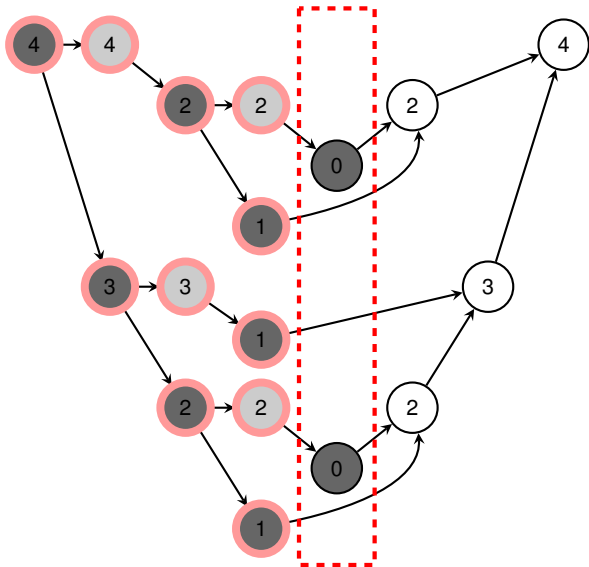
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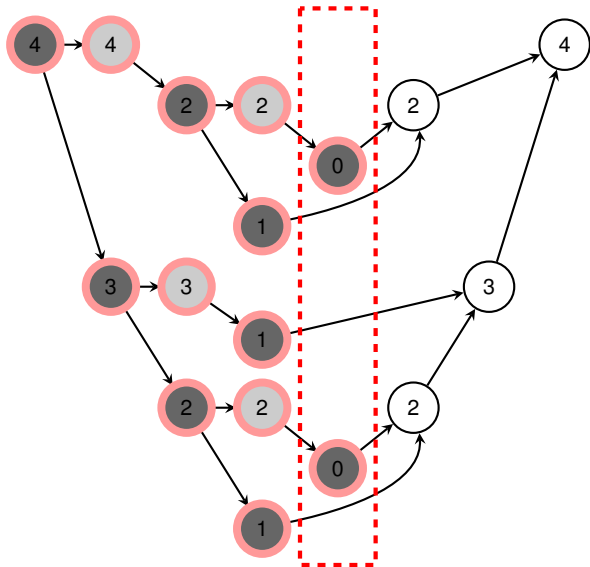
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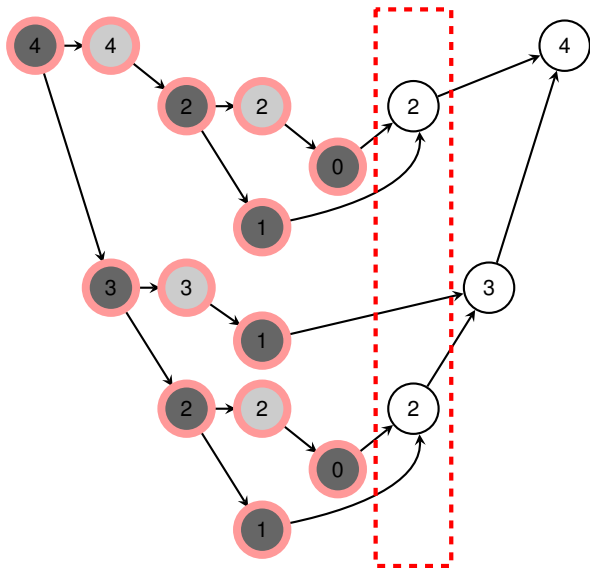
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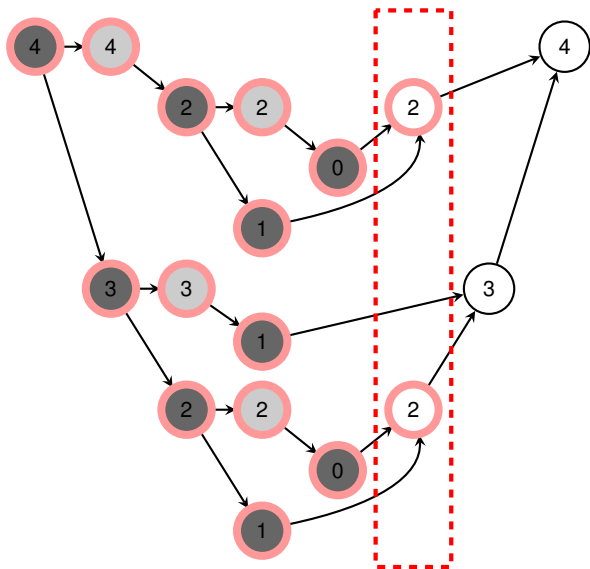
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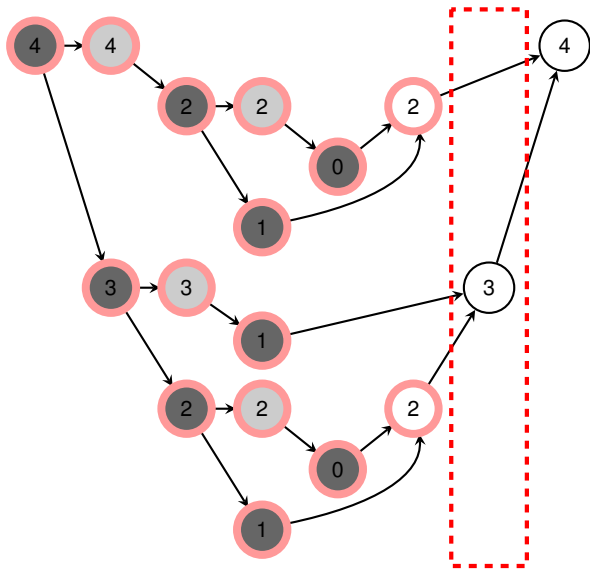
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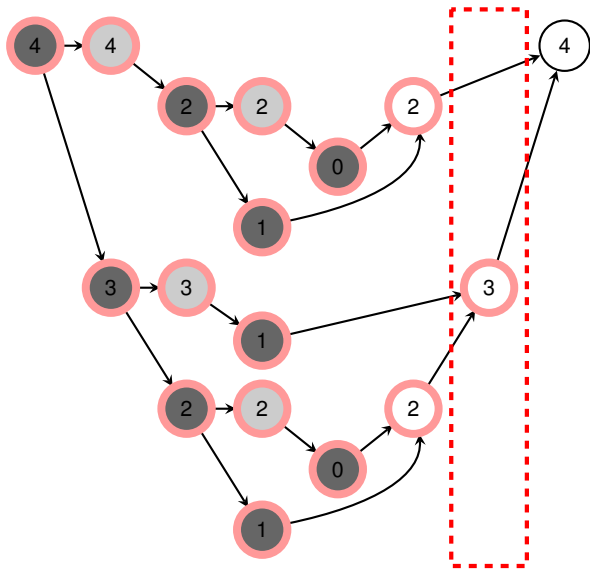
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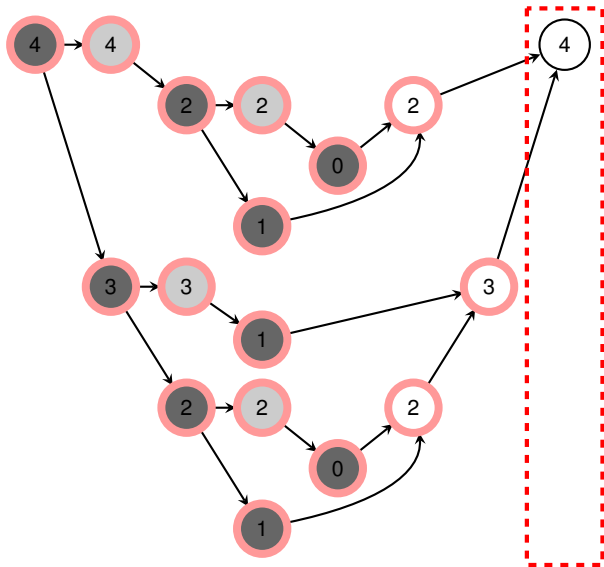
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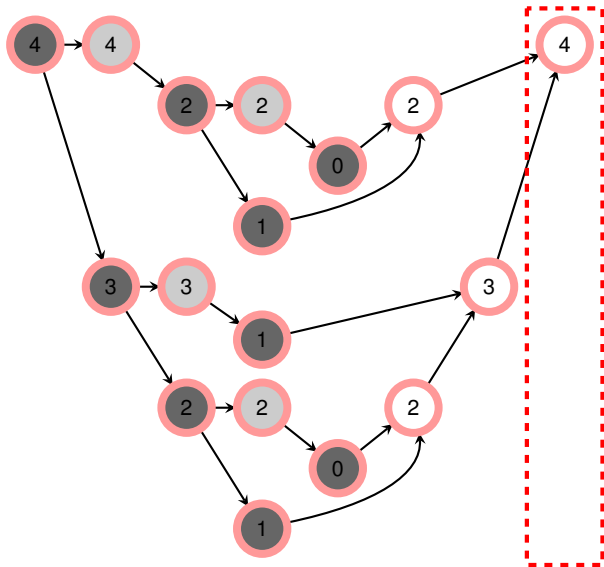
Computing Fibonacci Numbers in Parallel (DAG Perspective)



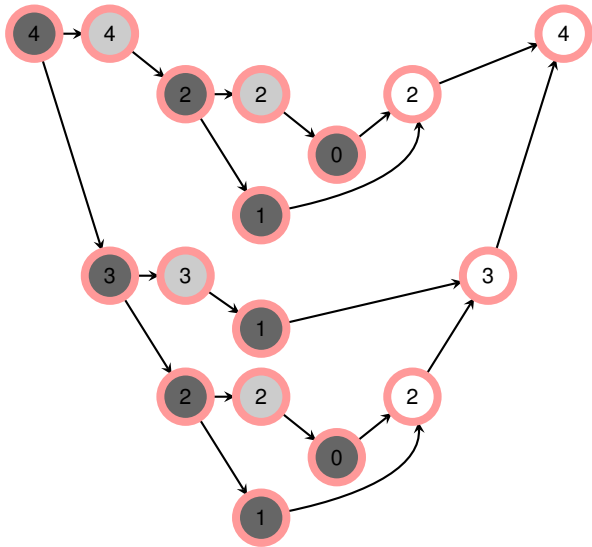
Computing Fibonacci Numbers in Parallel (DAG Perspective)



Computing Fibonacci Numbers in Parallel (DAG Perspective)



Computing Fibonacci Numbers in Parallel (DAG Perspective)



Performance Measures

Work

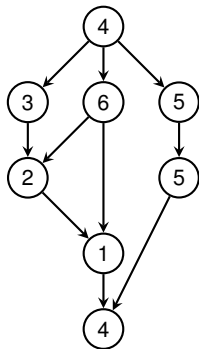
Total time to execute everything on a single processor.



Performance Measures

Work

Total time to execute everything on a single processor.

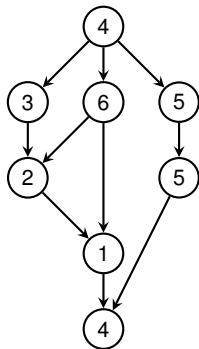


Performance Measures

Work

Total time to execute everything on a single processor.

$$\Sigma = 30$$



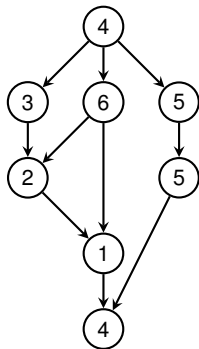
Performance Measures

Work

Total time to execute everything on a single processor.

Span

Longest time to execute the threads along any path.



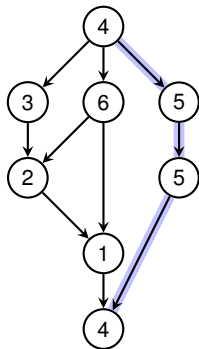
Performance Measures

Work

Total time to execute everything on a single processor.

Span

Longest time to execute the threads along any path.



Performance Measures

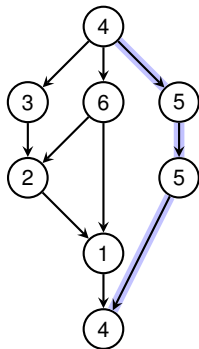
Work

Total time to execute everything on a **single processor**.

Span

Longest time to execute the threads along any path.

$$\Sigma = 18$$



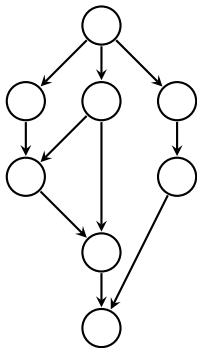
Performance Measures

Work

Total time to execute everything on a single processor.

Span

Longest time to execute the threads along any path.



Performance Measures

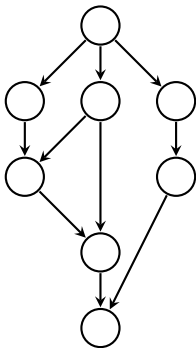
Work

Total time to execute everything on a single processor.

Span

Longest time to execute the threads along any path.

If each thread takes unit time, span is the length of the critical path.



Performance Measures

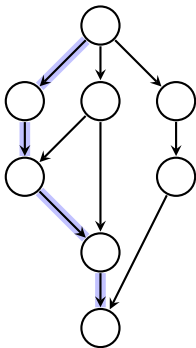
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Total time to execute everything on a single processor.

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Longest time to execute the threads along any path.

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Performance Measures

Work

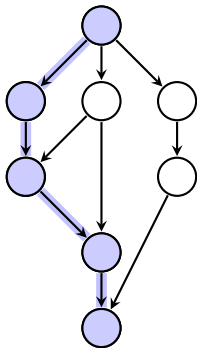
Total time to execute everything on a single processor.

Span

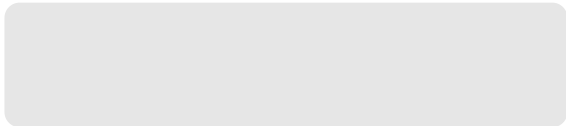
Longest time to execute the threads along any path.

If each thread takes unit time, span is the length of the critical path.

#nodes = 5



Work Law and Span Law



Work Law and Span Law

- $T_1 = \text{work}$, $T_\infty = \text{span}$



Work Law and Span Law

- $T_1 = \text{work}$, $T_\infty = \text{span}$
- $P = \text{number of (identical) processors}$
- $T_P = \text{running time on } P \text{ processors}$



Work Law and Span Law

- $T_1 = \text{work}$, $T_\infty = \text{span}$
- $P = \text{number of (identical) processors}$
- $T_P = \text{running time on } P \text{ processors}$

Running time actually also depends on scheduler etc.!

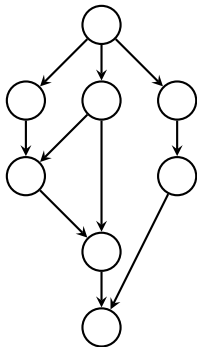


Work Law and Span Law

- $T_1 = \text{work}$, $T_\infty = \text{span}$
- $P = \text{number of (identical) processors}$
- $T_P = \text{running time on } P \text{ processors}$

Work Law

$$T_P \geq \frac{T_1}{P}$$



Work Law and Span Law

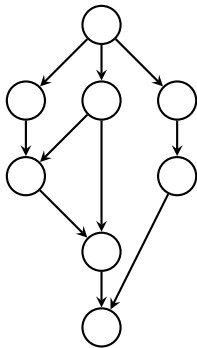
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Work Law

$$T_P \geq \frac{T_1}{P}$$

Time on P processors can't be shorter than if all work all time

$$T_1 = 8, P = 2$$



Work Law and Span Law

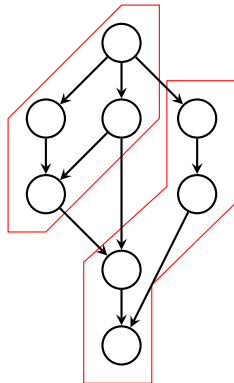
- $T_1 = \text{work}$, $T_\infty = \text{span}$
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$$T_1 = 8, P = 2$$



Work Law and Span Law

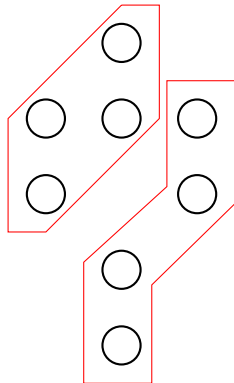
- $T_1 = \text{work}$, $T_\infty = \text{span}$
- $P = \text{number of (identical) processors}$
- $T_P = \text{running time on } P \text{ processors}$

Work Law

$$T_P \geq \frac{T_1}{P}$$

Time on P processors can't be shorter than if all work all time

$$T_1 = 8, P = 2$$



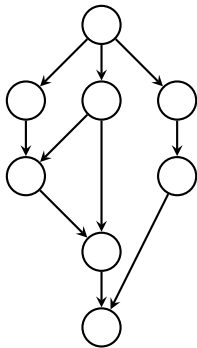
Work Law and Span Law

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Work Law and Span Law

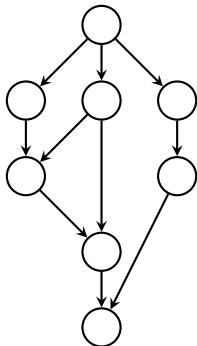
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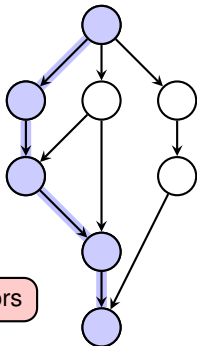
$$T_P \geq \frac{T_1}{P}$$

Span Law

$$T_P \geq T_\infty$$

Time on P processors can't be shorter than time on ∞ processors

$$T_\infty = 5$$



Work Law and Span Law

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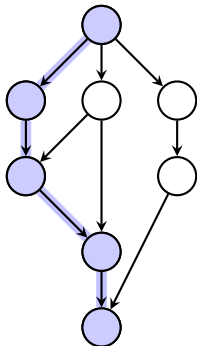
$$T_P \geq \frac{T_1}{P}$$

Span Law

$$T_P \geq T_\infty$$

- Speed-Up: $\frac{T_1}{T_P}$

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Work Law and Span Law

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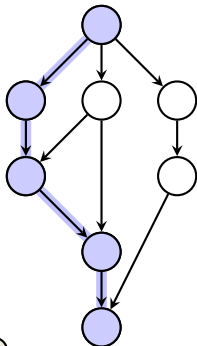
Span Law

$$T_P \geq T_\infty$$

- Speed-Up: $\frac{T_1}{T_P}$

Maximum Speed-Up bounded by P !

$$T_\infty = 5$$



Work Law and Span Law

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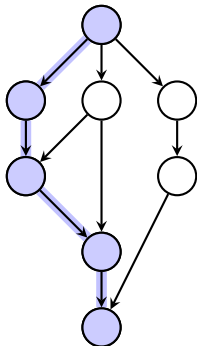
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- Speed-Up: $\frac{T_1}{T_P}$
- Parallelism: $\frac{T_1}{T_\infty}$



Work Law and Span Law

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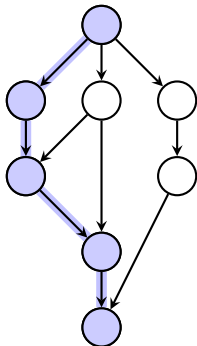
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$$T_\infty = 5$$



- Speed-Up: $\frac{T_1}{T_P}$
- Parallelism: $\frac{T_1}{T_\infty}$

Maximum Speed-Up for ∞ processors!



Outline

Introduction

Serial Matrix Multiplication

Digression: Multithreading

Multithreaded Matrix Multiplication



Warmup: Matrix Vector Multiplication

Remember: Multiplying an $n \times n$ matrix $A = (a_{ij})$ and n -vector $x = (x_j)$ yields an n -vector $y = (y_i)$ given by

$$y_i = \sum_{j=1}^n a_{ij}x_j \quad \text{for } i = 1, 2, \dots, n.$$



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MAT-VEC(A, x)

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How can a compiler implement the **parallel for**-loop?



Implementing `parallel for` based on Divide-and-Conquer

MAT-VEC-MAIN-LOOP(A, x, y, n, i, i')

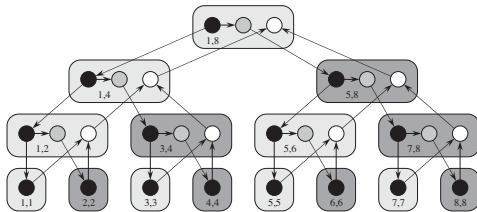
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Implementing parallel for based on Divide-and-Conquer



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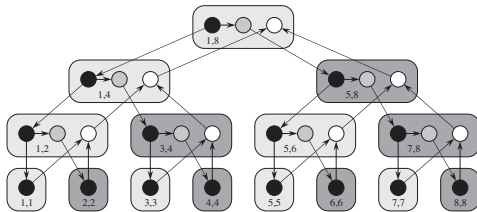
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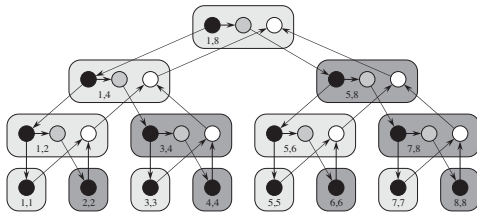
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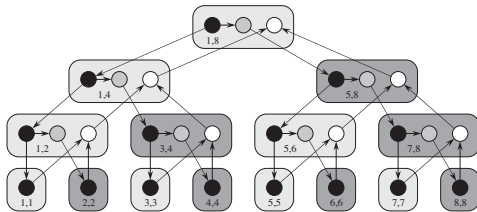
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Work is equal to running time of its serialization; overhead of recursive spawning does not change asymptotically.



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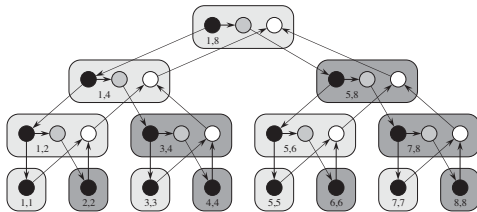
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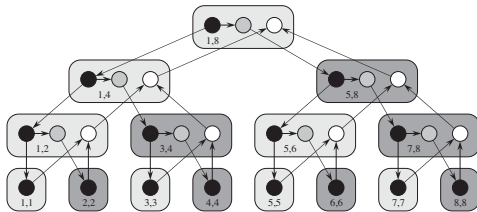
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$$T_\infty(n) =$$

Span is the depth of recursive callings plus the maximum span of any of the n iterations.



Implementing parallel for based on Divide-and-Conquer



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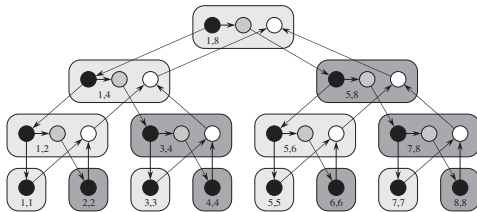
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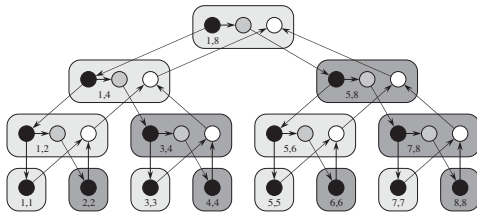
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$$T_\infty(n) = \Theta(\log n) + \max_{1 \leq i \leq n} \text{iter}(n)$$

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Implementing parallel for based on Divide-and-Conquer



MAT-VEC-MAIN-LOOP(A, x, y, n, i, i')

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```

$$T_1(n) = \Theta(n^2)$$

Work is equal to running time of its serialization; overhead of recursive spawning does not change asymptotically.

$$T_\infty(n) = \Theta(\log n) + \max_{1 \leq i \leq n} \text{iter}(n) \\ = \Theta(n).$$

Span is the depth of recursive callings plus the maximum span of any of the n iterations.



Naive Algorithm in Parallel

P-SQUARE-MATRIX-MULTIPLY(A, B)

```
1   $n = A.rows$ 
2  let  $C$  be a new  $n \times n$  matrix
3  parallel for  $i = 1$  to  $n$ 
4      parallel for  $j = 1$  to  $n$ 
5           $c_{ij} = 0$ 
6          for  $k = 1$  to  $n$ 
7               $c_{ij} = c_{ij} + a_{ik} \cdot b_{kj}$ 
8  return  $C$ 
```



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7               $c_{ij} = c_{ij} + a_{ik} \cdot b_{kj}$ 
8  return  $C$ 
```

With a more careful implementation,
 $T_\infty(n) = O(\log n)$ (CLRS, Exercise 27.2-3)

P-SQUARE-MATRIX-MULTIPLY(A, B) has work $T_1(n) = \Theta(n^3)$ and span $T_\infty(n) = \Theta(n)$.

The first two nested for-loops parallelise perfectly.



The Simple Divide&Conquer Approach in Parallel

P-MATRIX-MULTIPLY-RECURSIVE(C, A, B)

```
1   $n = A.rows$ 
2  if  $n == 1$ 
3       $c_{11} = a_{11}b_{11}$ 
4  else let  $T$  be a new  $n \times n$  matrix
5      partition  $A, B, C$ , and  $T$  into  $n/2 \times n/2$  submatrices
           $A_{11}, A_{12}, A_{21}, A_{22}; B_{11}, B_{12}, B_{21}, B_{22}; C_{11}, C_{12}, C_{21}, C_{22};$ 
          and  $T_{11}, T_{12}, T_{21}, T_{22};$  respectively
6      spawn P-MATRIX-MULTIPLY-RECURSIVE( $C_{11}, A_{11}, B_{11}$ )
7      spawn P-MATRIX-MULTIPLY-RECURSIVE( $C_{12}, A_{11}, B_{12}$ )
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12     spawn P-MATRIX-MULTIPLY-RECURSIVE( $T_{21}, A_{22}, B_{21}$ )
13     P-MATRIX-MULTIPLY-RECURSIVE( $T_{22}, A_{22}, B_{22}$ )
14     sync
15     parallel for  $i = 1$  to  $n$ 
16         parallel for  $j = 1$  to  $n$ 
17              $c_{ij} = c_{ij} + t_{ij}$ 
```



The Simple Divide&Conquer Approach in Parallel

```
P-MATRIX-MULTIPLY-RECURSIVE( $C, A, B$ )
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15     parallel for  $i = 1$  to  $n$ 
16         parallel for  $j = 1$  to  $n$ 
17              $c_{ij} = c_{ij} + t_{ij}$ 
```

The same as before.

P-MATRIX-MULTIPLY-RECURSIVE has work $T_1(n) = \Theta(n^3)$ and span $T_\infty(n) =$



The Simple Divide&Conquer Approach in Parallel

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3       $c_{11} = a_{11}b_{11}$ 
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5      partition  $A, B, C$ , and  $T$  into  $n/2 \times n/2$  submatrices
           $A_{11}, A_{12}, A_{21}, A_{22}; B_{11}, B_{12}, B_{21}, B_{22}; C_{11}, C_{12}, C_{21}, C_{22};$ 
          and  $T_{11}, T_{12}, T_{21}, T_{22};$  respectively
6      spawn P-MATRIX-MULTIPLY-RECURSIVE( $C_{11}, A_{11}, B_{11}$ )
7      spawn P-MATRIX-MULTIPLY-RECURSIVE( $C_{12}, A_{11}, B_{12}$ )
8      spawn P-MATRIX-MULTIPLY-RECURSIVE( $C_{21}, A_{21}, B_{11}$ )
9      spawn P-MATRIX-MULTIPLY-RECURSIVE( $C_{22}, A_{21}, B_{12}$ )
10     spawn P-MATRIX-MULTIPLY-RECURSIVE( $T_{11}, A_{12}, B_{21}$ )
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12     spawn P-MATRIX-MULTIPLY-RECURSIVE( $T_{21}, A_{22}, B_{21}$ )
13     P-MATRIX-MULTIPLY-RECURSIVE( $T_{22}, A_{22}, B_{22}$ )
14     sync
15     parallel for  $i = 1$  to  $n$ 
16         parallel for  $j = 1$  to  $n$ 
17              $c_{ij} = c_{ij} + t_{ij}$ 
```

The same as before.

P-MATRIX-MULTIPLY-RECURSIVE has work $T_1(n) = \Theta(n^3)$ and span $T_\infty(n) =$

$$T_\infty(n) = T_\infty(n/2) + \Theta(\log n)$$



The Simple Divide&Conquer Approach in Parallel

P-MATRIX-MULTIPLY-RECURSIVE(C, A, B)

```
1   $n = A.rows$ 
2  if  $n == 1$ 
3       $c_{11} = a_{11}b_{11}$ 
4  else let  $T$  be a new  $n \times n$  matrix
5      partition  $A, B, C$ , and  $T$  into  $n/2 \times n/2$  submatrices
           $A_{11}, A_{12}, A_{21}, A_{22}; B_{11}, B_{12}, B_{21}, B_{22}; C_{11}, C_{12}, C_{21}, C_{22};$ 
          and  $T_{11}, T_{12}, T_{21}, T_{22};$  respectively
6      spawn P-MATRIX-MULTIPLY-RECURSIVE( $C_{11}, A_{11}, B_{11}$ )
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The same as before.

P-MATRIX-MULTIPLY-RECURSIVE has work $T_1(n) = \Theta(n^3)$ and span $T_\infty(n) = \Theta(\log^2 n)$.

$$T_\infty(n) = T_\infty(n/2) + \Theta(\log n)$$



Strassen's Algorithm in Parallel

Strassen's Algorithm (parallelised)

1. Partition each of the matrices into four $n/2 \times n/2$ submatrices



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This step takes $\Theta(1)$ work and span by index calculations.

2. Create 10 matrices S_1, S_2, \dots, S_{10} . Each is $n/2 \times n/2$ and is the sum or difference of two matrices created in the previous step.



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Can create all 10 matrices with $\Theta(n^2)$ work and $\Theta(\log n)$ span using doubly nested **parallel for** loops.



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$$T_1(n) = \Theta(n^{\log 7})$$



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$$T_1(n) = \Theta(n^{\log 7})$$
$$T_\infty(n) = \Theta(\log^2 n)$$



Matrix Multiplication and Matrix Inversion

Speedups for Matrix Inversion by an equivalence with Matrix Multiplication.



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Theorem 28.1 (Multiplication is no harder than Inversion)

If we can invert an $n \times n$ matrix in time $I(n)$, where $I(n) = \Omega(n^2)$ and $I(n)$ satisfies the regularity condition $I(3n) = O(I(n))$, then we can multiply two $n \times n$ matrices in time $O(I(n))$.



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- Define a $3n \times 3n$ matrix D by:

$$D = \begin{pmatrix} I_n & A & 0 \\ 0 & I_n & B \\ 0 & 0 & I_n \end{pmatrix}$$



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If we can invert an $n \times n$ matrix in time $I(n)$, where $I(n) = \Omega(n^2)$ and $I(n)$ satisfies the regularity condition $I(3n) = O(I(n))$, then we can multiply two $n \times n$ matrices in time $O(I(n))$.

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- Matrix D can be constructed in $\Theta(n^2) = O(I(n))$ time,
- and we can invert D in $O(I(3n)) = O(I(n))$ time.

\Rightarrow We can compute AB in $O(I(n))$ time. □



Theorem 28.1 (Multiplication is no harder than Inversion)

If we can invert an $n \times n$ matrix in time $I(n)$, where $I(n) = \Omega(n^2)$ and $I(n)$ satisfies the regularity condition $I(3n) = O(I(n))$, then we can multiply two $n \times n$ matrices in time $O(I(n))$.

Theorem 28.2 (Inversion is no harder than Multiplication)

Suppose we can multiply two $n \times n$ real matrices in time $M(n)$ and $M(n)$ satisfies the two regularity conditions $M(n+k) = O(M(n))$ for any $0 \leq k \leq n$ and $M(n/2) \leq c \cdot M(n)$ for some constant $c < 1/2$. Then we can compute the inverse of any real nonsingular $n \times n$ matrix in time $O(M(n))$.



The Other Direction

Theorem 28.1 (Multiplication is no harder than Inversion)

If we can invert an $n \times n$ matrix in time $I(n)$, where $I(n) = \Omega(n^2)$ and $I(n)$ satisfies the regularity condition $I(3n) = O(I(n))$, then we can multiply two $n \times n$ matrices in time $O(I(n))$.

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Proof of this direction much harder (CLRS) – relies on properties of **SPD matrices**.



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Theorem 28.1 (Multiplication is no harder than Inversion)

If we can invert an $n \times n$ matrix in time $I(n)$, where $I(n) = \Omega(n^2)$ and $I(n)$ satisfies the regularity condition $I(3n) = O(I(n))$, then we can multiply two $n \times n$ matrices in time $O(I(n))$.

Allows us to use Strassen's Algorithm to invert a matrix!

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