

- (a) Given that the MIPS processor has a branch delay slot, what will the following code do?

```
foo: slti  $t0, 5  
      beq  $t0, $zero, foo  
      addi $t0, $t0, -1
```

- (b) Write a loop that copies a region of memory from the address in \$a0 to the address in \$a1 for the number of words specified in \$a2. You may assume that the regions of memory are none overlapping.
- (c) How might you improve the performance of your code in (b) by copying more than one word on each iteration of the loop?