

After the Spring School...



Presented by:
Andrew W. Moore
(University of Cambridge)



Cambridge UK
March 15-19, 2010

<http://NetFPGA.org>

Visit <http://NetFPGA.org>

The screenshot shows the NetFPGA website in a Windows Internet Explorer browser window. The address bar displays <http://netfpga.org/home.php>. The page features the NetFPGA logo at the top left and a navigation menu with links for Home, Events, Community, Documents, Sponsors, and About. Below the navigation menu are three main buttons: "Get Informed", "Join the community", and "Get Started". A "Latest News" box on the right highlights the "8/4 - NetFPGA Summer Camp". The main content area contains a paragraph describing the NetFPGA project: "The NetFPGA enables researchers and students to build working prototypes of high-speed, hardware-accelerated networking systems. The NetFPGA has been used in the classroom to teach students how to build Gigabit Ethernet (GigE) switches and Internet Protocol (IP) routers that use hardware rather than software to forward packets." To the right of this text is a diagram of a NetFPGA-based network architecture. The diagram shows a central "NetFPGA" block connected to a "User-defined switch/router/FPGA" block, which is in turn connected to a "Classical Ethernet network" and a "Classical IP network". The diagram also shows a "Classical IP router" and a "Classical Ethernet switch" connected to the network. A link below the text says "Click here to view larger diagram". The browser status bar at the bottom shows "Done, but with errors on page." and "Internet | Protected Mode: On".

Join the NetFPGA.org Community

- Log into the Wiki
- Access the Beta code
- Join the netfpga-beta mailing list
- Join the discussion forum



Contribute to the Project

- Search for related work
- List your project on the Wiki
- Link your project homepage



Submit to a Developers Workshop

Publish, Demonstrate and Promote your work

- [1st Asia NetFPGA Developers Workshop](#)
June 13-15, 2010 – KAIST, Daejeon, Korea
- [2nd North American NetFPGA Developers Workshop](#)
August 12-13, 2010 – Stanford, USA
- [1st European NetFPGA Developers Workshop](#)
September 9-10, 2010 – Cambridge, UK

Survey

- **How did you like this this tutorial?**
 - What did you find useful?
 - What should be improved?
 - What should be removed?
 - What should be added?
- **Can we post the video from this event?**
 - If not, please let us know.
- **Complete On-line survey**
 - http://netfpga.org/tutorial_survey.html

Cambridge Spring School Thanks

Presenters

- David Miller, Martin Žádník, Nadi Sarrar

Local Organization

- David Miller, Charalampos Rotsos
- Piete Brooks, Carol Nightingale

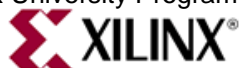
Verilog Experts

- Peter Ogden, Malcolm Scott, Phil Watts,
- Omar Choudary, Greg Chadwick, Jon Woodruff



NetFPGA special thanks to:

Patrick Lysaght, Veena Kumar, Paul Hartke, Anna Acevedo
Xilinx University Program (XUP)



Other NetFPGA Tutorial Presented At:



See: <http://NetFPGA.org/tutorials/>

Acknowledgments

- Support for the NetFPGA project has been provided by the following companies and institutions



***Disclaimer:** Any opinions, findings, conclusions, or recommendations expressed in these materials do not necessarily reflect the views of the National Science Foundation or of any other sponsors supporting this project.*