## 1997 Paper 9 Question 4

## VLSI

Write short notes on the advantages and disadvantages of *each* of the following approaches to clocking a VLSI chip:

- (a) single global clock
- (b) two-phase clock
- (c) synchronised clock using an on-chip phase-locked loop
- (d) self-timed logic (no clock)

[5 marks each]