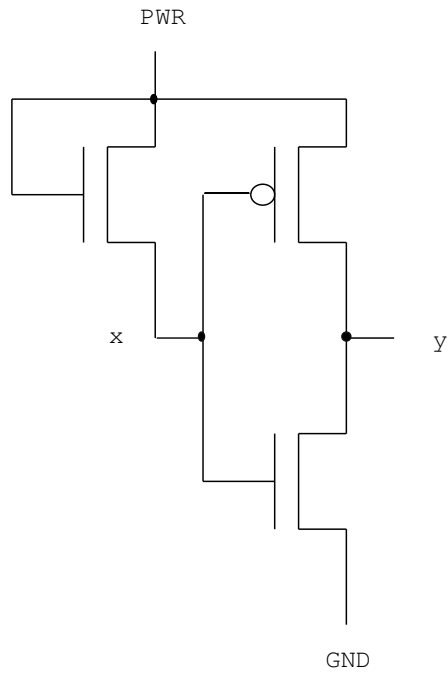


1996 Paper 8 Question 13

Specification and Verification II

Discuss the difficulties in finding a model of transistors that is suitable for formal verification. [10 marks]

Consider the circuit:



What can be deduced about the values at x and y

(a) using the simple switch model? [5 marks]

(b) using the difference switching model? [5 marks]