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Our dream

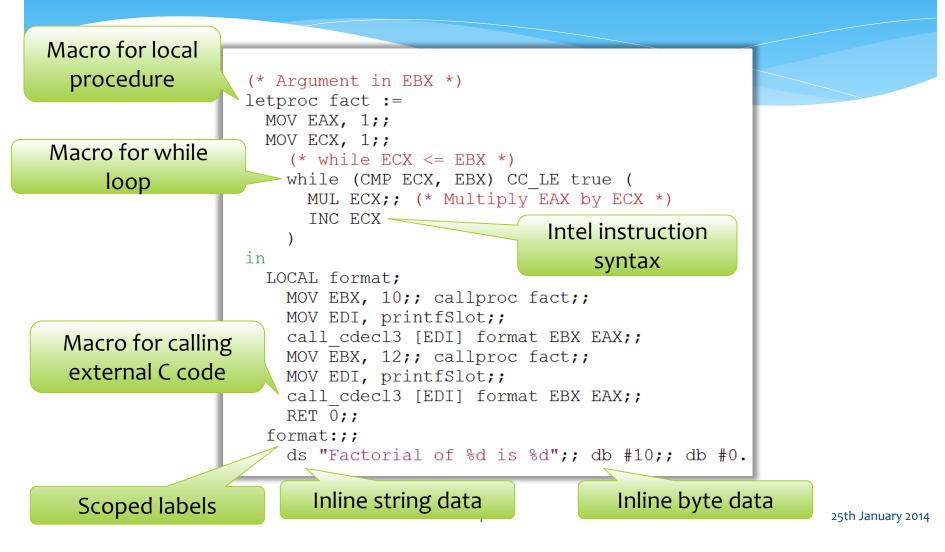
Highest assurance software correctness for machine code programs through machine-assisted proof

"Prove what you run"

One tool: Coq

- Model (sequential, 32-bit, subset of) x86 in Coq: bits, bytes, memory, instruction decoding, execution
- * Generate x86 programs from Coq: assembly syntax in Coq, with macros, run assembler in Coq to produce machine code, even EXEs and DLLs
- * Specify x86 programs in Coq: separation logic for low-level code
- Prove x86 programs in Coq: tactics and manual proof for showing that programs meet their specifications

x86 assembly code



X86 assembly code, in Coq

De	efinition main (printfSlot: DWORD) := (* Argument in EBX *)	
Actually, "just" a	<pre>letproc fact := MOV EAX, 1;;</pre>	Macros are "just"
definition in Coq	MOV ECX, 1;;	parameterized Coq
	(* while ECX <= EBX *) while (CMP ECX, EBX) CC_LE true (definitions
	MUL ECX;; (* Multiply EAX by ECX INC ECX	
)	Cefinition while (ptest: program) (cond: Condition) (value: bool)
Assembler syntax is	LOCAL format;	<pre>(pbody: program) : program := LOCAL BODY; LOCAL test;</pre>
"just" user-defined	MOV EBX, 10;; callproc fact;; MOV EDI, printfSlot;;	JMP test;; BODY:;; pbody;;
Coq notation	call_cdecl3 [EDI] format EBX EAX;	<pre>test:;; ptest;;</pre>
	MOV EBX, 12;; callproc fact;; MOV EDI, printfSlot;;	JCC cond value BODY.
	call_cdecl3 [EDI] format EBX EAX;;	;
	RET 0;; format:;;	
Scoped labels "just"	ds "Factorial of %d is %d";; db #1	10;; db #0.
use Coq binding		

In previous work...

POPL 2013

High-Level Separation Logic for Low-Level Code

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Model of x86 machine: binary reps, memory, instruction decoding, instruction execution

Assembly-code representation; assembler; proof of correctness

Low-level program logic for assembly; proof of soundness wrt machine model

Program specifications; program logic tactics; proofs of correctness for assembly programs

> Higher-level languages; compilers; compiler correctness

PPDP 2013

Coq: The world's best macro assembler?

Simple macros (if, while); User macros; DSLs (e.g. regexps)

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Today's talk

Extend generation, specification and verification of x86 machine code to

- * Generate binary link formats: EXEs and DLLs for Windows (i.e. **practice**)
- * Specify and verify behaviour of EXEs and DLLs
- * (Future work) Specify and verify loading and dynamic linking of EXEs and DLLs

But first, a quick overview of our x86 machine model.

Model x86

* Use Coq to construct a "reference implementation" of sequential x86 instruction decoding and execution

```
CALL src =>
let! oldIP = getRegFromProcState EIP;
let! newIP = evalSrc src;
do! setRegInProcState EIP newIP;
evalPush oldTP
```

Example fragment: semantics of call and return.

```
RET offset =>
let! oldSP = getRegFromProcState ESP;
let! IP' = getDWORDFromProcState oldSP;
do! setRegInProcState ESP (addB (oldSP+#4) (zeroExtend 16 offset));
setRegInProcState EIP IP'
```

Design an assembly language

 Define datatype of programs, with sequencing, labels, and scoping of labels

```
Inductive program :=
   prog_instr (c: Instr)
| prog_skip | prog_seq (p1 p2: program)
| prog_declabel (body: DWORD -> program)
| prog_label (l: DWORD)
```

* Use Coq variables for object-level 'variables' (labels),
 à la higher-order abstract syntax

Notation "'LOCAL' 1 ';' p" := (prog declabel (fun 1 => p))

Build an assembler (1)

* First implement instruction encoder:

| PUSH (SrcI c) =>
if signTruncate 24 (n:=7) c is Some b
then do! writeNext #x"6A"; writeNext b
else do! writeNext #x"68"; writeNext c

| PUSH (SrcR r) =>
writeNext (PUSHPREF ## injReg r)
| PUSH (SrcM src) =>
do! writeNext #x"FF";
writeNext (#6, RegMemM true src)

Build an assembler (2)

 Using instruction encoder, implement multi-pass assembler that determines a consistent assignment for scoped labels

assemble

- : DWORD -> program -> option (seq BYTE)
- Prove "round-trip" lemma stating that instruction decoding is inverse wrt instruction encoding
- Extend this to a full round-trip theorem for the assembler

Design a logic

 It's usual to use a program logic such as Hoare logic to specify and reason about programs

 {P} C {Q}
 Postcondition

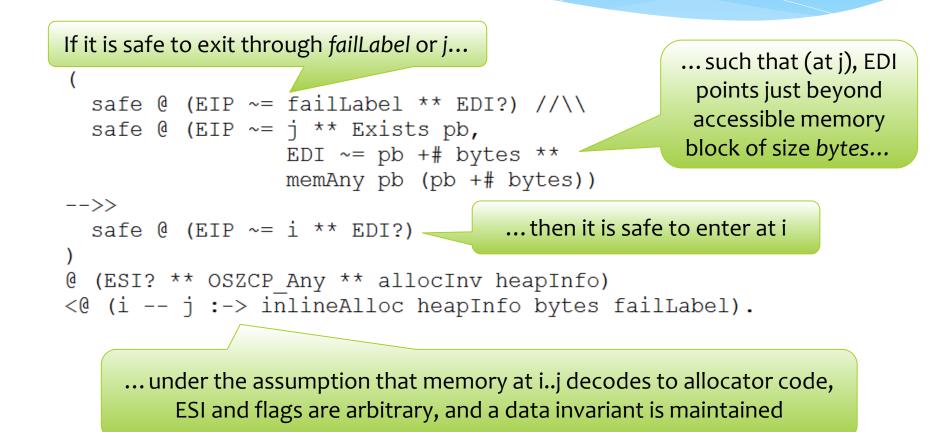
 Precondition
 Command

 Command
 Command

 Recent invention of separation logic makes reasoning about

- Recent invention of separation logic makes reasoning about pointers tractable
- * But still not appropriate for machine code
 - * Machine code programs don't "finish" (what postcondition?)
 - * Code and data are all mixed up ("command" is just bytes in memory), also code can be "higher-order" with code pointers
- We have devised a new separation logic that solves all these problems, embedded it in Coq, and proved it sound with respect to the machine model

Example: Specifying memory allocation



Trivial implementation of allocator

Prove some theorems

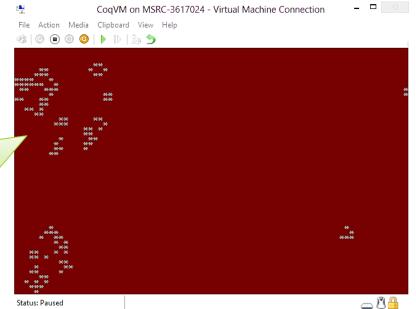
- We have developed Coq tactics to help prove that programs behave as specified
- Sometimes routine, sometimes careful reasoning required.
 Example proof fragment:

```
(* add EDI, EDX *)
eapply basic_seq; first eapply basic_basic;
first apply ADD_RR_ruleAux; sbazooka.
(* shl ECX, 1 *)
eapply basic_seq; first eapply basic_basic;
first apply SHL_RI_rule; sbazooka.
(* add EDI, ECX *)
eapply basic_basic;
first apply ADD RR ruleAux; rewrite /regAny; sbazooka.
```

Put it all together

- 1. Use Coq to produce raw bytes, link with a small boot loader, to produce a bootable image
- 2. Under assumptions about state of machine following boot loading, prove that program meets spec
- 3. Run!

Game of life, written in assembler using Coq, running on bare metal!



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Executables

* That's all well and good but

- We'd like to formalize the process of loading programs, and support dynamic linking, and
- Rather than booting the machine (or a VM) it would be nice to experiment on an existing OS e.g. Windows
- Also good to test our ideas on linking and loading using existing formats
- * So: model EXE's, DLL's, loading and dynamic linking

What's in an executable?

Some machine code, with an entry point, preferred base address, and...

- * Several **sections** (code, data, r/o data, thread local data, etc.)
- * **Relocation** information (if not loaded at preferred base address)

18

- * Imports, by name or number
- * **Exports** (if executable is a DLL)
- * A lot of metadata
- * Legacy cruft (e.g. MSDOS stub!)
- Informally documented in a ~100 page spec

Microsoft Portable Executable and Common Object File Format Specification

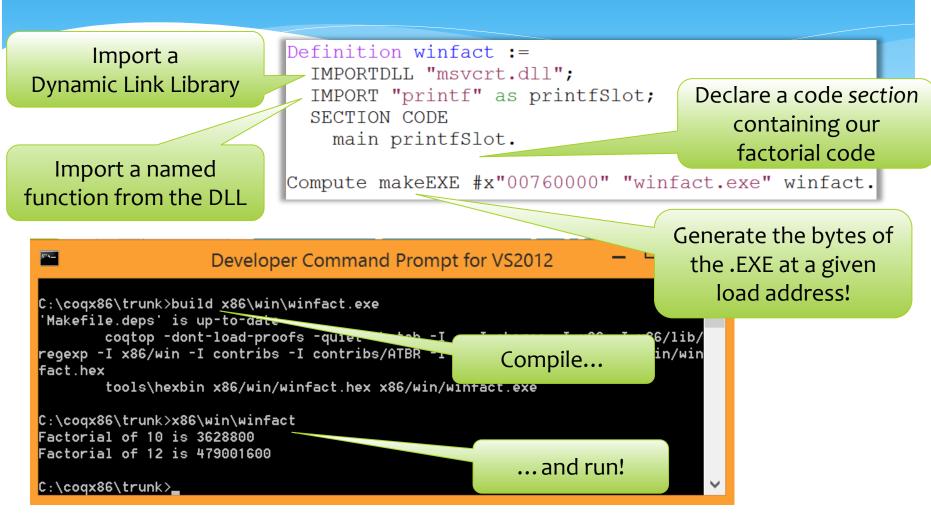
Revision	8.3 –	February	6,	2013

Windows

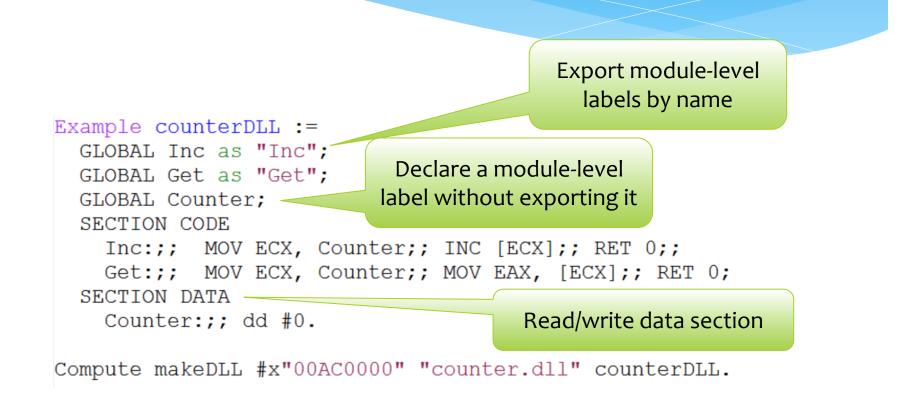
What's in an executable? Let's look inside

<pre>static void main() { printf("hello, world.\n"); }</pre>		5E00	size of code size of initialized data
87654321 0011 2233 4455 6677 8899 abb ccdd eeff 0123456789abcdef 00000001: 445a 9000 0300 0000 0400 0000 fff 0000 00000010: b800 000	dumpbin /all	1244 1000 8000 400000 200 6.00 0.00 6.00 0.00 6.00 0 F000 400 0 3	<pre>size of uninitialized data entry point (00401244) base of code base of data image base (00400000 to 0040EFFF) section alignment file alignment operating system version image version subsystem version Win32 version size of image size of headers checksum subsystem (Windows CUI) DLL characteristics</pre>
00000060: 7420 6265 2072 756e 2069 6220 444f 5320 t t be run in DOS 000000001: 6d6f 6465 2e0d 0d0a 2400 0000 0000 mode\$ 00000080: f238 02ab b659 6cf8 b659 6cf8 8YlYlYl. 00000080: f238 df59 6cf8 f26 b559 6cf8 8YlYlYl. 00000000: r29c a3f8 af59 6cf8 729c a1f8 bc59 6cf8 rYl.rYl. 00000000: 729c a2f8 f59 6cf8 f269 f6f8 rYl.Rich.Yl. 00000000: 919f a1f8 b759 6cf8 f56 fcf8 rYl.Rich.Yl. 000000000: 0000 0000 0000 0000 0000 0000 e 000000000: 0000 0000 0000 0000 0000 0000 e 000000000:		100000	Diff Characteristics Dynamic base NX compatible Terminal Server Aware size of stack reserve size of stack commit

Example .EXE, in Coq



Example DLL counter.dll



Example client usecounter.exe

```
Example useCounterCode :=
                                                 Import Get from
  IMPORTDLL "msvcrt.dll";
                                                   counter.dll
  IMPORT "printf" as printfSlot;
  IMPORTDLL "counter.dll";
  IMPORT "Inc" as incSlot; IMPORT "Get" as getSlot;
  SECTION CODE
    LOCAL formatString;
   MOV EDI, incSlot;; CALL [EDI];; CALL [EDI];;
   MOV EDI, getSlot;; CALL [EDI];;
    PUSH EAX;;
                                               Call indirect through
    MOV EBX, formatString;; PUSH EBX;;
    MOV EDI, printfSlot;; CALL [EDI];;
                                                   Get's "slot"
   ADD ESP, 8;;
    RET 0;;
  formatString:;;
    ds "Got %d";; db #10;; db #0.
```

Compute makeEXE #x"12E30000" "usecounter.exe" useCounterCode.

The messy details

- Our assembly datatype and assembler give us all the mechanisms we need to generate the structures found in EXE's and DLL's
 - * Byte, word, string representations
 - * RVAs (Relative Virtual Address)
 - * Padding
 - * Alignment constraints
 - Bitfields
 - * Multi-pass fixed-point iteration to deal with forward references
- * One small annoyance: file image not identical to in-memory image (e.g. alignment of sections); RVAs wrt in-memory image
 - Hack: add "skip" primitive in our writer monad to advance the assembler's "cursor" without producing any bytes

Exports and imports

Exports

Logically: a list of (name,address) pairs

Imports

Logically: for each imported DLL,

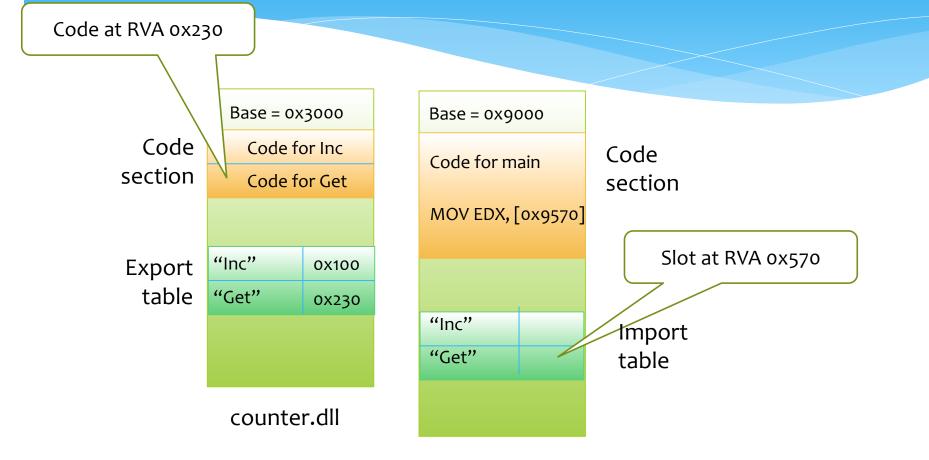
- Its name
- * A list of imported symbols (by name or ordinal)
- * A list of slots, one for each imported symbol: the Import Address Table or IAT

In binary format, this is all somewhat messier!

Relocateable code

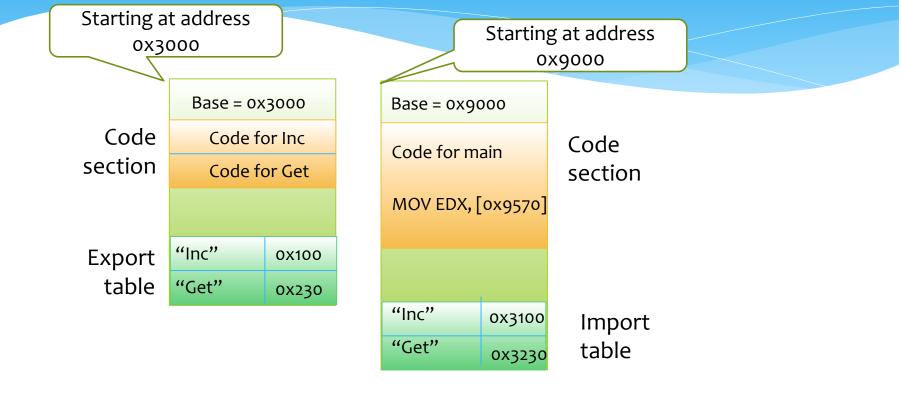
- Some x86 code is position independent e.g. makes use of PC-relative offsets (jumps)
- * But much is not: especially on 32-bit, it's hard to refer to global *data* in position independent way
- * So: executables have a "preferred base address"
- If not loaded at this address, absolute addresses embedded in code and data must be *rebased* i.e. patched at load-time
- * The executable lists these in a special ".reloc" section

What does the OS loader do? Before: in-file



usecounter.exe

What does the OS loader do? After loading: in-memory



counter.dll

usecounter.exe

Patching of instructions

- We want to relocate addresses ("rebasing") and perhaps link modules (in some non-Windows loader) by in-place update of instructions
- * Encodings matter. Prove lemmas such as

```
Lemma PUSH_decoding (p addr: DWORD) q :
    p -- q :-> PUSH addr -|-
    p -- q :-> (#x"68", addr) \\//
    (Exists b:BYTE, signTruncate _ (n:=7) addr = Some b
    /\\ p -- q :-> (#x"6A", b)).
```

(Towards) Specifying calling conventions

 "fastcall" calling convention for function of one argument (passed in ECX) and one result (in EAX)

What's to do?

- Separately specify different modules; prove correctness of combination, already loaded and with imports resolved
- * Model the loading process itself
- Implement a small loader, in machine code using Coq, with export/import resolution
- * Prove its correctness